COE 301 / ICS 233 – Computer Organization

Term 172 – Spring 2018

Single-Cycle Processor Evaluation Form

Student Name / ID	Grade	Remarks	Bonus/Penalty

Section	Grade	Max	Remarks
Designing the Register File (RF)		15	
Developing the arithmetic and logic unit (ALU) with its test cases to verify the unit's functionality		30	
Designing the control unit with all required signals to operate the other units		30	
Implanting branch unit, instruction and data memory.		15	
Completeness of the Single-Cycle Datapath and Control. Demonstrating the execution of a complete program. Reporting corrective actions and modifications.		10	