COE 301 / ICS 233 – Computer Organization

Term 172 – Spring 2018

Pipelined Processor Evaluation Form

Student Name / ID	Grade	Remarks	Bonus/Penalty

Section	Grade	Max	Remarks
Implementing the basic pipelines of datapath and control by inserting needed registers and testing it		24	
Implementing forwarding unit to handle data hazards and testing it		24	
Implementing stall unit for handling lw and control hazards and testing it		23	
Report document properly describes the final implementation of the processor pipeline and control, hazard detection, forwarding, and stalling. Sufficient drawings and description is provided for all components, pipeline stages, and control		29	