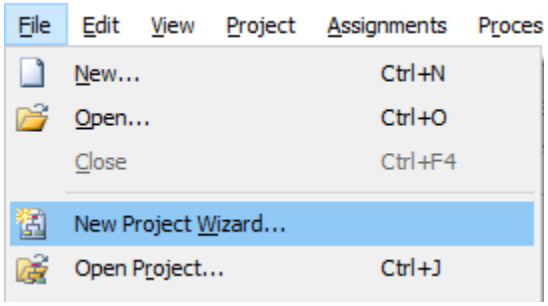
Creating a new project

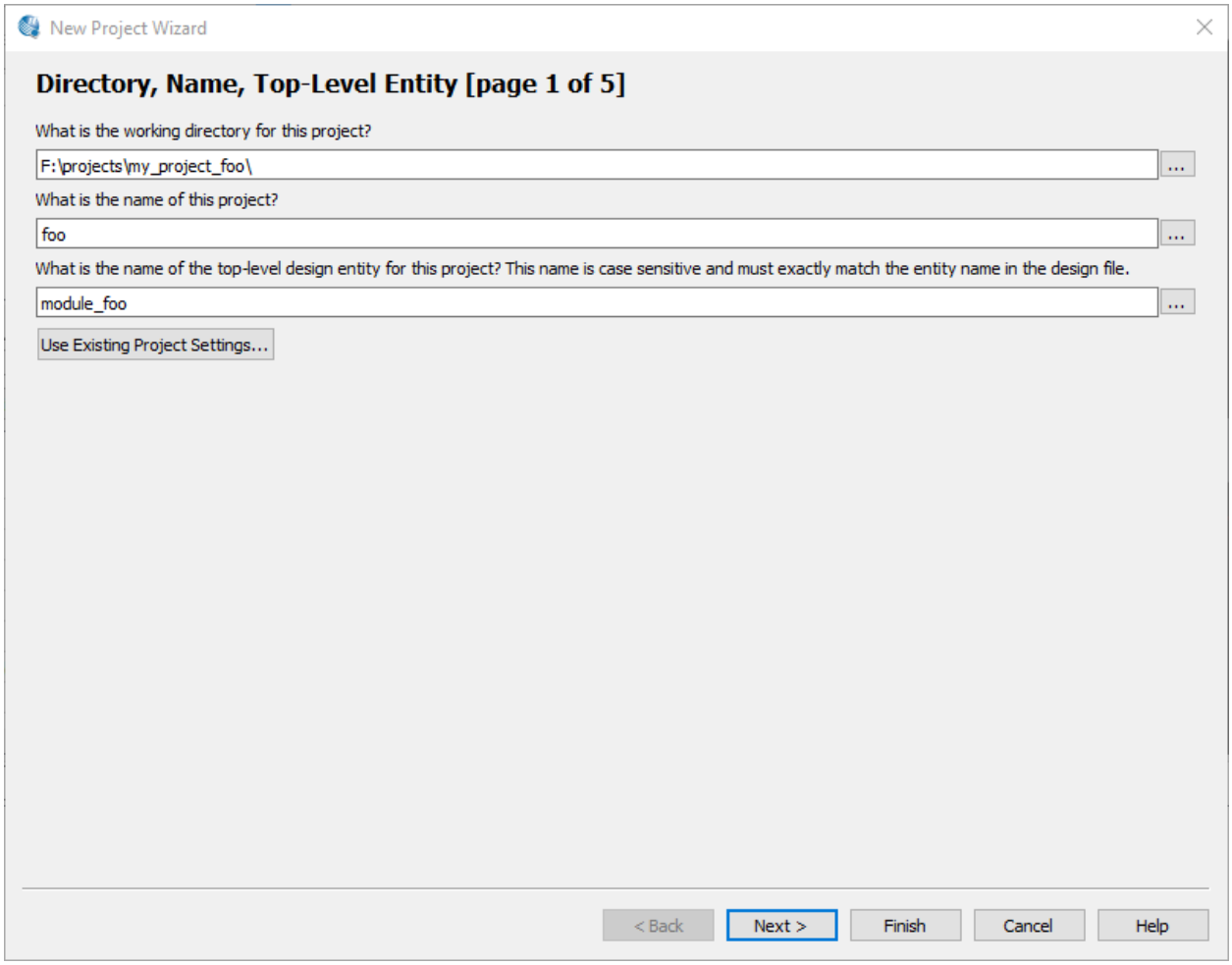
Click the new project wizard button on the welcome screen

OR

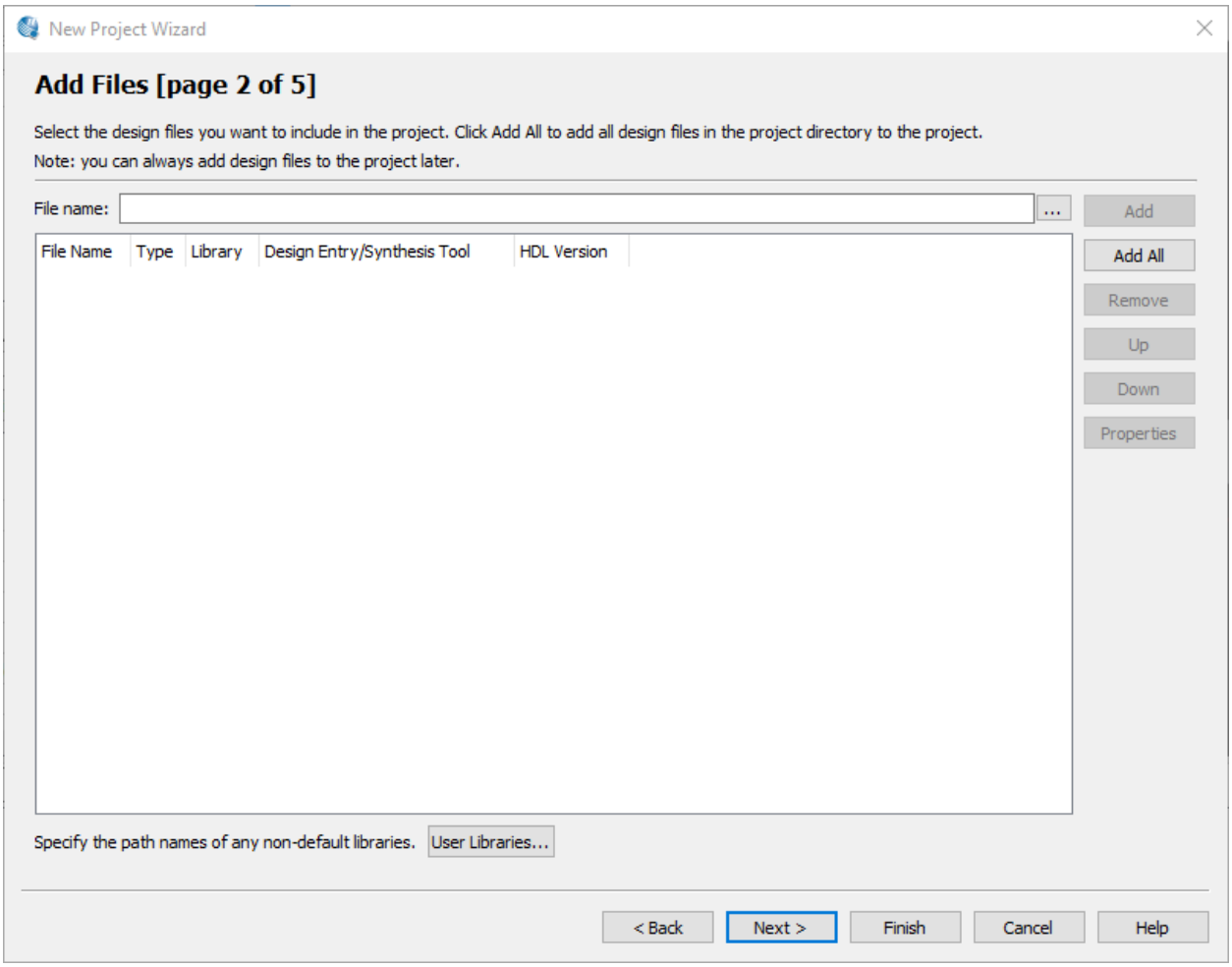
Go to File > New Project Wizard



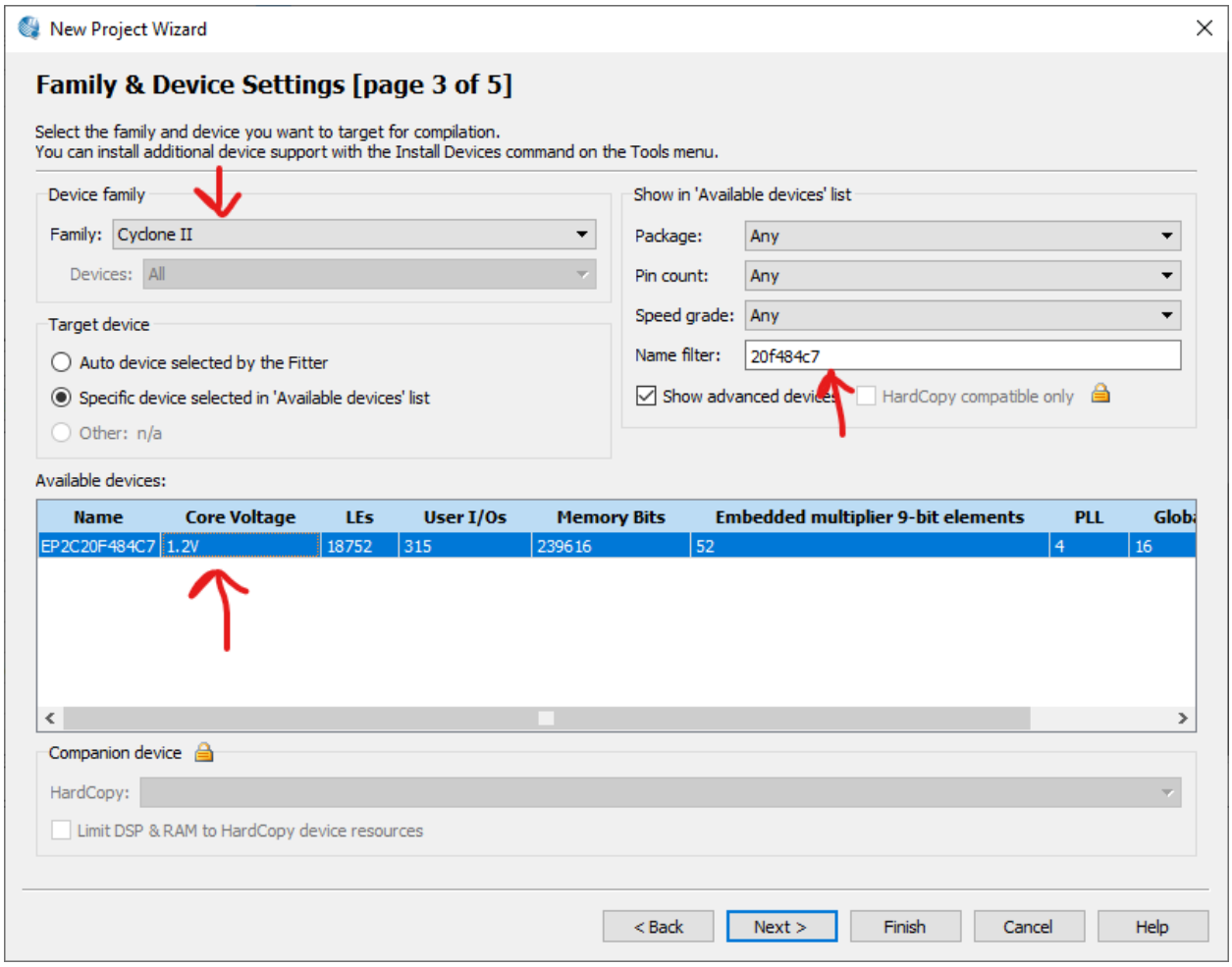
Click Next if an intro page opens



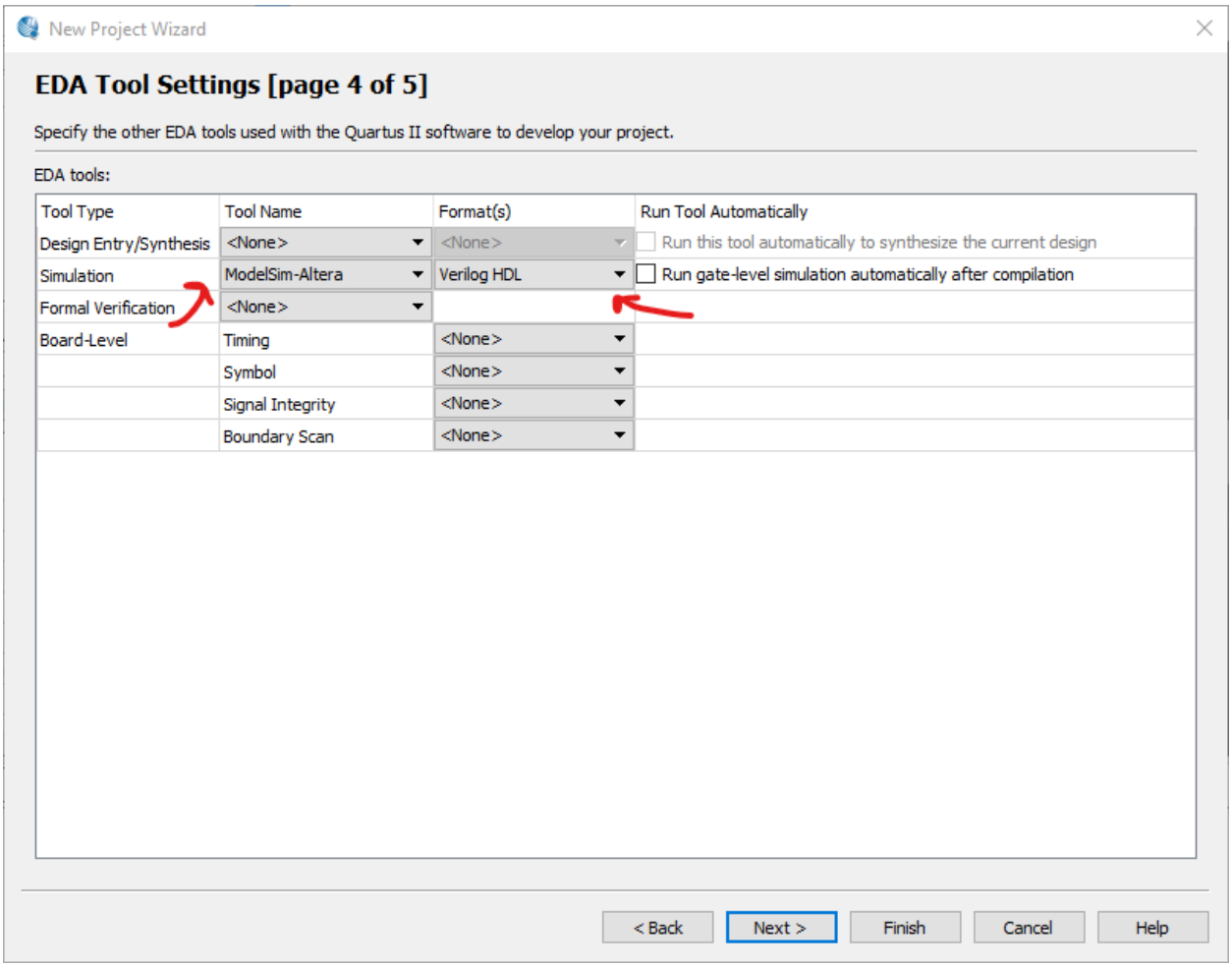
Enter path of your project.
Enter the name.
Enter the module name. (NOTE: This ***must*** match with the Verilog module you will write)
Click Next



Use this page to add any existing Verilog files to your project. If there are none, click Next



Set the family to “Cyclone II”
In the name filter type “20f484C7”, which is the code for the FPGA on our board
Select the device in the list
Click Next



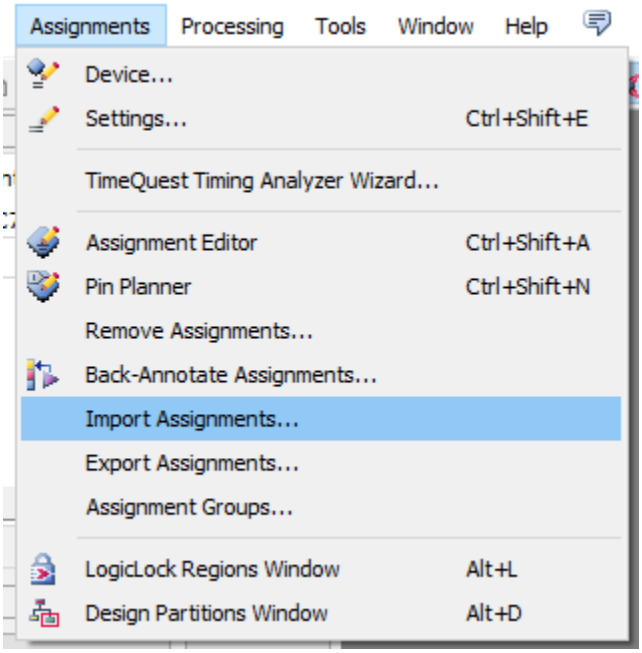
Set the simulation tool to “ModelSim-Altera” and language to “Verilog-HDL”

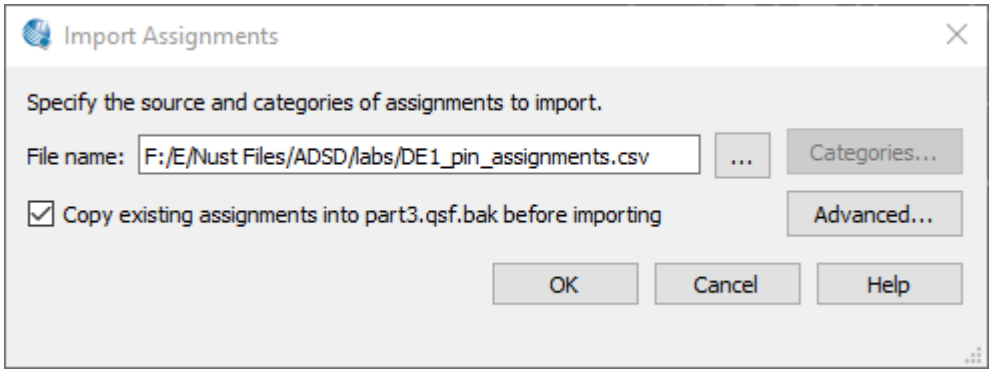
Click Next

After that, click Finish on the Summary page

Now you need to add pin assignment file so that Quartus knows which FPGA pins are mapped to the peripherals on the board. For more information about this, view the user manual document.

To add pin assignments, go to Assignments > Import Assignments





Give the path to the assignment file and click Ok.

Now you are ready to start coding.

To add a new Verilog file, go to File > New > Verilog HDL file

A blank file will open, write your code in this file and save it in the project directory.