

	UART (Uni	versal	asynchro	anone	recione	- transn	isti on
	Consist of - Start / 81 - Data bits	op bits					
	- Parity bits (optional) · High voltage ("mark") = 1, 1ow voltage ("space") = 0						
	idle		data bit			idle	
	Start						
	SPI (Serial	periphs		erjace)			
					2-222122		
		MOSI		MISO			
		Master		CS Per	ipheral +0 Slave		
C	SCK idle ock from master		7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7			idle on next byte	
	MOSI						
	MISO						
	CS			0	, 0		



