

**Software Requirements Data**

**FOR**

**Sensor Data Acquisition and Relay System (SDARS)**

Table of Contents

[CHANGE HISTORY 5](#_Toc53398707)

[SRD COMPLIANCE MATRIX WITH RESPECT TO DO-178B 6](#_Toc53398708)

[1 INTRODUCTION 7](#_Toc53398709)

[1.1 Purpose 7](#_Toc53398710)

[1.2 SCOPE 7](#_Toc53398711)

[1.3 REFERENCES 7](#_Toc53398712)

[1.3.1 EXTERNAL DOCUMENTS 7](#_Toc53398713)

[1.3.2 INTERNAL DOCUMENTS 7](#_Toc53398714)

[1.4 Acronyms 8](#_Toc53398715)

[2 Software Overview 9](#_Toc53398716)

[2.1 User Characteristics 9](#_Toc53398717)

[2.2 Functional allocations 9](#_Toc53398718)

[2.3 System Architecture 11](#_Toc53398719)

[2.4 Software Architecture 12](#_Toc53398720)

[3 Functional Requirements 13](#_Toc53398721)

[3.1 PROCESSOR REQUIREMENTS 13](#_Toc53398722)

[3.1.1 SDARS\_Flash\_Req\_001 13](#_Toc53398723)

[3.1.2 SDARS\_Flash\_Req\_002 13](#_Toc53398724)

[3.1.3 SDARS\_Flash\_Req\_001 13](#_Toc53398725)

[3.2 POST REQUIREMENTS 13](#_Toc53398726)

[3.2.1 SDARS\_Init\_Req\_001 13](#_Toc53398727)

[3.2.2 SDARS\_POST\_Req\_001 13](#_Toc53398728)

[3.2.3 SDARS\_POST\_Req\_002 13](#_Toc53398729)

[3.3 CBIT REQUIREMENTS 14](#_Toc53398730)

[3.3.1 SDARS\_CBIT\_Req\_001 14](#_Toc53398731)

[3.3.2 SDARS\_CBIT\_Req\_002 14](#_Toc53398732)

[3.4 FUNCTIONALITY REQUIREMENTS 14](#_Toc53398733)

[3.4.1 SDARS\_Aquire\_Req\_001 14](#_Toc53398734)

[3.4.2 SDARS\_Aquire\_Req\_002 14](#_Toc53398735)

[3.4.3 SDARS\_Kalman\_Req\_001 14](#_Toc53398736)

[3.4.4 SDARS\_Kalman\_Req\_002 14](#_Toc53398737)

[3.4.5 SDARS\_RS422\_Req\_001 14](#_Toc53398738)

[3.4.6 SDARS\_RS422\_Req\_002 14](#_Toc53398739)

[3.4.7 SDARS\_UART\_Req\_001 15](#_Toc53398740)

[4 PERFORMANCE CRITERIA 15](#_Toc53398741)

[5 TIMING REQUIREMENTS 15](#_Toc53398742)

[5.1 POST REQUIREMENTS 15](#_Toc53398743)

[5.1.1 SDARS\_time\_Req\_001 15](#_Toc53398744)

[5.2 CBIT REQUIREMENTS 15](#_Toc53398745)

[5.2.1 SDARS\_time\_Req\_002 15](#_Toc53398746)

[5.3 RS422 REQUIREMENTS 15](#_Toc53398747)

[5.3.1 SDARS\_time\_Req\_003 15](#_Toc53398748)

[6 MEMORY SIZE CONSTRAINTS 15](#_Toc53398749)

[7 INTERFACES 16](#_Toc53398750)

[7.1 RS422 REQUIREMENTS 16](#_Toc53398751)

[7.1.1 SDARS\_RS422\_Req\_003 16](#_Toc53398752)

[7.1.2 SDARS\_RS422\_Req\_004 16](#_Toc53398753)

[7.1.3 SDARS\_RS422\_Req\_005 16](#_Toc53398754)

[7.1.4 SDARS\_RS422\_Req\_006 16](#_Toc53398755)

[7.1.5 SDARS\_RS422\_Req\_007 16](#_Toc53398756)

[8 FAILURE DETECTION AND SAFETY MONITORING REQUIREMENTS 16](#_Toc53398757)

[8.1 Safety REQUIREMENTS 16](#_Toc53398758)

[8.1.1 SDARS\_Fail\_Req\_001 16](#_Toc53398759)

[8.1.2 SDARS\_Fail\_Req\_002 17](#_Toc53398760)

[8.1.3 SDARS\_Fail\_Req\_003 17](#_Toc53398761)

[9 PARTITIONING REQUIREMENTS 17](#_Toc53398762)

[10 COTS REQUIREMENTS 17](#_Toc53398763)

CHANGE HISTORY

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Version | Date | Change Description | Author | Reviewers | Remarks |
| 1.0a | 12/10/20 | Initial Release | Jyothesh Rao |  |  |
|  |  |  |  |  |  |

# SRD COMPLIANCE MATRIX WITH RESPECT TO DO-178B

Table 1: SRD Compliance Matrix with respect to DO-178B

|  |  |  |
| --- | --- | --- |
| **DO-178B section** | **Section Heading** | **Sections in SRD** |
| 11.9 a | Description of the allocation of system requirements | Section 2 |
| 11.9 b | Functional and operational requirements | Section 3 |
| 11.9 c | Performance criteria | Section 4 |
| 11.9 d | Timing requirements and constraints | Section 5 |
| 11.9 e | Memory size constraints | Section 6 |
| 11.9 f | Hardware and software interfaces | Section 7 |
| 11.9 g | Failure detection and safety monitoring requirements | Section 8 |
| 11.9 h | Partitioning requirements | Section 9 |

# INTRODUCTION

This document specifies the Software Requirements of Sensor Data Acquisition and Relay System (SDARS), designed and developed by Oak Systems, Bangalore for Aircraft as per RTCA DO-178B, Level-B process.

## Purpose

This Software Requirements Data (SRD) describes the Software CSCI Description to be met by SDARS. The Software Requirements laid down here shall be used as a baseline in the Software Design and Development to achieve System Requirements in the form of Software product.

## SCOPE

This document provides a definition of the high-level functional, performance and derived software requirements of the SDARS software (SDARS\_SW\_01).

## REFERENCES

### EXTERNAL DOCUMENTS

1. Software Considerations in Airborne Systems and Equipment certification: RTCA/DO-178B, Date: 01.12.1992
2. TECHNICAL SPECIFICATIONS of SDARS.

### INTERNAL DOCUMENTS

1. TECHNICAL SPECIFICATIONS of SDARS for Aircraft, Oak\_Ts\_SensorDataAcq\_v1.0, Issue No: 001
2. Software Development Plan of SDARS, Oak\_SDP\_SensorDataAcq\_v1.0, Issue No: 001
3. Software Requirement Standards of SDARS, Oak\_SRS\_SensorDataAcq\_v1.0, Issue No: 001

## Acronyms

|  |  |
| --- | --- |
| **Acronym** | **Abbreviation** |
| CBIT | Continuous Built In Test |
| CSCI | Computer Software Configuration Item |
| ICD | Interface Control Document |
| NA | Not Applicable |
| PBIT | Power-on Built in Test |
| POST | Power On Self-Test |
| SDP | Software Development Plan |
| SDARS | Sensor Data Acquisition and Relay System |
| SRD | Software Requirements Data |
| SRS | Software Requirement Standards |
| TS | Technical Specifications |

# 

# Software Overview

SDARS Software provides sensor information to external Systems. It is Embedded System software that Acquires data from any bus and Sends it to other systems after applying filtering. SDARS communicates through RS-422 protocol. High level block diagram is show in Figure 1.

Velocity North East, roll, pitch and heading

External System

SDARS

RS- 422

+12 VDC Magnetic Heading

Figure 1: "System overview diagram"

## User Characteristics

The Magnetic heading is a navigational data. The system sends data to Display unit which processes data and displays on display panel.

## Functional allocations

1. SDARS shall receive navigation data from sensors vi pitch, yaw, roll in digital form and send magnetic heading data to external interface
2. There shall be a built-in test for runtime health monitoring.
3. Power on self-test (POST ) shall indicate the status of internal voltages, internal memory
4. Continuous built in test (CBIT) shall start after POST indicates the status of external input and internal voltages. CBIT shall run continuously at intervals of 20millisecond.
5. There shall be a parameter estimation algorithm that takes the sensor data for estimation of magnetic heading.
6. The system shall provide heading information over RS422 interface in UART protocol at 9600 baud rate.

## System Architecture

Magnetic Heading Data from RS422 channel 1 and channel 2

SDARS CPU

8051

Power Source

RS 422

Channel 1: TX / Rx

RS422

Channel 2: TX /RX

Discrete O/P STAB

SDARS S/w and Kalman Filter

Magnetometer Sensor

Figure 2: Overall Architecture of SDARS

SDARS software will acquire the Magnetometer sensor data, filter the data by Kalman filter software and provides magnetic heading data. Ittransmits the Filtered magnetic heading data via RS422 interface to other LRUs.

## Software Architecture

SDARS software shall start with initialization and POST. After completion of POST it enters normal mode where s/w shall acquire the magnetic sensor data, transmits it to Kalman filter software and filtered data shall be transmitted to external interfaces via RS422. The Kalman filter algorithm takes the input of roll, pitch & yaw and its rate of change to filter and generate the magnetic heading. This heading data is then transmitted to external interfaces via two RS422 channels.

Filtered Heading data

Transmits Roll, Pitch & Yaw and its rate

KALMAN Algorithm Software

Initialize

POST

CBIT/ Normal Mode

Acquire Sensor data

Receive Filtered Data

UART protocol for RS422 channel 1 and RS422 Channel 2

Figure 3: Software Architecture of SDARS

# Functional Requirements

## PROCESSOR REQUIREMENTS

### SDARS\_Flash\_Req\_001

SDARS\_SW\_01 shall be flashed on 8051 controller having flash memory of 16MB.

### SDARS\_Flash\_Req\_002

SDARS\_SW\_01 shall have code size less than 50% of flash memory size.

### SDARS\_Flash\_Req\_001

SDARS\_SW\_01 shall have RAM memory of 256bites and the execution consumption should not exceed 50%

## POST REQUIREMENTS

### SDARS\_Init\_Req\_001

SDARS\_SW\_01 shall initialize the following:

1. RS422
2. Magnetic sensor.

### SDARS\_POST\_Req\_001

SDARS\_SW\_01 shall carry out power on self-test after initialization is completed

POST includes:

1. Processor check
2. Power supply checks
3. RS422 check
4. Magnetic sensor check.

### SDARS\_POST\_Req\_002

Behavior of the system shall not be normal if POST failure occurs

## CBIT REQUIREMENTS

### SDARS\_CBIT\_Req\_001

SDARS\_SW\_01shall do CBIT checks at periodicity of 1 minutein Normal mode after POST check is successful. The CBIT checks are as follows:

1. RS422 check
2. Magnetic sensor check.

### SDARS\_CBIT\_Req\_002

Behavior of the system shall not be normal if CBIT failure occurs

## FUNCTIONALITY REQUIREMENTS

### SDARS\_Aquire\_Req\_001

SDARS\_SW\_01shall acquire the magnetic sensor data containing Roll, Pitch and Yaw along with its rates at sampling rate of 1000samples/second.

### SDARS\_Aquire\_Req\_002

SDARS\_SW\_01shall transmit the acquired data to Kalman software and receives the filtered heading data via RS232 port at baud rate of 9600.

### SDARS\_Kalman\_Req\_001

Kalman softwareshall receive the sensor data, normalize the data and shall apply the Kalman filter to the data.

### SDARS\_Kalman\_Req\_002

Kalman software shall transmit the filtered magnetic heading data to the SDARS\_SW\_01.

### SDARS\_RS422\_Req\_001

SDARS\_SW\_01 shall transmit the magnetic heading data at a baud rate of 9600.

### SDARS\_RS422\_Req\_002

SDARS\_SW\_01 shall receive the acknowledgement at a baud rate of 9600.

### SDARS\_UART\_Req\_001

SDARS\_SW\_01 shall use UART Interrupt (UART ISR) to detect the completion of data transmission or reception without continuously polling the flag bit, thus saving processor time.

# PERFORMANCE CRITERIA

There is no functionality having any precision or accuracy

# TIMING REQUIREMENTS

## POST REQUIREMENTS

### SDARS\_time\_Req\_001

SDARS\_SW\_01 shall complete Initialization and PBIT within 90 sec

## CBIT REQUIREMENTS

### SDARS\_time\_Req\_002

CBIT in SDARS\_SW\_01 shall be performed at a periodicity of 10milisecs.

## RS422 REQUIREMENTS

### SDARS\_time\_Req\_003

SDARS\_SW\_01 shall transmit and receive the data at baud rate of 9600

# MEMORY SIZE CONSTRAINTS

NA as there are no memory related requirement

# INTERFACES

## RS422 REQUIREMENTS

### SDARS\_RS422\_Req\_003

SDARS\_SW\_01 shall have Message Format is as follows: HEADER - ADDRESS - COMMAND/ERROR CODE - PARAMETERS - TRAILER – CHECKSUM.

### SDARS\_RS422\_Req\_004

SDARS\_SW\_01 shall calculate the checksum byte as follows:

The sum modulo 95 of all message characters beginning with the header byte up to and including the trailer byte. The value 32 is subtracted from each character value before taking the modulo 95 sum. The value 32 is added to the final sum to obtain the checksum value. All values are in decimal.

### SDARS\_RS422\_Req\_005

SDARS\_SW\_01 shall have the Header byte as 7BH and Trailer byte as 7DH.

### SDARS\_RS422\_Req\_006

SDARS\_SW\_01 shall have the address may take on the values from 64 to 95 decimal (40H to 5FH).

### SDARS\_RS422\_Req\_007

SDARS\_SW\_01 shall have the Parameters as all ASCII printable characters in the range of 20H to 7EH

# FAILURE DETECTION AND SAFETY MONITORING REQUIREMENTS

POST and CBIT failures are already captured in Functional requirements

## Safety REQUIREMENTS

### SDARS\_Fail\_Req\_001

SDARS\_SW\_01 shall restart the system if WDT failure occurs

### SDARS\_Fail\_Req\_002

SDARS\_SW\_01 shall not have a normal behavior if POST/CBIT check fails

### SDARS\_Fail\_Req\_003

SDARS\_SW\_01 shall handle the Power supply exceptions in the POST

# PARTITIONING REQUIREMENTS

NA as there is no operating systems and memory.

# COTS REQUIREMENTS

Kalman filter software a Commercial-off-the-shelf software which shall be interfaced with RS232 port. This software receives thesensor data from SDARS\_SW\_01, normalizes the data and shall apply the Kalman filter to the data. Kalman software shall continuously run at periodicity of 10ms and transmits the Filtered heading data to SDARS\_SW\_01 whenever Kalman filter *software* receives request command from SDARS\_SW\_01. The Kalman software shall be designed with Roll, Pitch and Yaw and its rates (Velocity north east) as inputs. These data will be transmitted at a rate of 100Hz and required to generate filtered Heading data at the rate of 100Hz. Kalman algorithm should use Yaw as the heading data and roll and pitch are supported for the filter as estimates. Roll, pitch & yaw rates are also used for the new estimates and error estimates. Roll, Pitch and Yaw data are normalized and data will be in degrees. Rate will be in Degrees/sec. Tolerance for the data will be varied from 1 degree to 2 degree where 1 degree as maximum tolerance when no rate of change and 2 degree as maximum tolerance when rate of change is present. The data will be transmitted via RS232 internally with a baud rate of 9600.