



emu8086 - assembler and microprocessor emulator 4.08

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new open examples save compile emulate calculator console

```
01 MOV AX, 15h
02 MOV BX, 46
03 MOV BX, AX
04 MOV AH, BL
05 MOV CL, 10h
06
07 PUSH BX
08 PUSH 9h
09 PUSH AX
10
11 POP CX
12 POP DX
13
14 XCHG AH, BH
15 XCHG DX, AX
16 MOV CH, AL
17 XCHG BL, DH
18
19 ADD CX, DX
20 ADC DX, 1h
21
22 SUB BH, 2h
23 SBB BH, 15h
24
25 AND AL, BL
26 CMP DH, DL
27 CMP AH, AL
28 OR CH, CL
29 NOT DL
30
31
32 SHR BH, 1
33 ROL BH, 1
34
35
36
37
38
39
40 HLT
```

stack

0100:FFFF	FF00 <
0100:FFFC	0015
0100:FFFA	0009
0100:FFF8	1515
0100:FFF6	0000
0100:FFF4	0000
0100:FFF2	0000
0100:FFF0	0000
0100:FFEE	0000
0100:FFEC	0000
0100:FFEA	0000
0100:FFE8	0000
0100:FFE6	0000
0100:FFE4	0000
0100:FFE2	0000
0100:FFE0	0000
0100:FFDE	0000
0100:FFDC	0000
0100:FFDA	0000
0100:FFD8	0000
0100:FFD6	0000
0100:FFD4	0000

registers

	H	L
AX	00	15
BX	00	2E
CX	00	00
DX	00	00
CS	0100	
IP	0006	
SP	0100	
BP	FFFE	
SI	0000	
DI	0000	
DS	0100	
ES	0100	

0100:0006

01000:	B8	184	j
01001:	15	021	S
01002:	00	000	NUL
01003:	BB	187	n
01004:	2E	046	
01005:	00	000	NUL
01006:	8B	139	i
01007:	D8	216	+
01008:	8A	138	e
01009:	E3	227	h
0100A:	B1	177	h
0100B:	10	016	
0100C:	53	083	S
0100D:	6A	106	jAB
0100E:	09	009	TAB
0100F:	50	080	P
01010:	59	089	v
01011:	5A	090	Z
01012:	86	134	A
01013:	E7	231	r

0100:0006

```
MOV AX, 00015h
MOV BX, 0002Eh
MOV BX, CX
MOV CL, 010h
PUSH BX
PUSH 09h
PUSH AX
POP CX
POP DX
XCHG BH, AH
XCHG AX, DX
MOV CH, AL
XCHG DH, BL
ADD CX, DX
ADC DX, 01h
SUB BH, 02h
SBB BH, 015h
AND AL, BL
CMP DH, DL
CMP AH, AL
OR CH, CL
NOT DL
...
```

flags

CF 0 ZF 0 SF 0 OF 0 PF 0 AF 0 IF 1 DF 0

original source co...

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```

line: 30 col: 35

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0100:FFD6	0000
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BX	00	15
CX	00	00
DX	00	00
CS	0100	
IP	0008	
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BP	0000	
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ES	0100	

0100:0008

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