

Seat No.	
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S.E. (Computer Science & Engineering) (Semester - IV)**Examination, May - 2019****COMPUTER ORGANIZATION****Sub. Code : 63533****Day and Date : Monday, 20 - 05 - 2019****Total Marks : 50****Time : 02.30 p.m. to 04.30 p.m.**

- Instructions :**
- 1) Figures to the right indicate full marks.
 - 2) Question Q.1 & Question Q.4 are compulsory.
 - 3) Attempt any one Question from Q.2, Q.3 and one from Question Q.5, Q.6

- Q1)** a) Explain the Mechanical era computers. [4]
 b) Give the comparison between RISC and CISC. [4]
 c) With a block diagram explain accumulator based CPU. [5]
- Q2)** a) Explain the architecture of Pentium 4 Processor. [6]
 b) Explain the IEEE 754 standard 32 bit floating point format with a one example. [6]
- Q3)** a) With the HDL description explain the Booths algorithm. [6]
 b) Explain non-restoring division algorithm for unsigned integers with one example. [6]
- Q4)** a) Explain One-hot method of designing control unit for GCD processor. [7]
 b) With a neat diagram explain the basic structure of micro programmed control unit. [6]
- Q5)** a) Explain the micro programmed control unit for 2's-compliment multiplier. [6]
 b) Draw and explain the structure of an associative memory. [6]
- Q6)** a) Write a short note on TLB. [6]
 b) Write a short note on memory allocation. [6]

