

Seat No.	
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S.E. (CSE) (Semester - III) Examination, May - 2019

MICROPROCESSORS

Sub. Code : 63528

Day and Date : Wednesday, 22 - 05 - 2019

Total Marks : 50

Time : 09.30 a.m. to 11.30 a.m.

- Instructions :**
- 1) Attempt any one Question from Q.No.1, 2 and 5, 6.
 - 2) Question No. 3, 4 and 7,8 are compulsory.

SECTION - I

- Q1) a)** Write and explain program of Addition between two 16-bit no. [5]
b) Explain Stack Memory addressing Mode of advanced microprocessors. [5]
- Q2) a)** Draw and explain Flag register for entire 80×86 and Pentium microprocessor. [5]
b) Explain descriptor of 80286 microprocessor with proper diagram. [5]
- Q3) a)** Draw and Explain internal architecture of Microprocessors in detail with programming model. [5]
b) Explain all Logical instructions of 8085 Microprocessors. [5]
- Q4) Write Short Note on (any one) :** [5]
a) Classification of Instructions.
b) Access Right Byte.
c) MOV Revisited.

SECTION - II

- Q5) a)** Explain the structure of 80386 Microprocessor: The memory system. [5]
b) Write and explain all Division instructions of microprocessor with proper syntax. [5]

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- Q6)** a) Explain all register set of the Pentium-4 microprocessor. [5]
b) Explain DO-WHILE, REPEAT-UNTIL instructions. [5]
- Q7)** a) Explain WAIT, LOCK Prefix and ENTER and LEAVE instructions in details. [5]
b) Explain Short, Near and Far Jump with proper example. [5]
- Q8)** Write Short Note on (any one) : [5]
a) BOUND instruction
b) Hyper Threading Technology
c) Interrupt Vector Table

