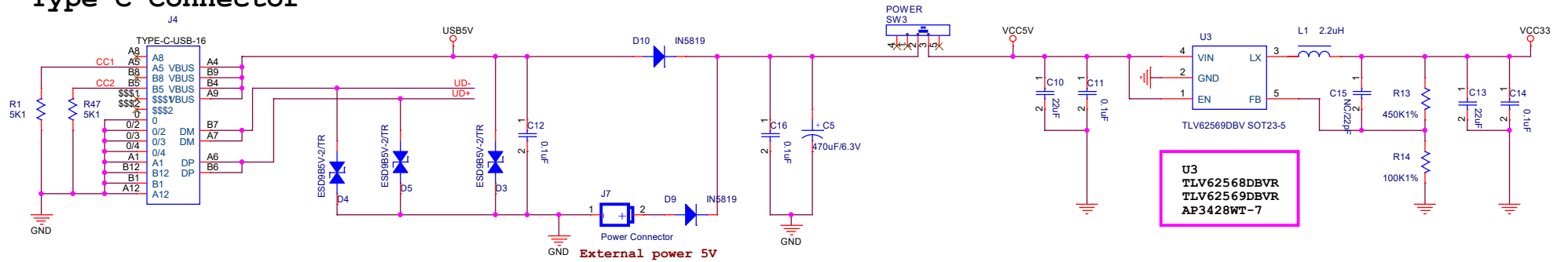
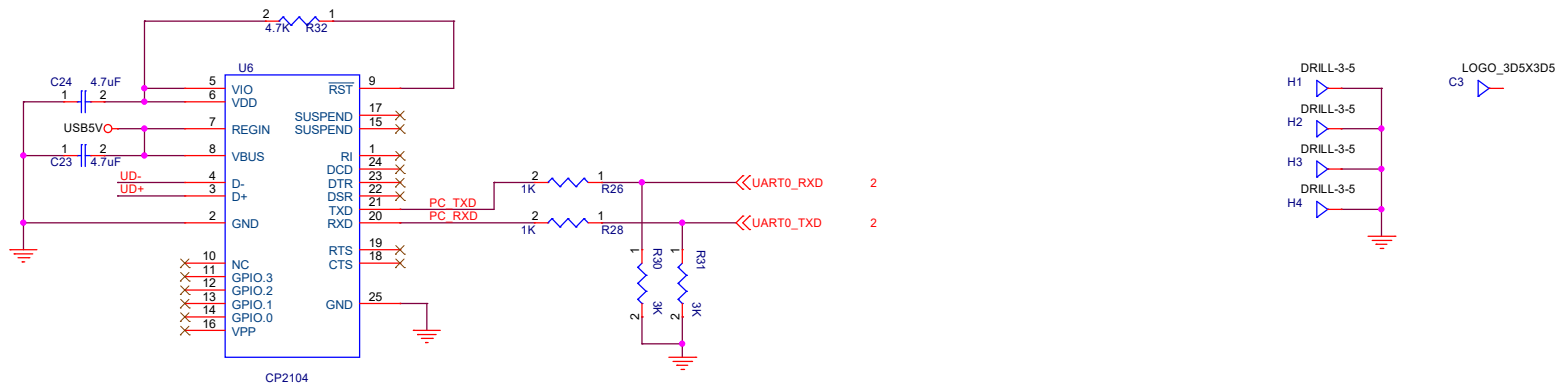


Type-C Connector



Type-C (USB) to Omega2S-Uart



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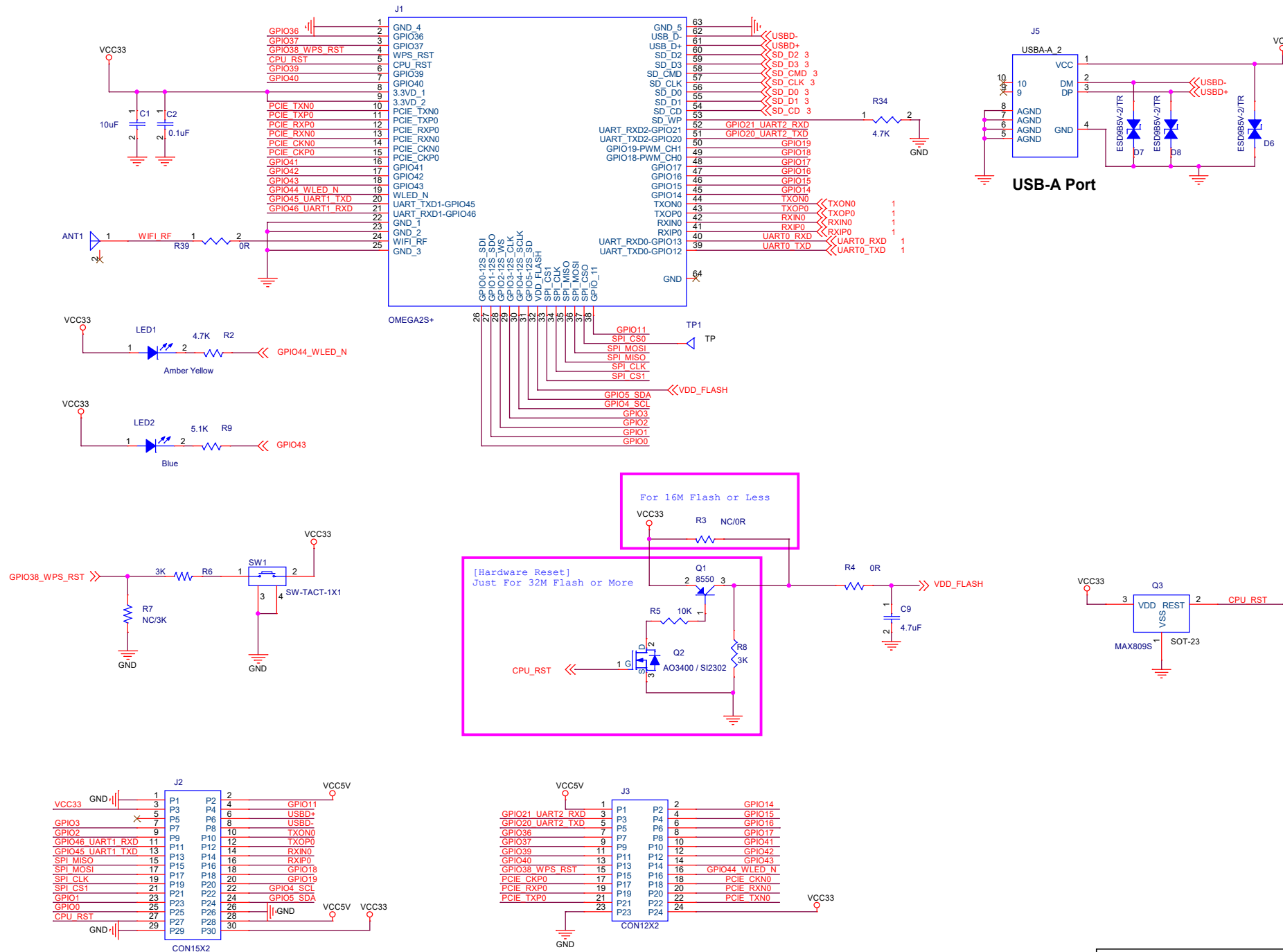
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[illegible]

The schematic diagram illustrates the Ethernet Port circuit. It features four input lines: RXIP0, RXIN0, TXOP0, and TXON0. Each line is connected to a 0.1uF capacitor (C22, C26, C18, C19) which is grounded. The RX lines (RXIP0, RXIN0) are also connected to 49.9R resistors (R38, R37, R36, R35) which are grounded. The TX lines (TXOP0, TXON0) are connected to 49.9R resistors (R38, R37, R36, R35) which are grounded. The RX lines are connected to pins 1, 2, 3, and 4 of the WAN port. The TX lines are connected to pins 5, 6, 7, and 8 of the WAN port. The WAN port is labeled '10/100 WAN port' and 'SMD'. The Ethernet Port is labeled 'Ethernet Port'.

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