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	4th Sen / Comp. Engg.
	Microprocessors And Phripheral Devices
	Practical -
	Apparatus Required: 8085 KIT and power supply.
	Theory: Problem is analyzed and assembly language program for the problems is written using the instructions MOV, LXI, TNX, ADD, SUB, STA and HLT.
	SUB STA and HLT.
	Program for Addition of Two 8-bit Numbers (Result 8 bit):
' - r e	The first number 48 H is in memory location 2401.
7	The second number 56H is in memory location 2402.
1 - 1	The result is to be stored in memory
	location 2403.

Memory	Machine	Mnemonics	Oberands	Comments	
Address	Coole				
2000	21,01,24	LXE	H. 2401 H	Place address of the 1st	
				number in H-L	
1				register baix.	
	-25			0	

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1						piore in c
	2003	76	MOV	A.M	Move the contents of	
j.,					memory addressed by	
<u>_</u>					H-L pain to the	_
_					accumulator.	
_	2004	23	INX	1-1	Jucreased the contents of	,
					H-L pain by 1 i.e.	
					address is 2402 in	
7					the H-L pain.	
-	2005	86	ADD	M	Add tae 2nd number in	
_					the first number, and	
_					the result is in the	
-					accumulator.	-
_	2006	32,03,24	STA	2403H	Store the result in	1
_					mornory lecation 2403.	
_	2009	76	HLT		Stop.	
_						
_	12.3.	2.				
_			,			

Program for Subtraction of Two-8 bit Numbers:

1st number of 48H in memory location 2401 2nd number 33H in memory location 2402 result is to be stored in memory location 2403.

r					And Continues an	
	Memory	Machine	Mnemorica	Abenanda	Comments	
	Address	Coole		Officeros	Chilliand 143	
ľ		COACC				
Ļ						e,
	2000	21,01,24	LXI	H.2401	1000	_
					Load address of direct!	_
					number in the	
Ļ					presiden bain H-L	
				- Aug 12	Paris Hall	_
r		per co)			

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2003	FE	MOV	A,M	More the first number
2			,	in accumulator
2004	23	INX	H	Add I to the contents
2005	0.			of H-L register pair.
3	96	SUB	M	Subtract 2nd number
				from the first number
			-	and the result is
			-	placed in the
2004			-	accumulator.
2006	23	INX	H	Get memory adolress
			-	2403 in the H-L
2007			-	pain.
2007	77	MOV	M,A	Move the contents of
				accumulator to the
				memory location
*				whose address is in
				the H-L register
				pair (memory address 2403)
2008	76	111		addhess 2403)
	76	HLT		Stop.
	Result -			
		48	Н	
		-33		
		15	,, _	
	Procedure			
	11			

1) Connect the power supply to the 8085 pit and switch on the power supply.

	Page Ho. Date
2	Enter the program and the input data.
3)	Enter the program and the input data. Execute the program. Check the result.
4)	Check the result.
	A
P	