

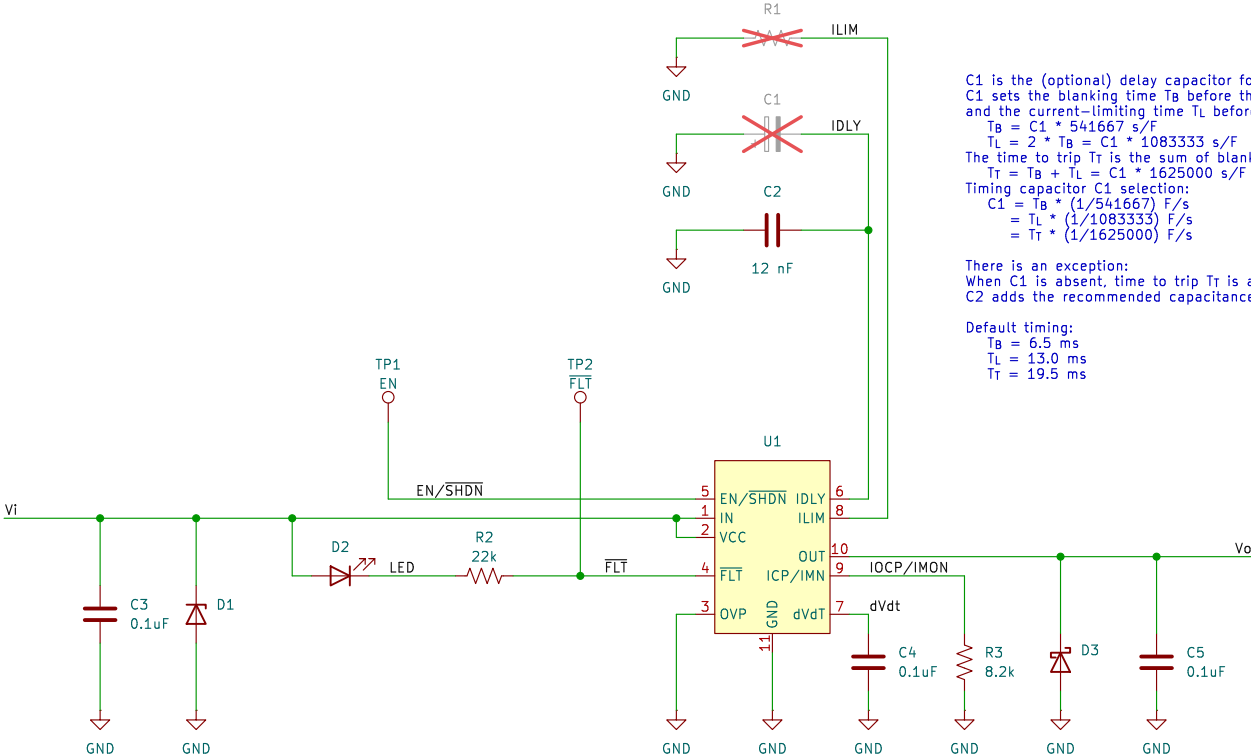
R1 is the current-programming resistor.
Current-limit constant (pin 8) is 9840 Volts
Current limit:
 $I_{LM} = 9840 \text{ V} / R1$
Resistor selection:
 $R1 = 9840 \text{ V} / I_{LM}$

C1 is the (optional) delay capacitor for loads with high inrush current.
C1 sets the blanking time T_B before the eFuse starts to limit current,
and the current-limiting time T_L before the eFuse trips.
 $T_B = C1 * 541667 \text{ s/F}$
 $T_L = 2 * T_B = C1 * 1083333 \text{ s/F}$
The time to trip T_T is the sum of blanking time T_B and current limiting time T_L .
 $T_T = T_B + T_L = C1 * 1625000 \text{ s/F}$
Timing capacitor C1 selection:
 $C1 = T_B * (1/541667) \text{ F/s}$
 $= T_L * (1/1083333) \text{ F/s}$
 $= T_T * (1/1625000) \text{ F/s}$

There is an exception:
When C1 is absent, time to trip T_T is a slow 155 ms.
C2 adds the recommended capacitance (12nF) to avoid this exception.

Default timing:
 $T_B = 6.5 \text{ ms}$
 $T_L = 13.0 \text{ ms}$
 $T_T = 19.5 \text{ ms}$

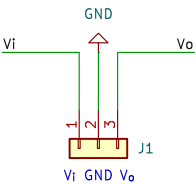
R3 defines the Over-Current Protection (OCP) current limit I_{ocp} .
This is a separate mechanism that works at higher currents and faster than I_{LM} .
OCP constant is 16470 Volts.
 $I_{ocp} = 16470 \text{ V} / R3$
 $R3 = 16470 \text{ V} / I_{ocp}$
In this implementation,
 $I_{ocp} = 16470 \text{ V} / R3 = 2.01 \text{ A}$



FID1 Fiducial
FID2 Fiducial
FID3 Fiducial

Front silkscreen text

Rear silkscreen text



TXT1 Vi
TXT2 GND
TXT3 Vo
TXT4 Vi
TXT5 GND
TXT6 Vo

Oops Paradox

Sheet: /
File: efuse-module.kicad_sch

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