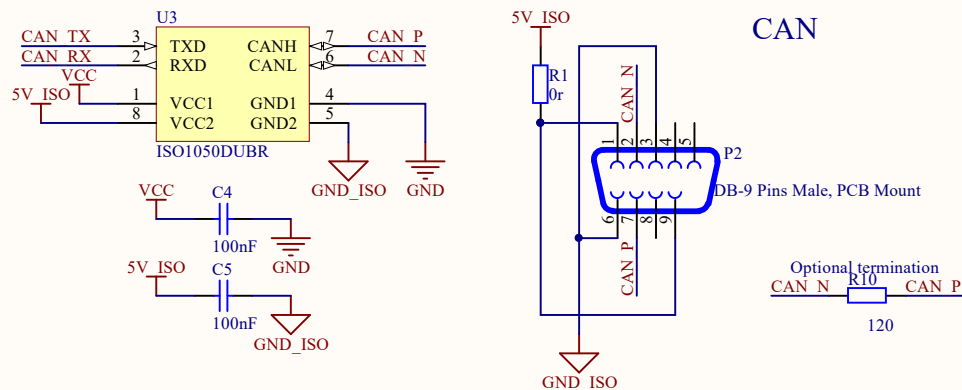


1

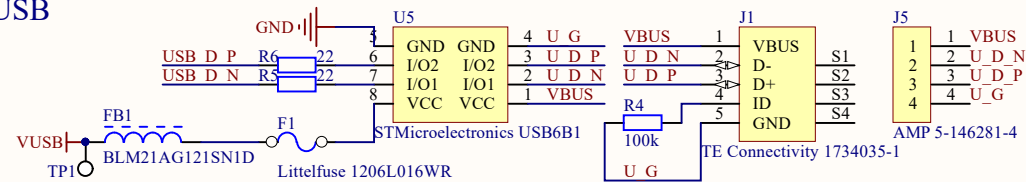
2

3

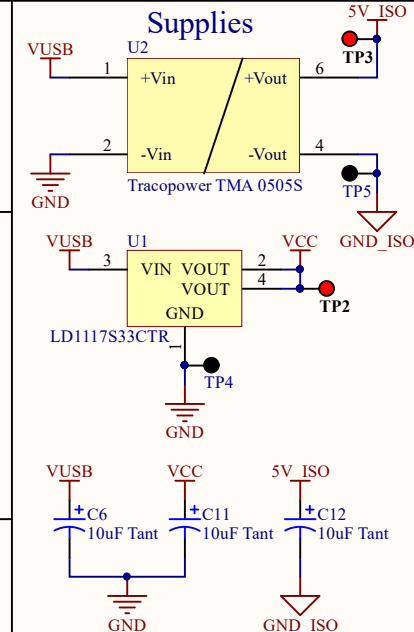
4



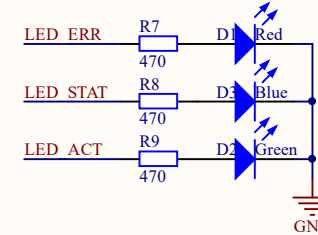
## USB



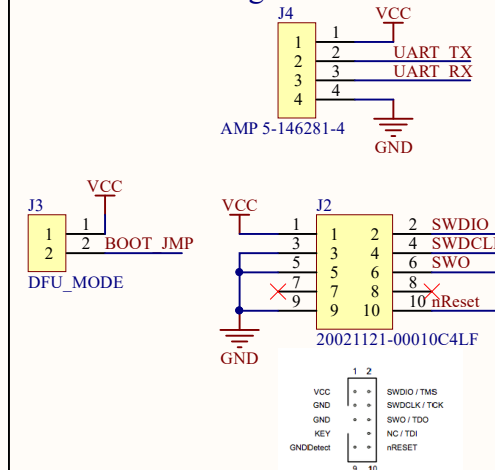
## Supplies



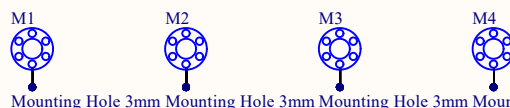
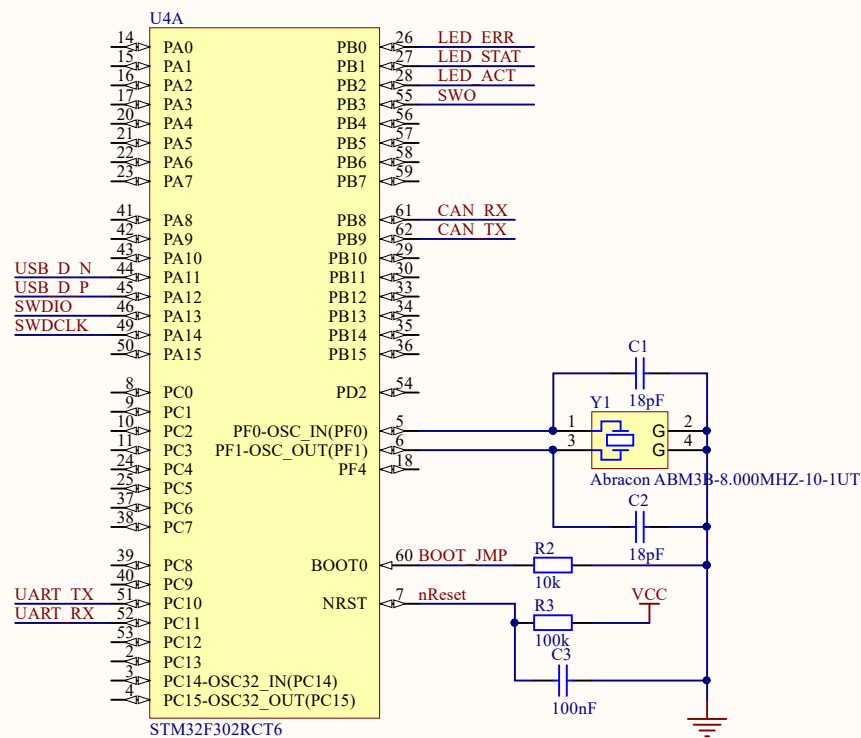
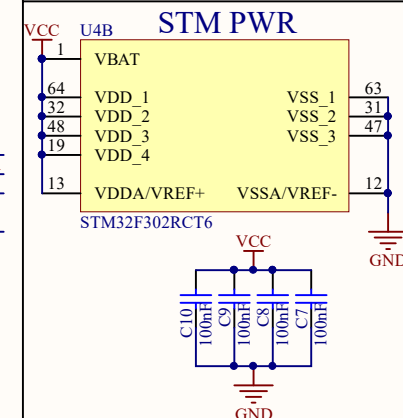
## LEDs



## Debug



## STM PWR

Title **Open-Canalyzer**Size: **A4**

Number:

Revision: **R01**

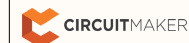
Date: 4-6-2016

Time: 15:07:31

Sheet 1 of 1

File: Top.SchDoc

xor-gate & dev-ty  
 Jerry Jacobs  
 Maarten Tamboer  
<http://xor-gate.org>  
<http://dev-ty.nl>



1

2

3

4

