VCC

GND

J201

1{DCC}

PWR_FLAG VCC PWR_FLAG

J62

GND

1{DCC}

{COIL}

B

R11
22R

A

R12
22R

R13
10k

R14
10k

Q11
AO3400A

Q12
AO3400A

VCC

D11

SS34

D12

J11
1
2
3

GND

{COIL}

R21
22R

R22
22R

R23
10k

R24
10k

Q21
AO3400A

Q22
AO3400A

VCC

D21

SS34

D22

J21

GND

Sheet: /outputB/
File: output.kicad_sch
Title:
Size: A4   Date:
KiCad E.D.A. 8.0.8

Rev:
Id: 5/8

LED_BLUE   LED

R1 R6
10k 6k2

D2   D4
LED   LED

GND   GND

+5V

C3
100nF

GND

+5V

Y1
Crystal_GND2

X1   X2

C1   C2

12pF   12pF

GND

+5V

J3   R2
ICSP   10k

6   RESET
5   MOSI
4   MISO
3   SCK
2
1

GND

U101
ATmega328-A

VCC AVCC

AREF 20

ADC6 19
ADC7 22

PB0 12
PB1 13
PB2 14
PB3 15   MOSI
PB4 16   MISO   LED_BLUE
PB5 17   SCK   LED
XTAL1/PB6 7   X1
XTAL2/PB7 8   X2

PC0 23   dipSw1
PC1 24   dipSw2
PC2 25   dipSw3
PC3 26   dipSw4
PC4 27   dipSw5
PC5 28
RESET/PC6 29   RESET

PD0 30   2.B
PD1 31   2.A
PD2 32
PD3 1   1.B
PD4 2   1.A
PD5 9
PD6 10
PD7 11

GND

SW1
SW_SPST
1   2

GND

8
9   tie to GPIO1 due to PWM
10
11 MOSI
12 MISO
13 SCK

A0
A1
A2
A3
A4
A5

0
1
2   DCC_T
3
4   pin 4 can become 2nd status LED
5
6
7

1{COIL}
1.A
1.B

2{COIL}
2.A
2.B

dipSw[1..5]

dipSw1
dipSw2
dipSw3
dipSw4
dipSw5

{DCC}

R5
1k5

D1

U801
HCPL-0631

+5V

+5V

R801
10k

DCC_TTL

A1
1

C1
2

C2
3

A2
4

VCC
8

VO1
7

VO2
6

GND
5

GND

U21
L78L05_SOT89

VCC

+5V

3 IN   OUT 1

GND
2

C301
330nF

C302
100nF

C303
100uF

GND

dipSw[1..5] ▷

dipSw1 5	6
dipSw2 4	7
dipSw3 3	8
dipSw4 2	9
dipSw5 1	10

SW_DIP_x05
SW901

GND