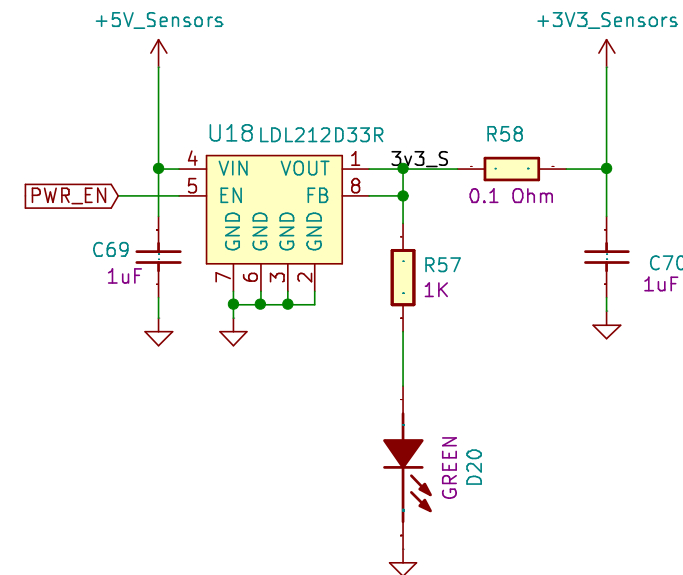
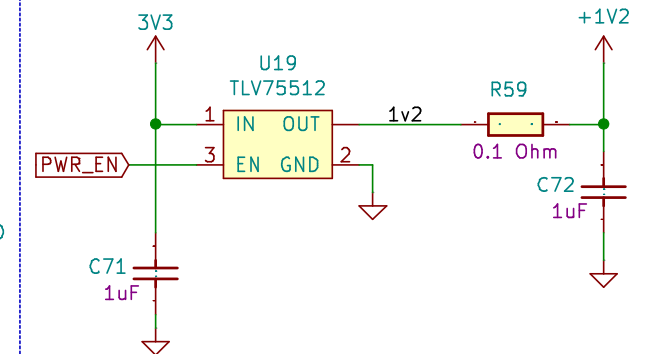


10 SCLK/IOL12A

+5V to 3V3 LDO for  
ADC and 3V3 sensors

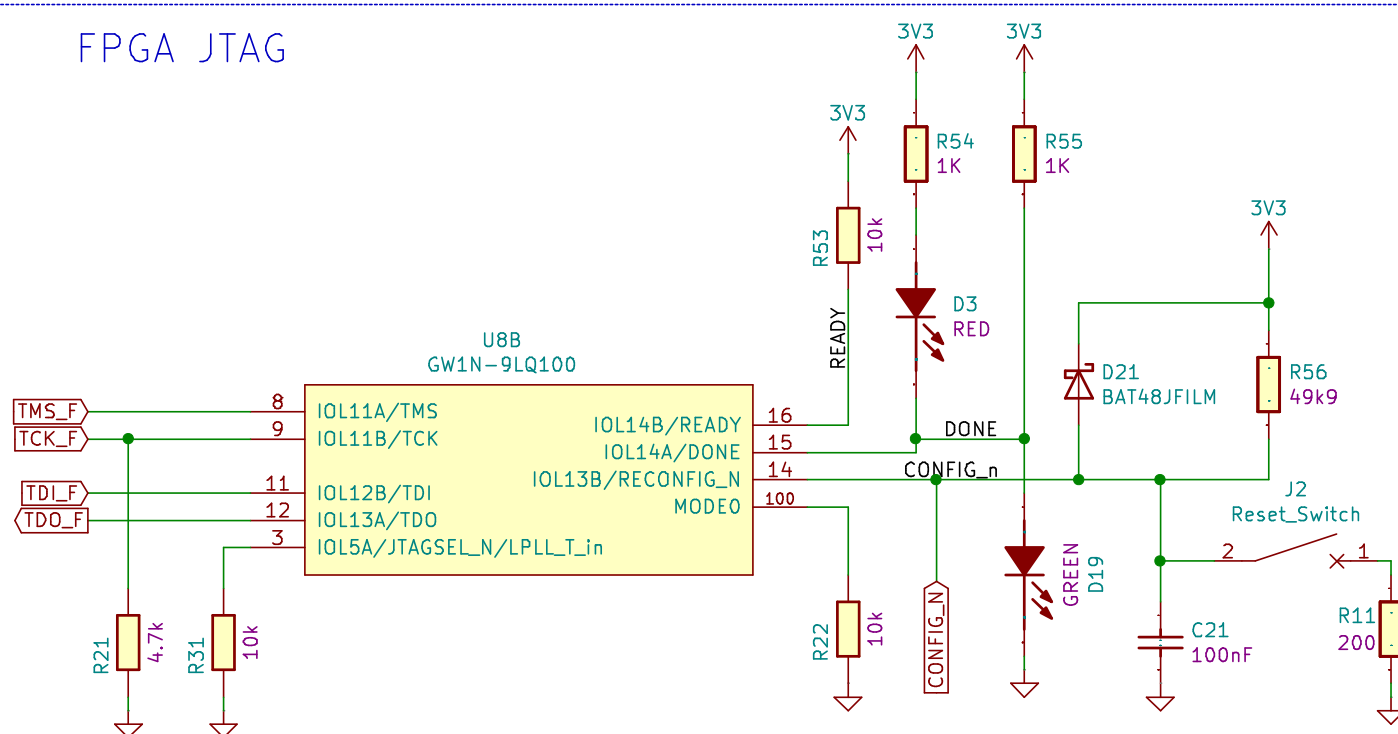


3V3 to 1.2 LDO for VCC

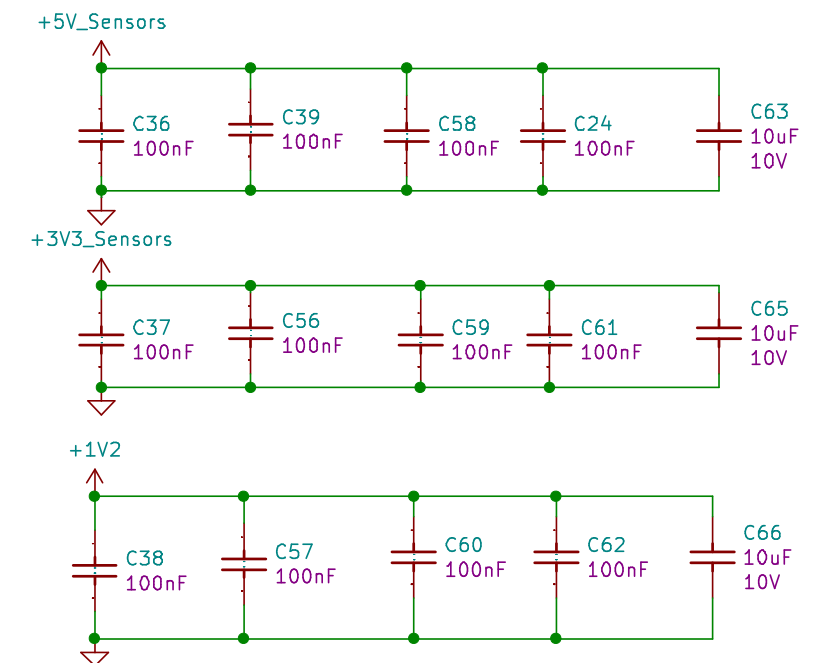


Optional Circuit to use with  
GW1N-LV4 or GW1N-LV9 device.  
With GW1N-UV4 or GW1N-UV9  
devices omit this circuit and R26  
and add R25

## FPGA JTAG



## Bypass Caps for Power Block

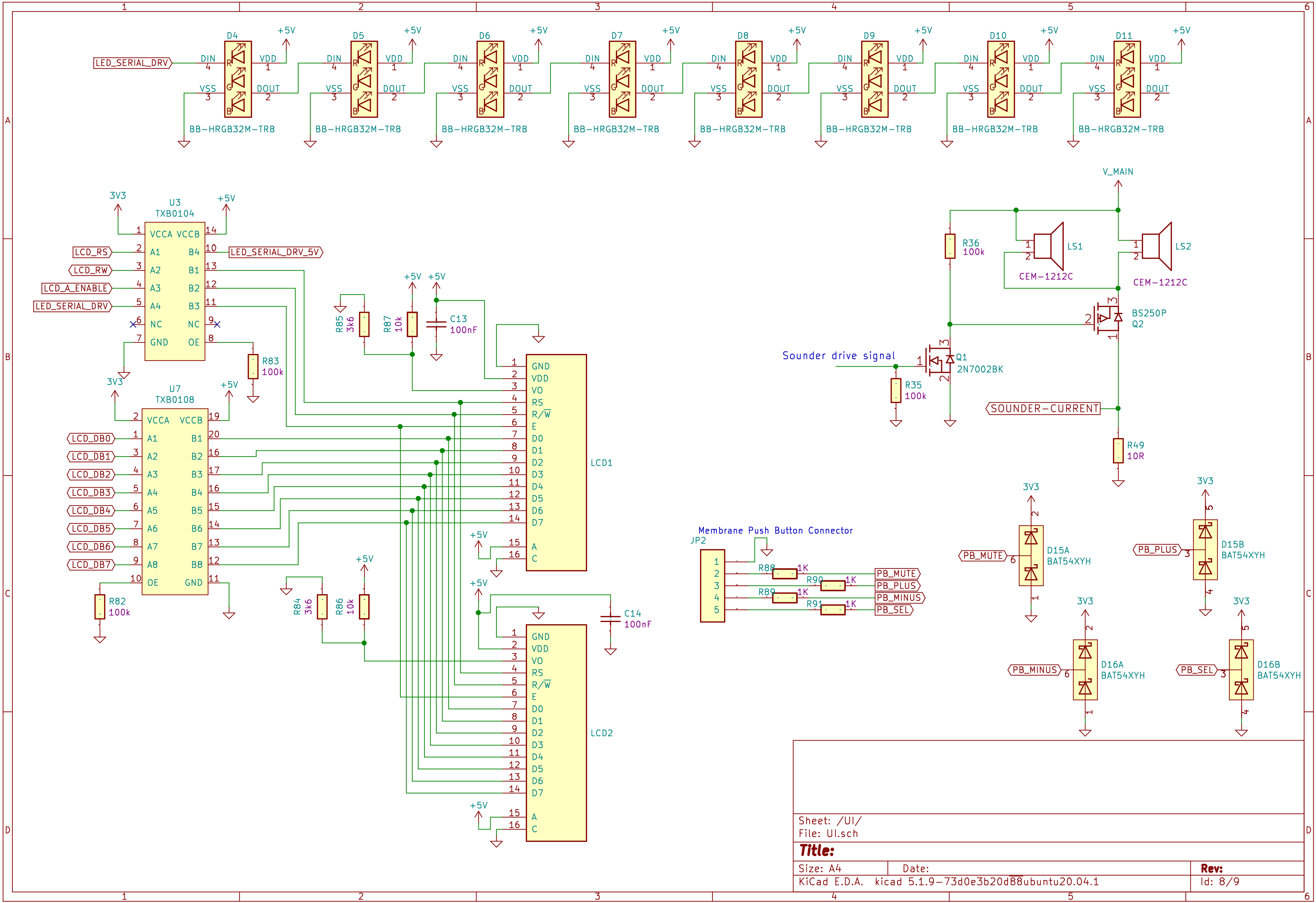


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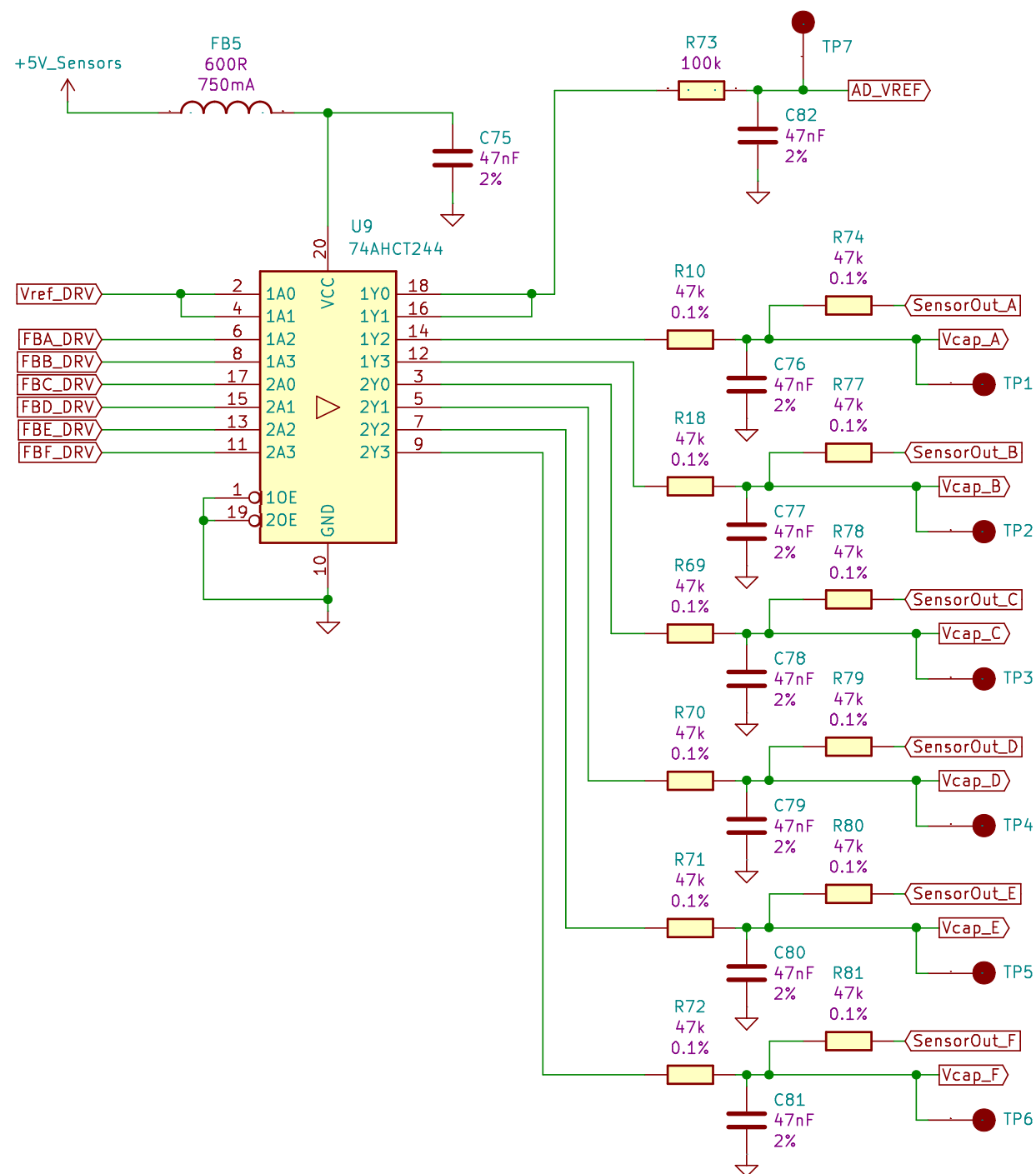
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**Rev:**   
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