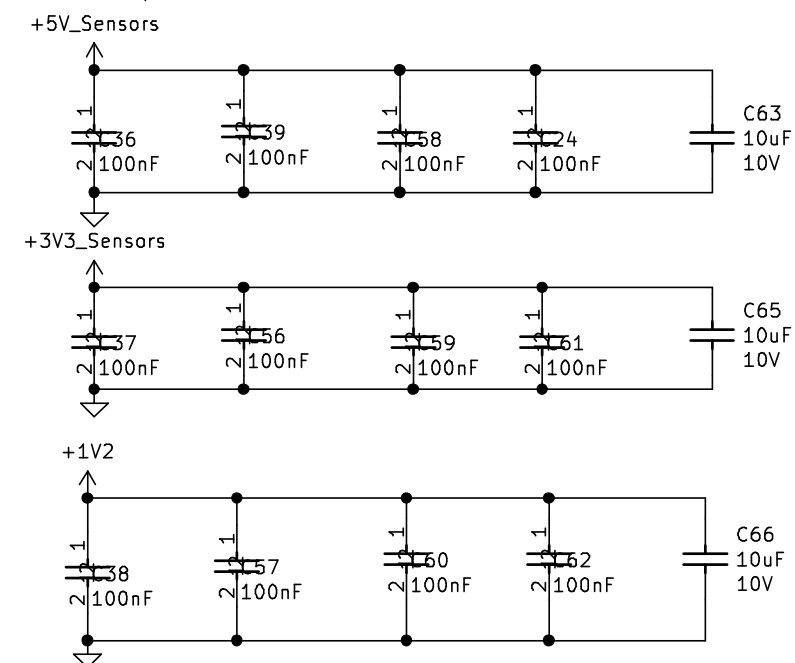
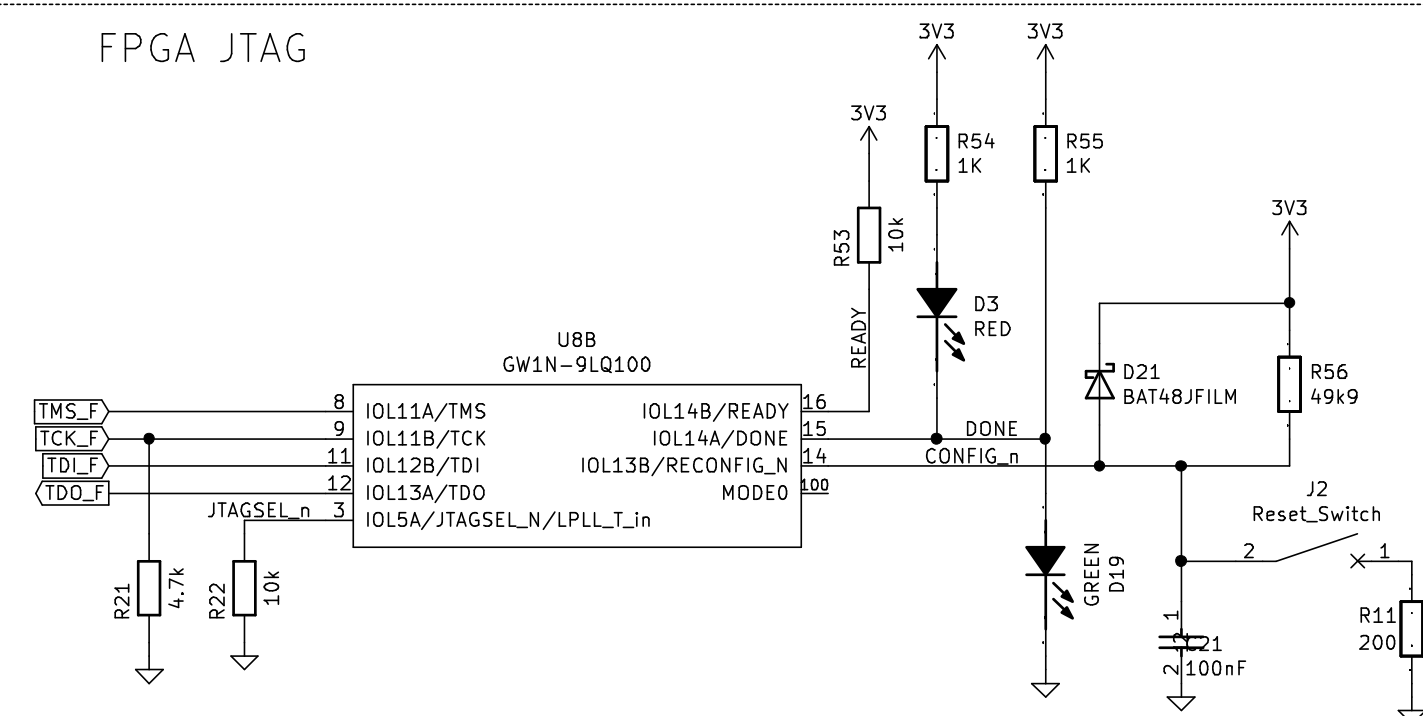
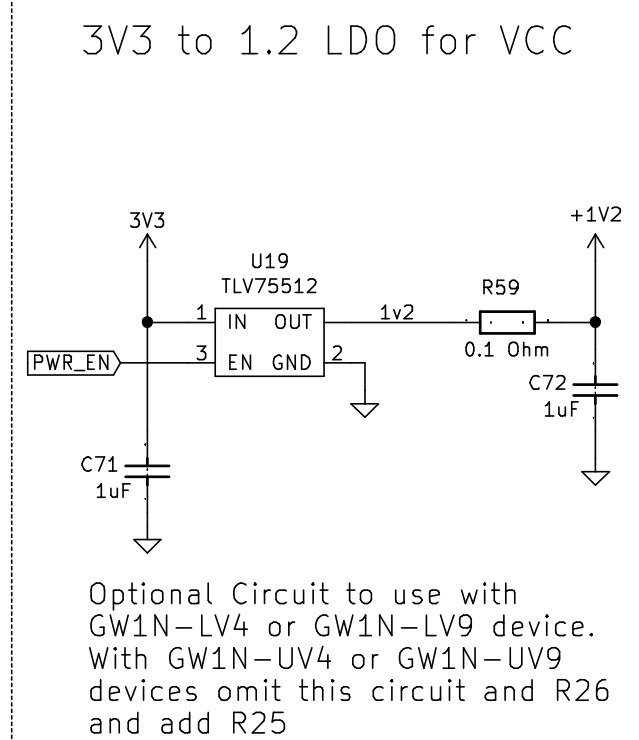
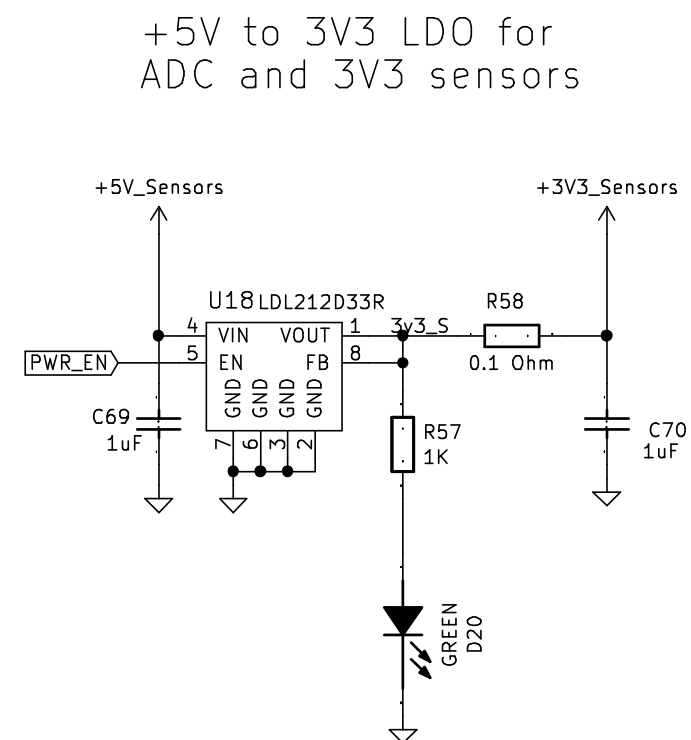


Pin Number	– 54	71	
GW1N–xV4 LQ100	– Vcco1	Vccx	R24, R29
GW1N–xV9 LQ100	– Vccx	Vcco1	R23, R30

Pins 54 and 71 are swapped between the -4 and -9 FPGA devices.

Add R25 and omit R26 when using GW1N-UV device



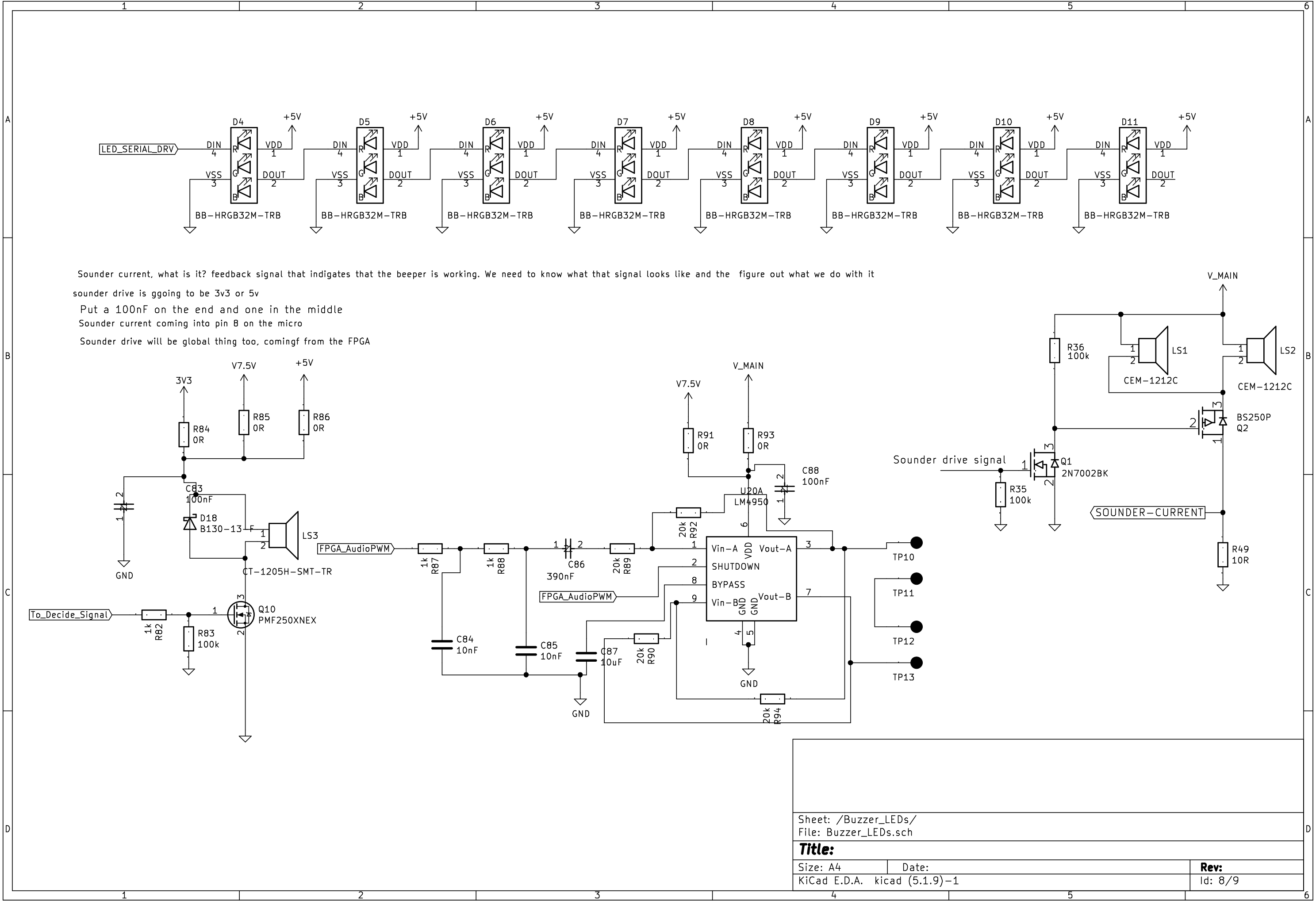
Sheet: /FPGA JTAG SPECIAL PWR/
File: FPGA_JTAG_SPECIAL_PWR.sch

Title:

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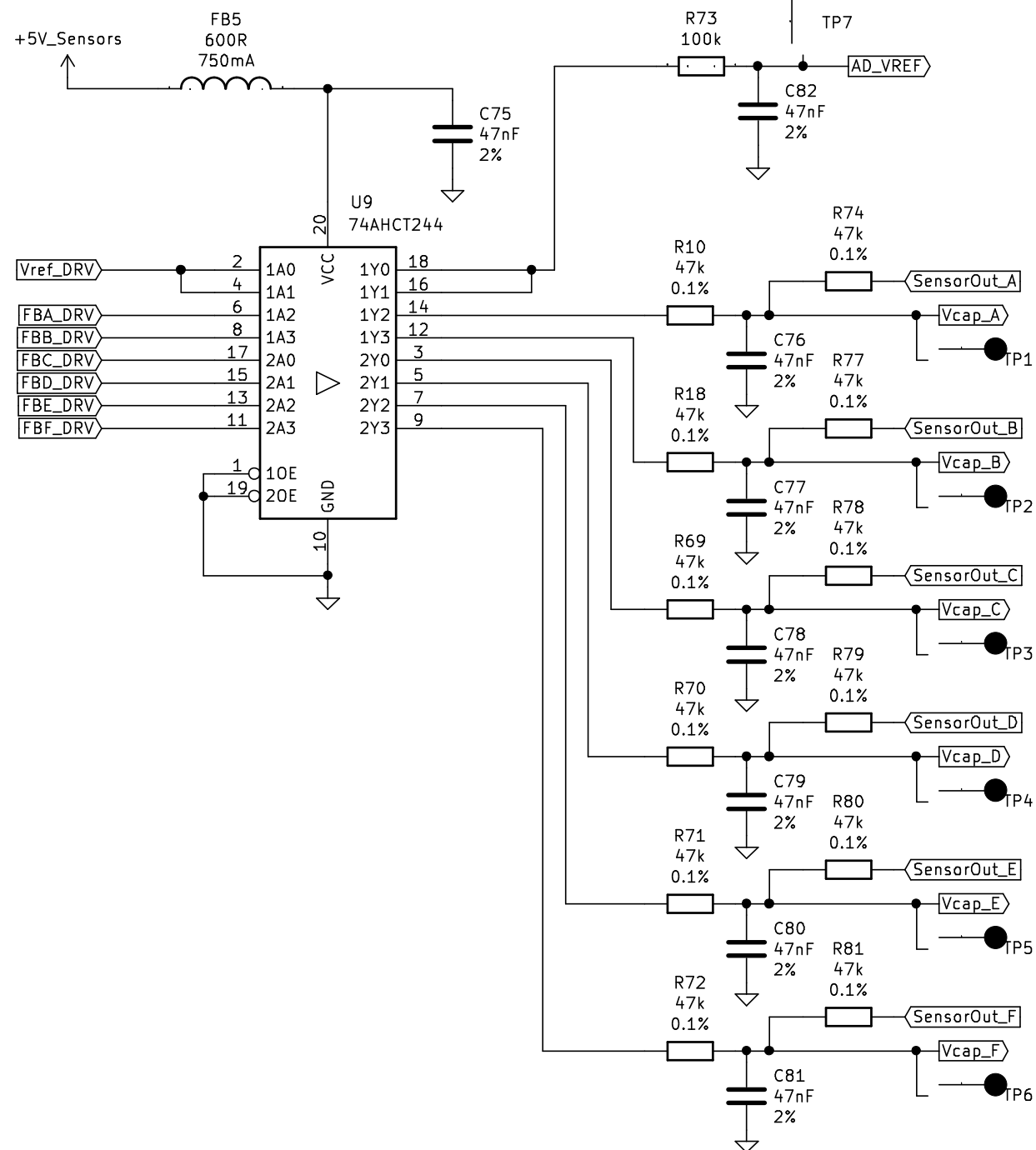
Sounder current, what is it? feedback signal that indigates that the beeper is working. We need to know what that signal looks like and the figure out what we do with it

sounder drive is ggoing to be 3v3 or 5v

Put a 100nF on the end and one in the middle

Sounder current coming into pin 8 on the micro

Sounder drive will be global thing too, comingf from the FPGA



Sheet: /AD CONVERTERS/
File: AD_conv.sch

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