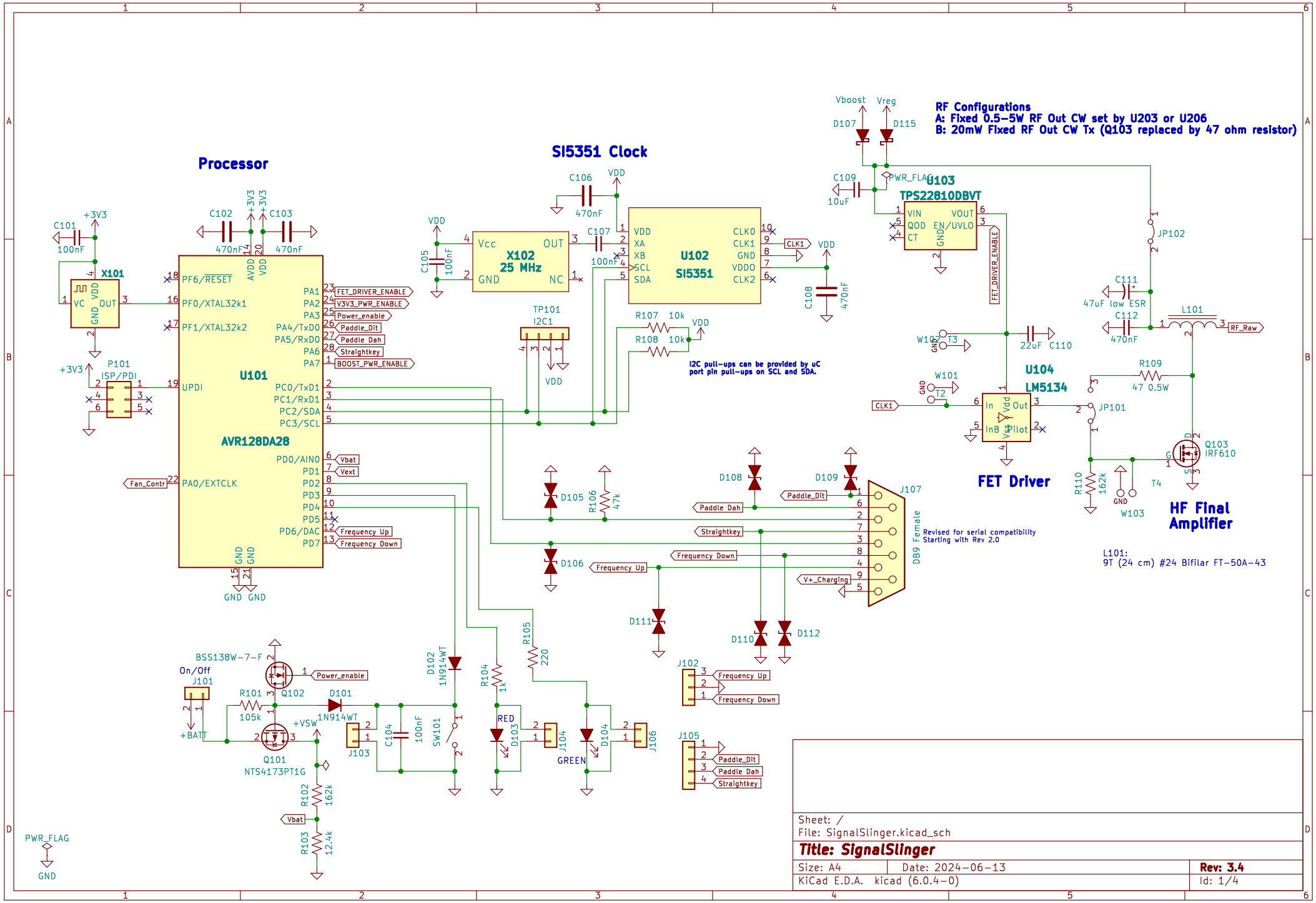
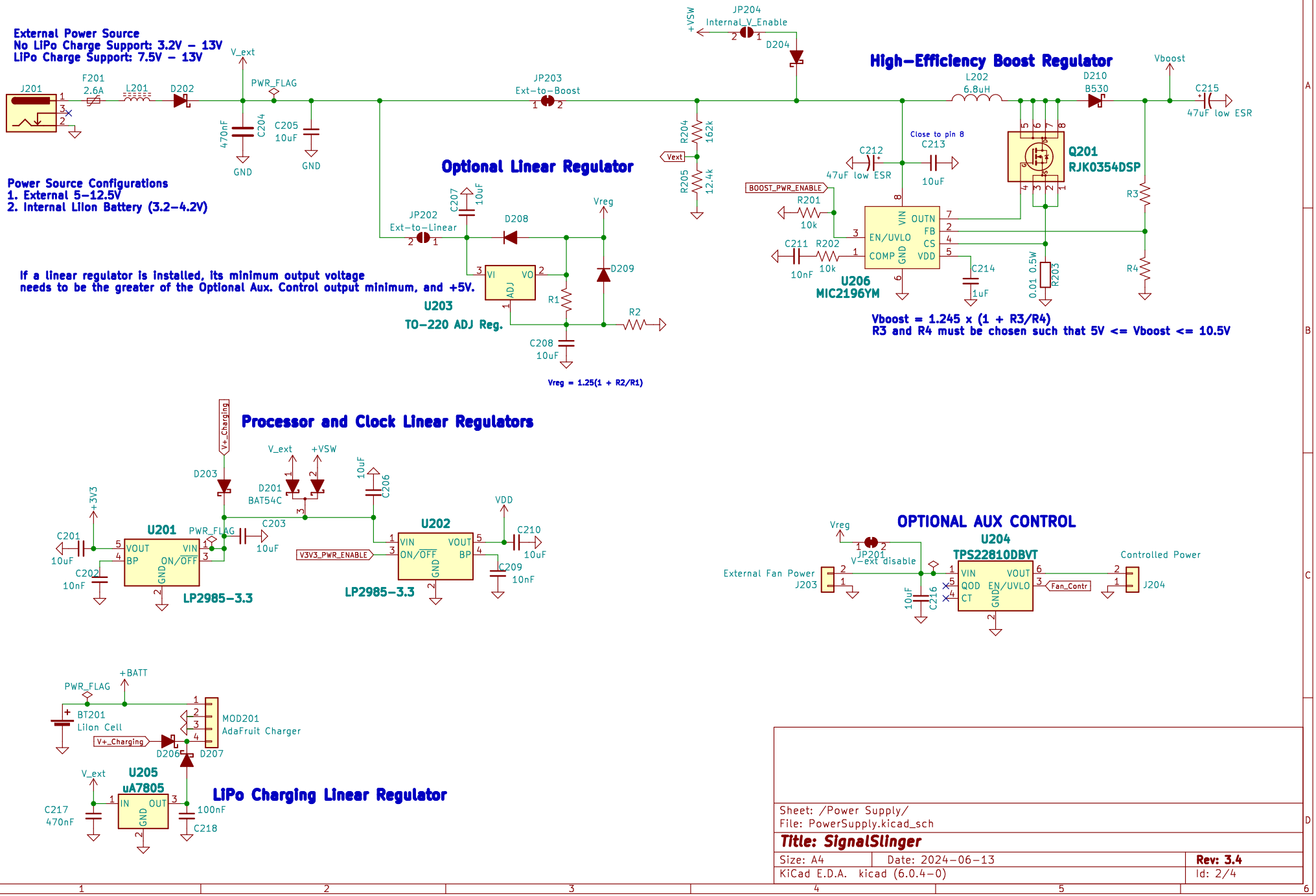

Processor
SI5351 Clock
RF Configurations
A: Fixed 0.5–5W RF Out CW set by U203 or U206
B: 20mW Fixed RF Out CW Tx (Q103 replaced by 47 ohm resistor)
U101
AVR128DA28
U102
SI5351
X102
25 MHz
X101
U103
TPS22810DBVT
U104
LM5134
FET Driver
HF Final Amplifier
I2C pull-ups can be provided by uC port pin pull-ups on SCL and SDA.
Revised for serial compatibility Starting with Rev 2.0
L101:
9T (24 cm) #24 Bifilar FT-50A-43
BSS138W-7-F
NTS4173PT1G
1N914WT
DB9 Female
Sheet: /
File: SignalSlinger.kicad_sch
Title: SignalSlinger
Size: A4
Date: 2024-06-13
Rev: 3.4
KiCad E.D.A. kicad (6.0.4-0)
Id: 1/4



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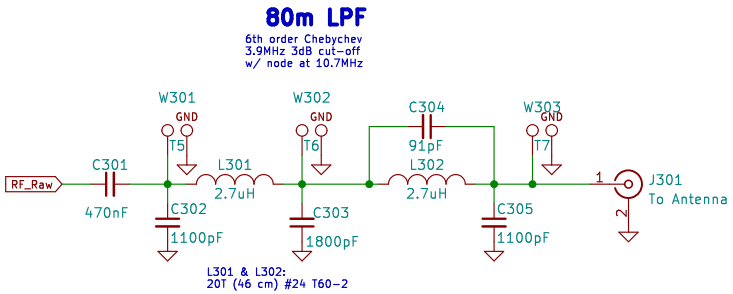
Size: A4	Date: 2024-06-13
KiCad E.D.A. kicad (6.0.4-0)	Id: 2/4

Rev: 3.4

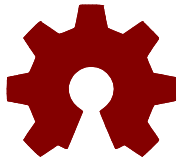


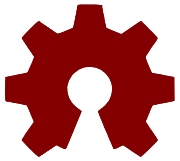
40m LPF
6th order Chebychev
8MHz 3dB cut-off
w/ node at 21.5 MHz

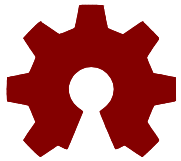
L301 & L302 = 1.3uH
C302 & C305 = 470pF
C303 = 820pF
C304 = 43pF

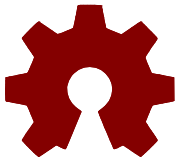
Sheet: /RF Filters/ File: RF_Filters.kicad_sch		
Title: SignalSlinger		
Size: A4	Date: 2024-06-13	**Rev: 3.4**
KiCad E.D.A. kicad (6.0.4-0)		Id: 3/4

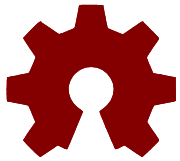
Core Product

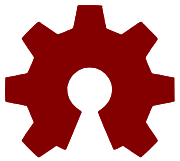

Chassis Box

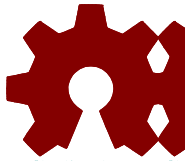

PCB Screws

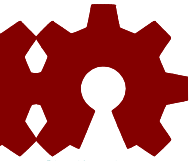

LED Light Pipes


Magnet Wire for RF Inductors

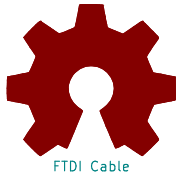

PCB_AND_ASSEMBLY

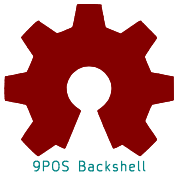

TO-220 Mounting Hardware

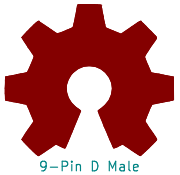

Shorting Jumper

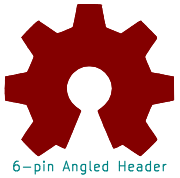

Shorting Jumper

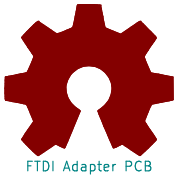
FTDI Support


FTDI Cable

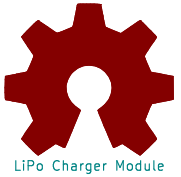

9POS Backshell


9-Pin D Male


6-pin Angled Header


FTDI Adapter PCB

Internal Battery Support


LiPo Charger Module

Sheet: /Misc/
File: Misc.kicad_sch

Title: SignalSlinger

Size: A4	Date: 2024-06-13	**Rev: 3.4**
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