





Sheet: /Processor/  
File: processor.kicad\_sch

**Title: SignalSnagger**

Size: A4 Date: 2024-03-29

KiCad E.D.A. kicad (6.0.4-0)

**Rev: 0.313**

Id: 2/4



	1	2	3	4	5	6
A						
B						
C						
D						
	1	2	3	4	5	6

Note for reference only and use  
the pins #'s in the root page of  
the schematic. Reason pin 7 and 8  
were swapped: to get proper  
phasing of this sense loop.

Sheet: /Antenna Diagram/ File: antenna_diagram.kicad_sch		
Title: <b>SignalSnagger</b>		
Size: A4	Date: 2024-03-29	Rev: <b>0.313</b>
KiCad E.D.A. kicad (6.0.4-0)		Id: 4/4