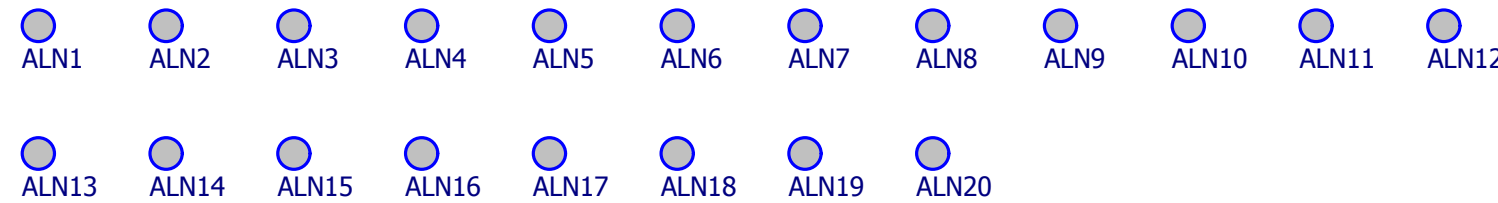


38 x IO + 2 x Clock In + 2 x Clock Out  
For each group with 200MHz maximum speed for each pin  
I/O Standards: 3.3V LVTTTL/LVCMOS, 2.5V, 1.8V, 1.5V and LVDS



A

A

B

B

C

C

D

D

