NSI State Machine

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Motivation

State machine = Clear behaviour for NSI implementations

Current problems:

Unclear division between control and data plane

Especially with auto-provision this becomes problematic

Suggestion:

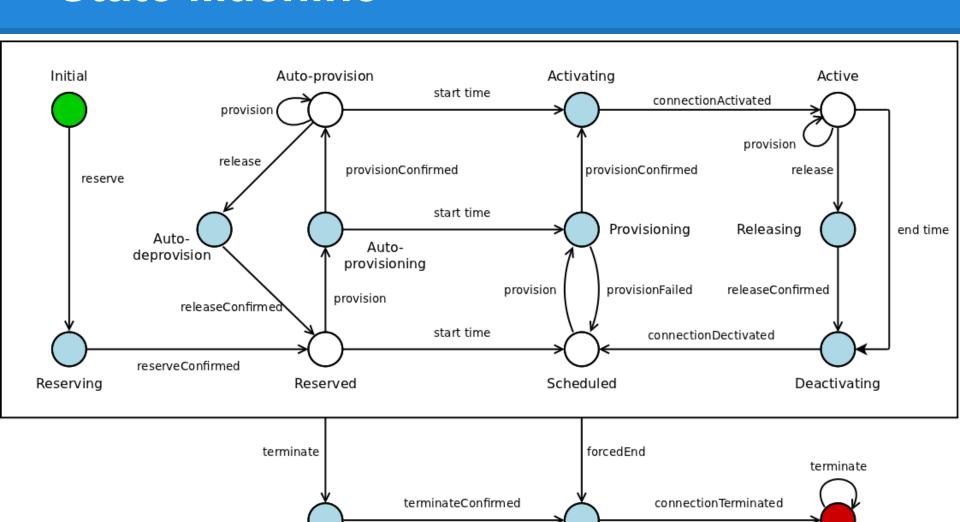
More distinct states for control and data plane

This introduces more states :-(

And potentially more primitives :-(

But makes it clear what is going on :-)

State Machine



Cleaning

Terminated

Terminating

Decision Points

Seperate states for control/data?

Add primitives for control/data?

Not exactly state machine though