INGENIC

PAVO

RD4740_PMP Development Board for Jz4740
Schematic Revision 1.2

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INGENIC Semiconductor Co.Ltd
System Technology Department

Release Data:
2007.08.1

INGENIC Semiconductor Co. Ltd		
Title: RD4740_PMP Development Board		
Size: B	Document Number: COVER	Rev: 1.2
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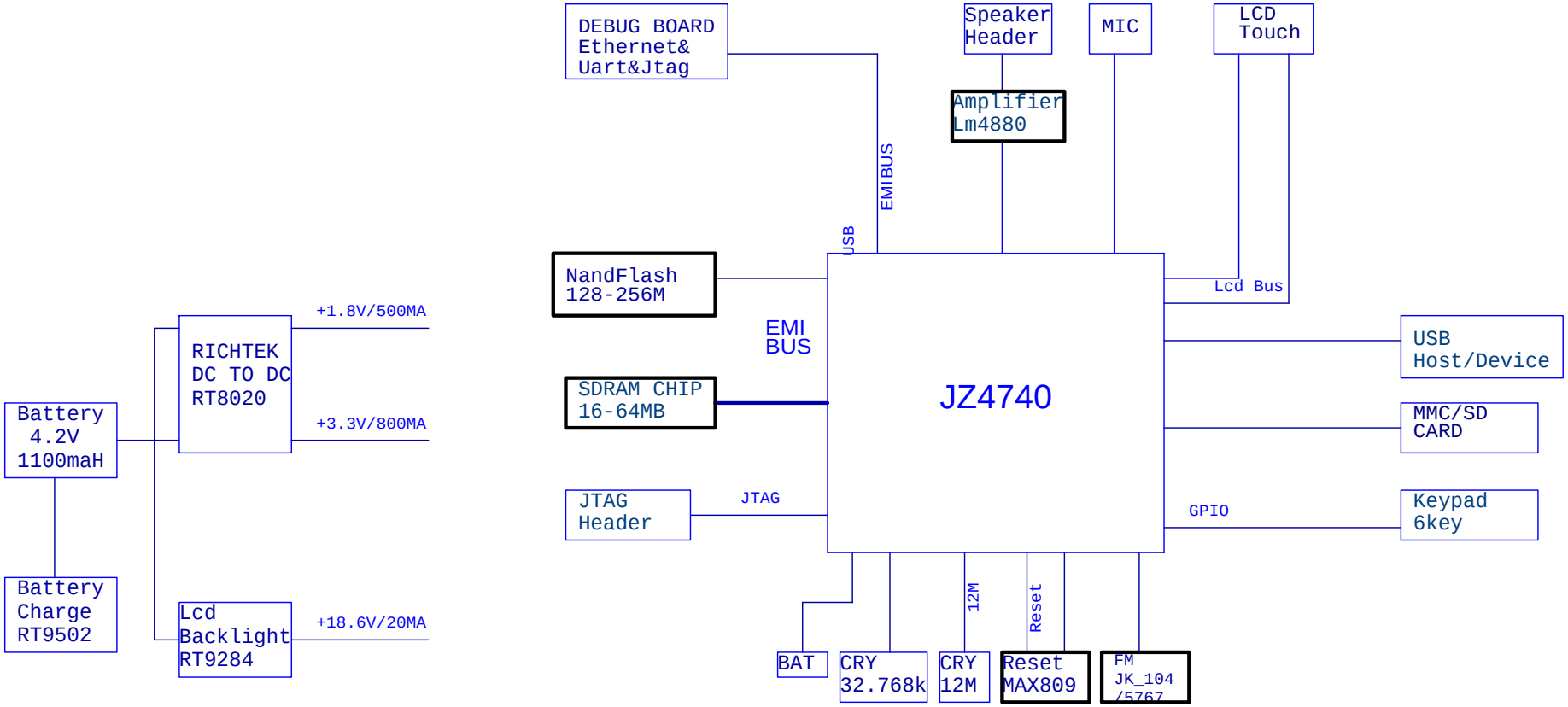

INGENIC Semiconductor Co. Ltd		
Title: RD4740_PMP Development Board		
Size: B	Document Number: System Architecture	Rev: 1.2
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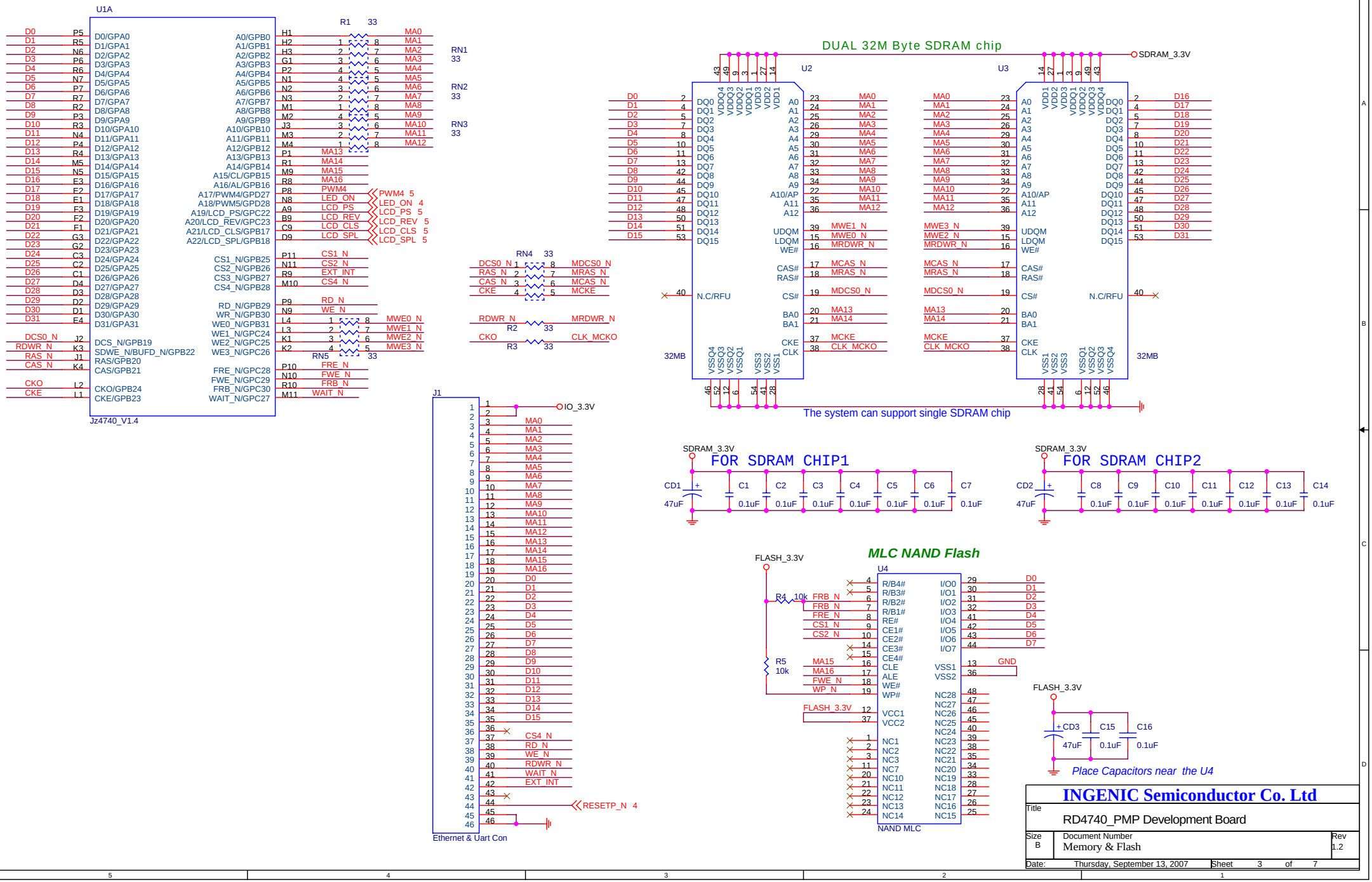
U1A
Jz4740_V1.4
R1 33
RN1 33
RN2 33
RN3 33
RN4 33
RN5 33
R2 33
R3 33
DUAL 32M Byte SDRAM chip
U2
U3
SDRAM_3.3V
32MB
The system can support single SDRAM chip
J1
IO_3.3V
Ethernet & Uart Con
SDRAM_3.3V
FOR SDRAM CHIP1
FOR SDRAM CHIP2
MLC NAND Flash
U4
NAND MLC
FLASH_3.3V
Place Capacitors near the U4
INGENIC Semiconductor Co. Ltd
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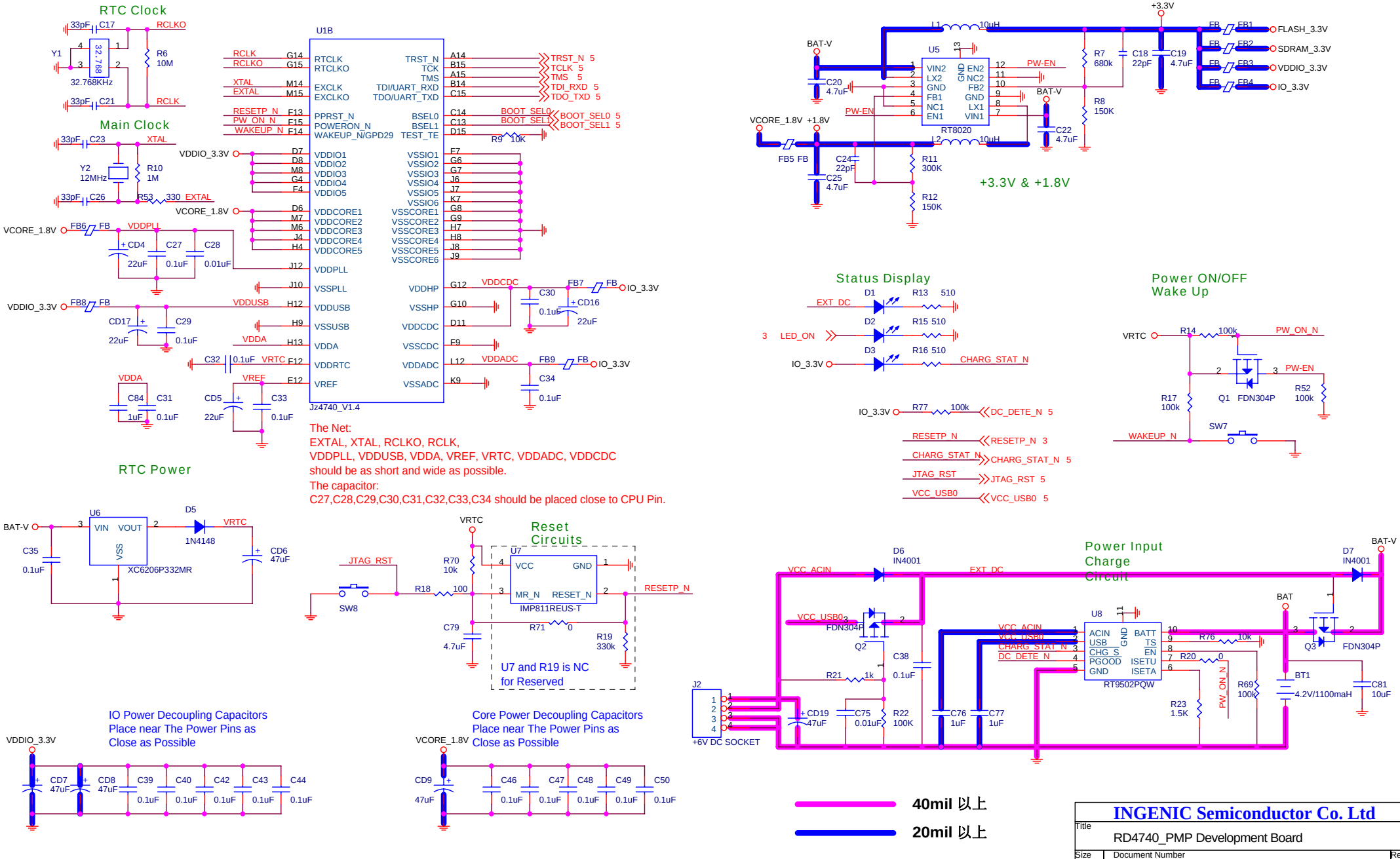

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Title: RD4740_PMP Development Board		
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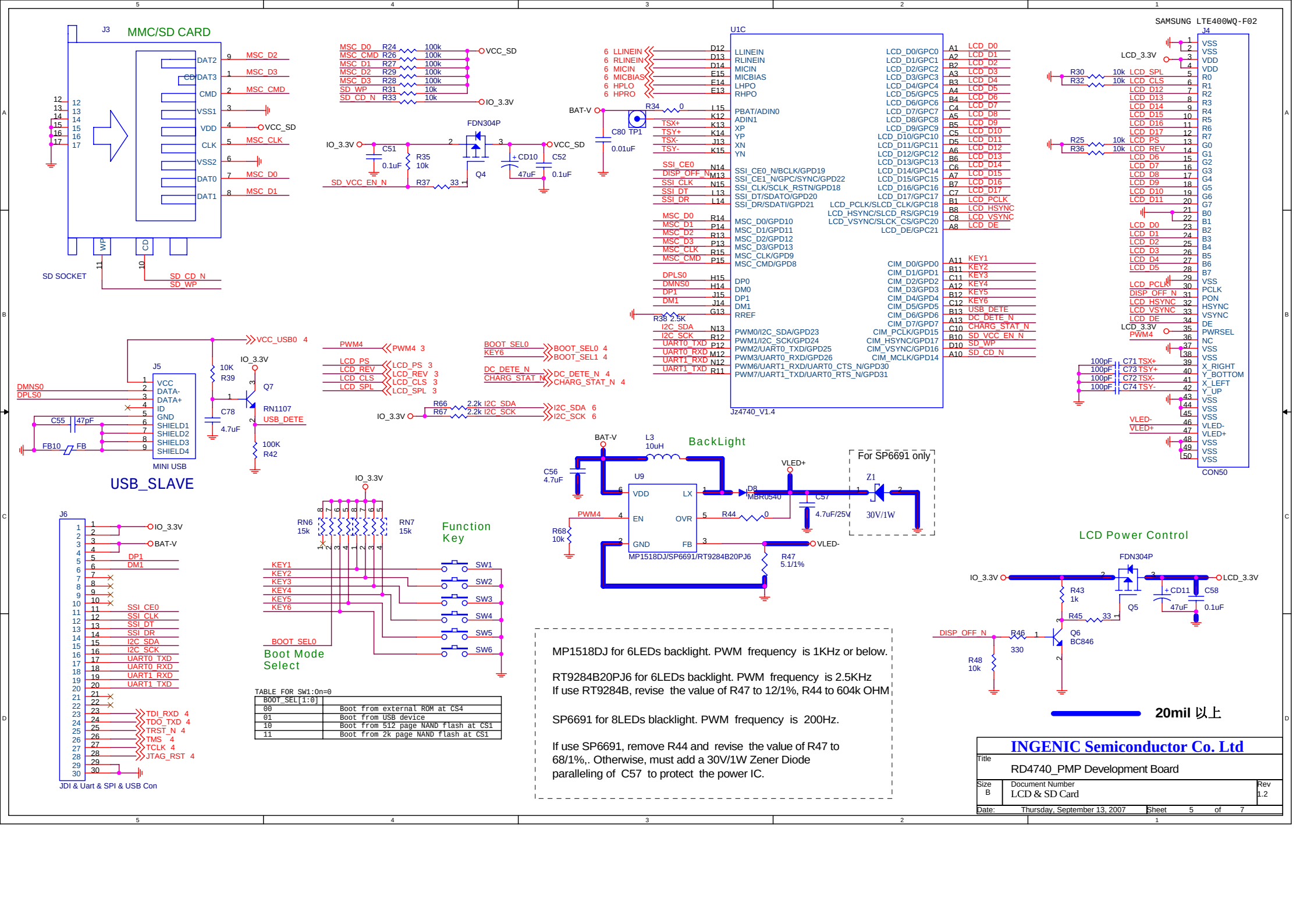
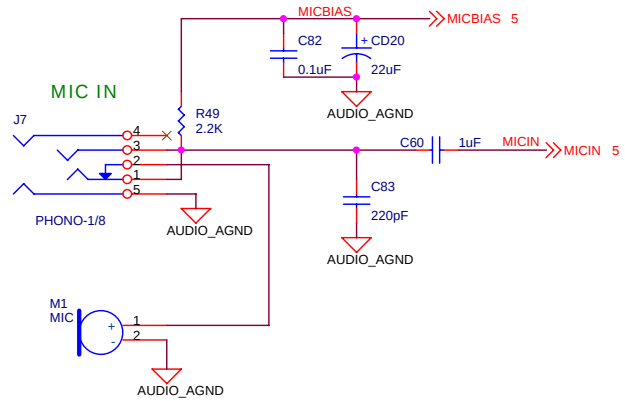
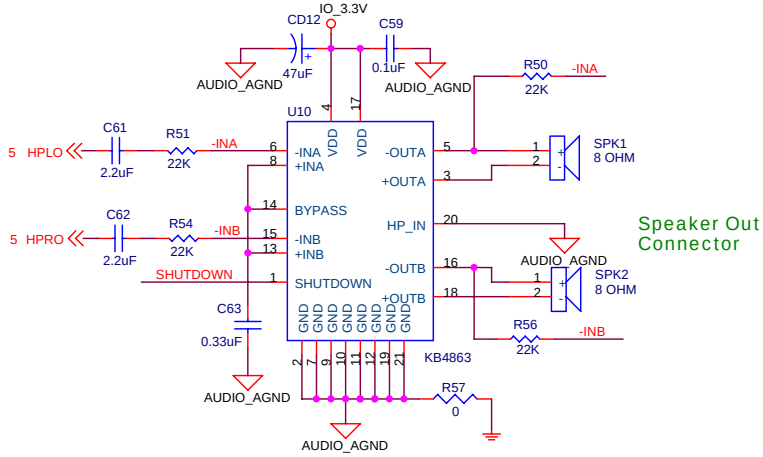
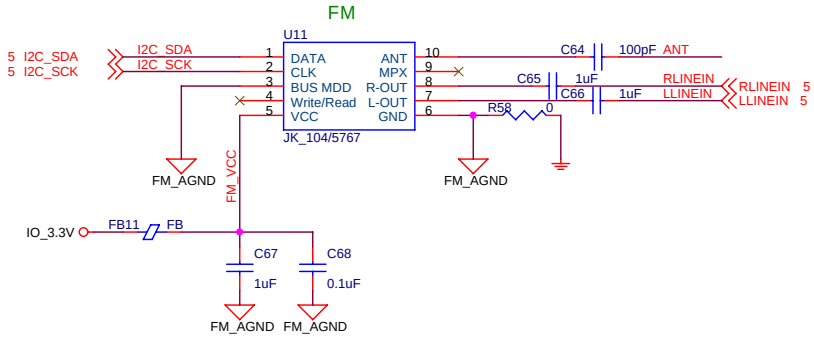


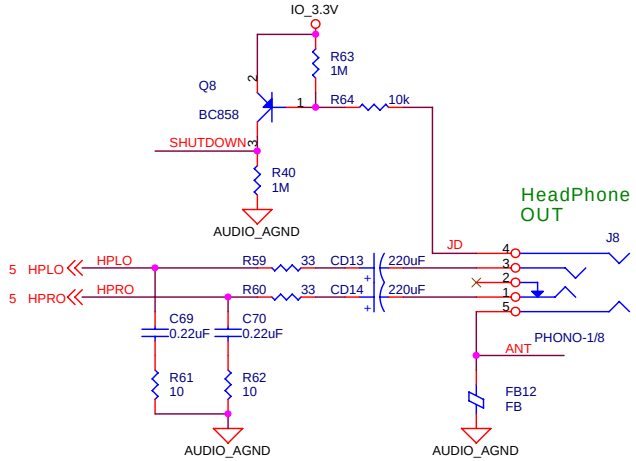
TABLE_FOR SW1:On=0

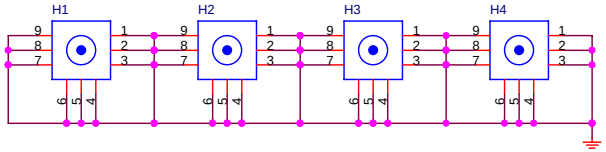
BOOT_SEL[1:0]	
00	Boot from external ROM at CS4
01	Boot from USB device
10	Boot from 512 page NAND flash at CS1
11	Boot from 2k page NAND flash at CS1











INGENIC Semiconductor Co. Ltd		
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Data	Revision	Change
Apr. 5 2007	Rev1.0	1. First released Revision
Jun. 26 2007	Rev1.1	1. Swap the signal of SD_VCC_EN_N and SD_CD_N 2. Change the R38 to 2.5k/1% 3. Add the R53 for oscillator circuit. 4. Modified the power_on circuit. 5. Modified the USB Power detected circuit. 6. Modified the LCD Backlight circuit.
Aug. 1 2007	Rev1.2	1. Add R75,R76 for charge circuit 2. Add RC reset circuit. 3. Add U12 for battery voltage detection. 4. Change the signal to Pin A10,B10,C10,D10. C10,D10 should be conneted to the same direction of GPIO.
Sep. 5 2007	Rev1.2.1	1. Remove R75, adjust value of R76 to 10k 2. Add C81 for charge status indicate 3. Add R77 high-drain for EXT_DC indicate 4. Add CD20,C82 for Celminating coupling of Micbias power 5. Add C83 for filter the high frequence noise of audio signal 6. Change the CD18 to C84. 7. Remove the U12 and related resistors.

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