

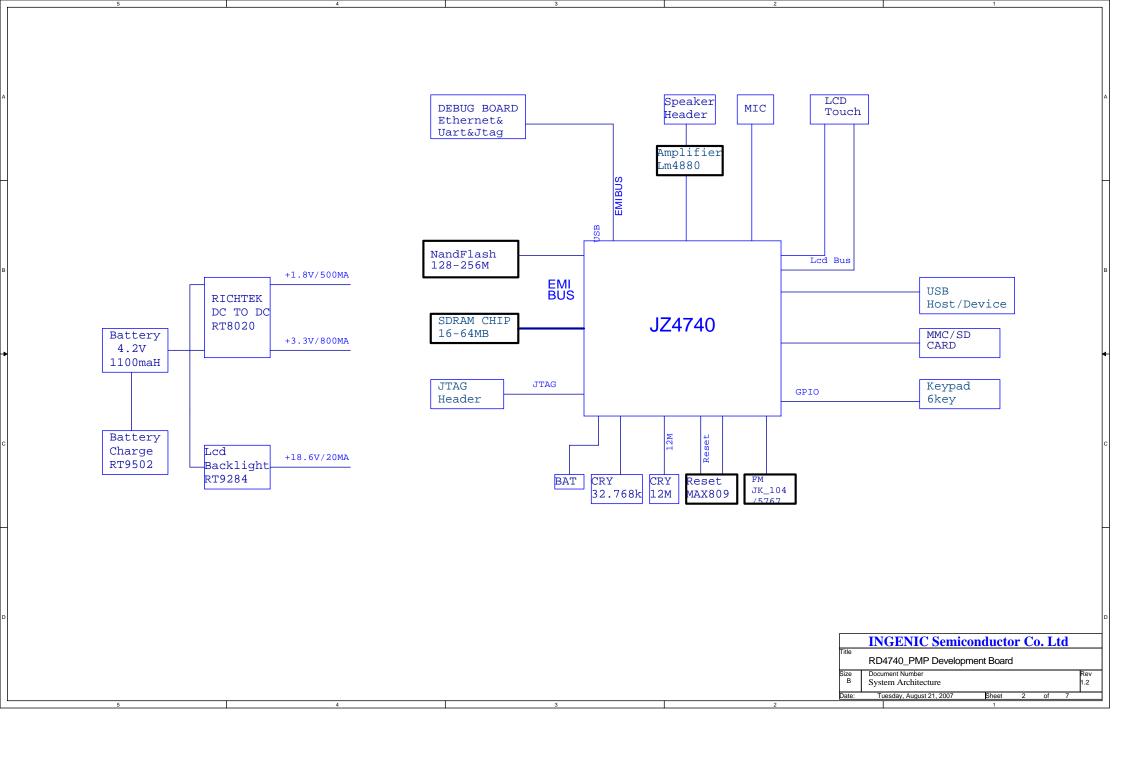
PAVO

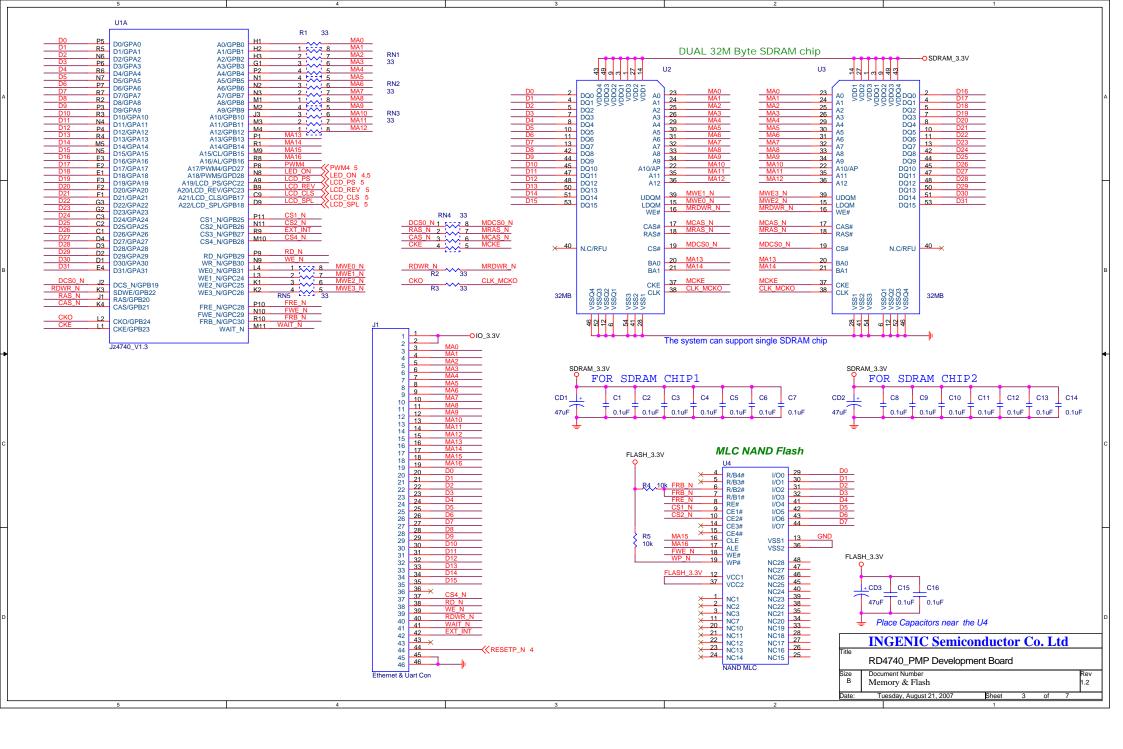
RD4740_PMP Development Board for Jz4740 Schematic Revision 1.2

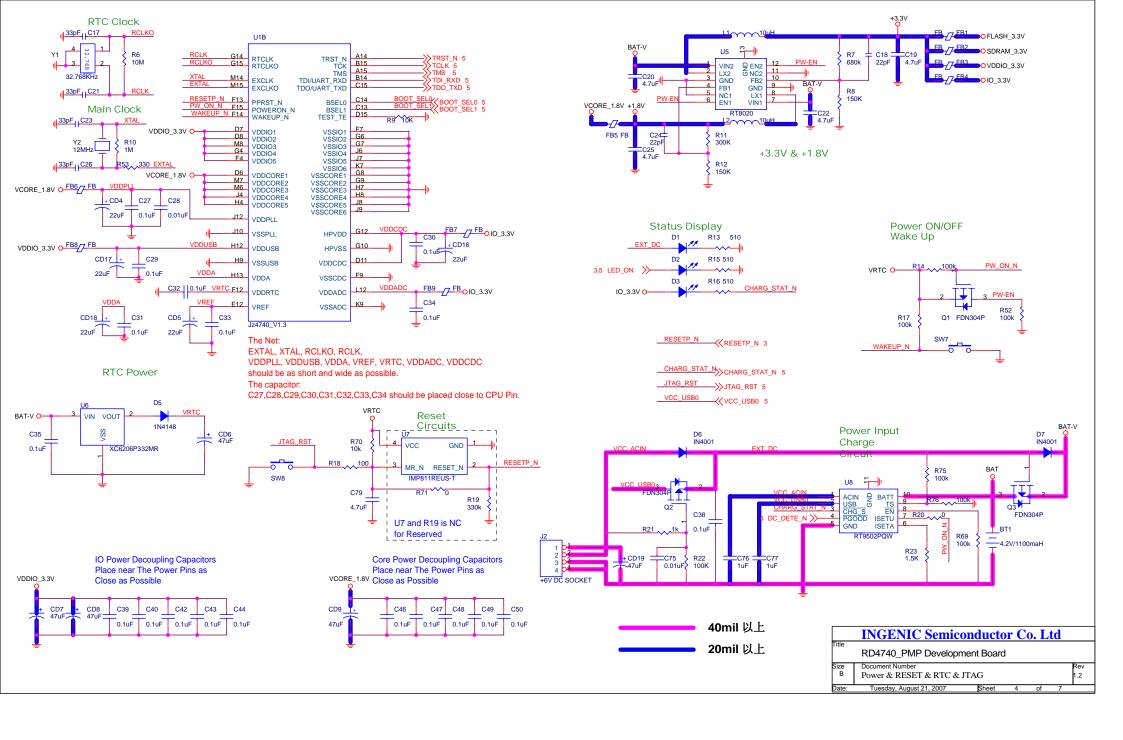
Title	Page
Cover Sheet	1
System Architecture	2
Memory & FLASH	3
Power RESET & RTC & JTAG	4
LCD & SD CARD	5
AUDIO	6
Revision History	7

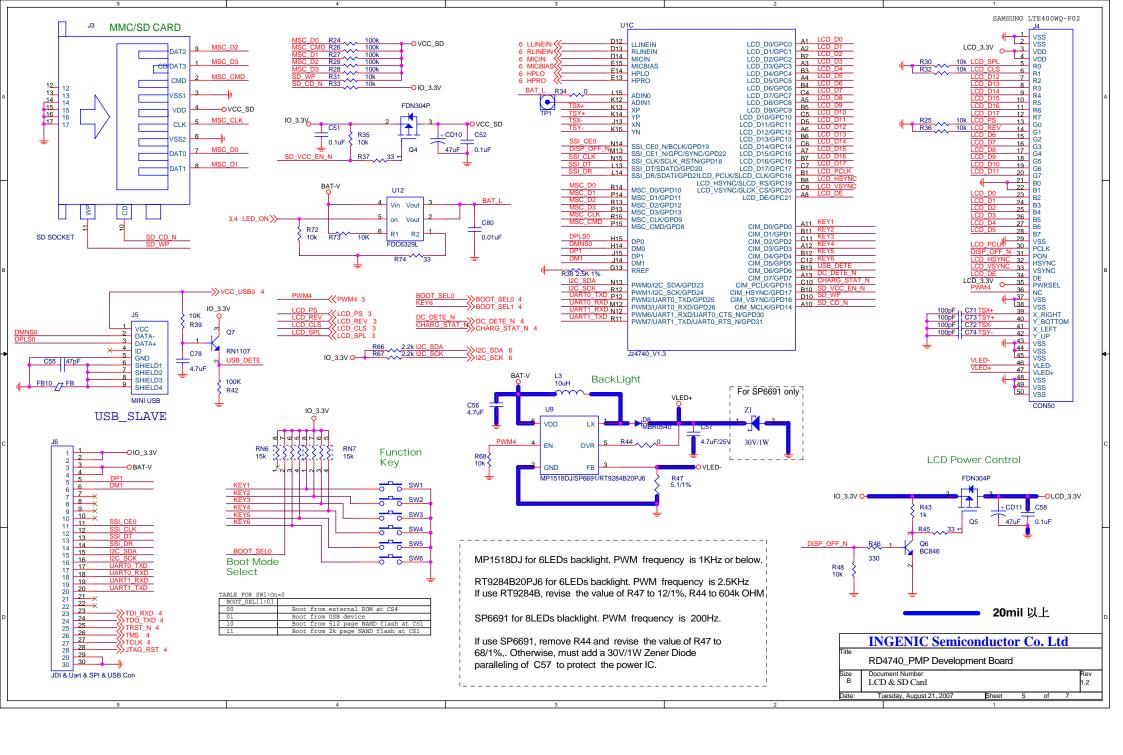
INGENIC Semiconductor Co.Ltd System Technology Department Release Data: 2007.08.1

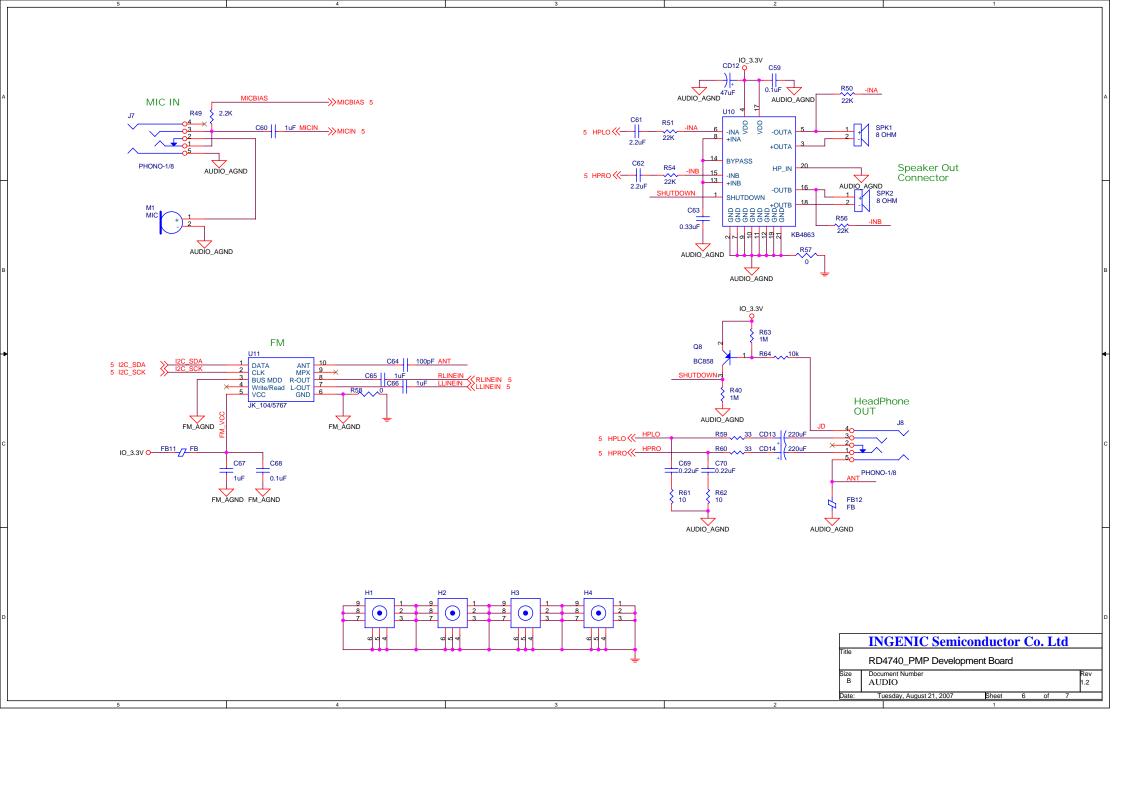
	INGENIC Semiconductor Co. Ltd					
Title	RD4740_PMP Development Board					
Size B	Document Number COVER					Rev 1.2
Date:	Tuesday, August 21, 2007	Sheet	1	of	7	











Revision Data Change Apr. 5 2007 Rev1.0 1. First released Revision Jun. 26 2007 Rev1.1 1. Swap the signal of SD_VCC_EN_N and SD_CD_N 2. Change the R38 to 2.5k/1% 3. Add the R53 for oscillator circuit. 4. Modified the power_on circuit. 5. Modified the USB Power detected circuit. 6. Modified the LCD Backlight circuit. Aug. 1 2007 Rev1.2 1. Add R75,R76 for charge circuit 2. Add RC reset circuit. 3. Add U12 for battery voltage detection. 4. Change the signal to Pin A10,B10,C10,D10. C10,D10 should be conneted to the same direction of GPIO.

5 4 3 2