

**ARM<sup>®</sup> Cortex<sup>®</sup>-M0**  
**32-bit Microcontroller**

**NuMicro<sup>®</sup> Family**  
**M0518 Series BSP**  
**Revision History**

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**Revision 3.00.003** (Released 2017-10-05)

1. Fix clear Time-out flag method bug in I2C\_ClearTimeoutFlag() of I<sup>2</sup>C driver.
2. Fix bug of PWM\_ConfigOutputChannel() and PWM\_ConfigCaptureChannel() for counter type setting error when u32ChannelNum is 1,3,5.
3. Fix bug for revised PWM\_Stop() implementation
4. Fix bug for return value error of PWM\_ConfigOutputChannel()
5. Add ADC\_MeasureAVDD sample code
6. Add new function to control systick and select systick clock source, CLK\_EnableSysTick() and CLK\_DisableSysTick()
7. Add CLK\_SysTickLongDelay() for long delay
8. Fix UART\_SelectLINMode() clear enable bit setting bug
9. Fix PLL clock source selection bug in CLK\_SetCoreClock()
10. Fix clear Receive Line Status interrupt flag bug in UART\_ClearIntFlag().

**Revision 3.00.002** (Released 2014-12-23)

11. Fix PWM driver bug for output low when duty is 100%
12. Fix BPWM driver bug for output low when duty is 100%
13. Fix CLK driver bug in CLK\_SetCoreClock():
14. Fix CLK driver constant definitions error of (B)PWM0/1\_MODULE clock source selection.
15. Fix GPIO\_ENABLE\_DOUT\_MASK() and GPIO\_DISABLE\_DOUT\_MASK() bug of GPIO driver.
16. Fix PWM driver bug of PWM\_MASK\_OUTPUT() to remove redundant parenthesis.
17. Fix BPWM driver bug of BPWM\_MASK\_OUTPUT() to remove redundant parenthesis
18. Fix UART driver clear flag bug in UART\_ClearIntFlag()
19. Fix I2C driver module reset bug of I2C\_Close().
20. Fix clear RS-485 address byte detection flag bug in UART\_RS485\_CLEAR\_ADDR\_FLAG() of UART driver.
21. Fix clear RS-485 address byte detection flag clear bug in RS485\_HANDLE() of UART\_RS485\_Slave sample code.
22. Fix NVIC\_EnableIRQ() to NVIC\_DisableIRQ() after CHIP wake-up in I2C\_Wakeup\_Slave sample code
23. Fix UART RS485 RTS active level to high level active in RS485\_9bitModeMaster() of UART RS485 Sample code.
24. Add PWM\_EnableLoadMode() and PWM\_DisableLoadMode() functions to PWM driver
25. Add PWM\_SetBrakePinSource() function to PWM driver
26. Add CLK\_GetPCLKFreq() function to CLK driver
27. Add new macro PWM\_SET\_DEADZONE\_CLK\_SRC() to PWM driver
28. Add nonblocking printf implementation and use predefine compiler option to enable/disable it.

**Revision 3.00.002** (Released 2014-07-18)

1. Fixed constant definitions of Timer 2 and Timer 3 in clk.h
2. Fixed SYS\_Init() GPIO initial bug of \SampleCode\StdDriver\ACMP.

**Revision 3.00.001** (Released 2014-06-12)

1. First Release

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