

**ARM<sup>®</sup> Cortex<sup>®</sup>-M0**  
**32-bit Microcontroller**

**NuMicro<sup>®</sup> Family**  
**M0519 Series BSP**  
**Revision History**

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### Revision 3.00.002 (Released 2017-10-19)

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1. Fix clear Receive Line Status interrupt flag bug in UART\_ClearIntFlag() of uart.c.
2. Fix UART\_SelectLINMode() clear enable bit setting bug in uart.c.
3. Modify to ignore debug message when enabling semihost with NuLink connected.
4. Fix PLL clock source selection bug in CLK\_SetCoreClock() in clk.c.
5. Fix MFP define "SYS\_MFP\_P43\_Msk" value.
6. Fix MFP define "SYS\_MFP\_P94\_Msk" value.
7. Fix MFP define "SYS\_MFP\_P41\_Msk" value.
8. Modify I2C name to I2C0.
9. Add new function to control systick and select systick clock source, CLK\_EnableSysTick() and CLK\_DisableSysTick() in clk.c.
10. Add CLK\_SysTickLongDelay() for long delay in clk.h.
11. Add missing MFP definitions of P3.6, P3.7, P4.3, P8.5 and P8.6 GPIO.
12. Add constant define "CLK\_CLKSEL0\_STCLK\_S\_HCLK" in CLKSEL0 constant definitions for CLK\_EnableSysTick() function to select HCLK as SysTick clock source.
- 13.

### Revision 3.00.001 (Released 2015-03-15)

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1. First release.

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