

ARM® Cortex®-M0
32-bit Microcontroller

NuMicro® Family
M0519 Series BSP
Revision History

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Revision 3.00.004 (Released 2021-01-17)

1. Added Apache-2.0 license declaration in driver source.
2. Added README.md file.
3. Fixed build error of FMC_IAP IAR sample code.

Revision 3.00.003 (Released 2019-11-12)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Fixed CLK_SetHCLK() bug of CLK driver.

Revision 3.00.002 (Released 2017-10-19)

1. Fixed clear Receive Line Status interrupt flag bug in UART_ClearIntFlag() of uart.c.
2. Fixed UART_SelectLINMode() clear enable bit setting bug in uart.c.
3. Fixed PLL clock source selection bug in CLK_SetCoreClock() in clk.c.
4. Fixed MFP define "SYS_MFP_P43_Msk" value.
5. Fixed MFP define "SYS_MFP_P94_Msk" value.
6. Fixed MFP define "SYS_MFP_P41_Msk" value.
7. Modified I2C name to I2C0.
8. Modified to ignore debug message when enabling semihost without NuLink connected.
9. Add new function to control systick and select systick clock source, CLK_EnableSysTick() and CLK_DisableSysTick() in clk.c.
10. Add CLK_SysTickLongDelay() for long delay in clk.h.
11. Add missing MFP definitions of P3.6, P3.7, P4.3, P8.5 and P8.6 GPIO.
12. Add constant define "CLK_CLKSEL0_STCLK_S_HCLK" in CLKSEL0 constant definitions for CLK_EnableSysTick() function to select HCLK as SysTick clock source.

Revision 3.00.001 (Released 2015-03-15)

1. First release.

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