ARM® Cortex®-M0

32-bit Microcontroller

NuMicro® Family
M0564 Series BSP
Revision History

For additional information or questions, please contact: Nuvoton Technology Corporation.

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Revision 3.00.005 (Released 2024-06-13)

1. Added SYS_PowerDown_MinCurrent sample code.
2. Updated SPI_Loopback sample code.
3. Modified SYS_UnLockReg() time-out handler.
4. Fixed return in main() cause hardfault issue.
5. Modified setting of Third-Party Driver for IAR Project.
6. Added infinite loop timeout handler.
7. Revised CLK, GPIO, SYS and UART driver comment description.
8. Added I2C hang up & recover mechanism for I2C Master and Slave sample code.
9. I2C pins enable schmitt trigger.
10. Fixed the issue causing execution failure in the USCI-I2C sample code by correcting the erroneous status return.
11. Added FMC_SPROM sample code.
12. Added slew rate control for SPI sample codes.
13. Updated sc library to resolve SC_ReadATR failure.
14. Modified SYS_PowerDown_MinCurrent and RegBased/FMC_IAP sample code.

Revision 3.00.004 (Released 2021-01-17)

1. Fixed USCI_SPI driver issue.
2. Fixed warnings of adc and pwm driver.
3. Fixed USPI_SET_SS_HIGH macro.
4. Corrected adc definitions for conversion sampling time selection.
5. Added Apache-2.0 license declaration in driver source.
6. Added README.md file.
7. Added SPI_TRIGGER_TX_RX_PDMA and SPI_DISABLE_TX_RX_PDMA API.
8. Fixed build error of FMC_IAP IAR sample code.

Revision 3.00.003 (Released 2019-11-12)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Added FMCIDLE_MODULE definition in CLK driver.
4. Added SYS_VoltageDetector sample code.
5. Fixed UART_Wakeup sample code comment typo.

Revision 3.00.002 (Released 2017-05-05)

1. Update multi-function pin name definitions.
2. Add single byte and multi byte write/read APIs for I2C.

Revision 3.00.001 (Released 2016-10-17)

1. Initial Release

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