

ARM® Cortex®-M0
32-bit Microcontroller

NuMicro® Family
M0564 Series BSP
Revision History

The information described in this document is the exclusive intellectual property of Nuvoton Technology Corporation and shall not be reproduced without permission from Nuvoton.

Nuvoton is providing this document only for reference purposes of NuMicro microcontroller based system design. Nuvoton assumes no responsibility for errors or omissions.

All data and specifications are subject to change without notice.

For additional information or questions, please contact: Nuvoton Technology Corporation.

www.nuvoton.com

Revision 3.00.005 (Released 2023-09-05)

1. Fixed the build error caused by the modification for compiler6.
2. Revised startup_NUC126.s and retarget.c files to fix Semihost build issue.
3. Modified settings of Keil project to speed up compilation.
4. Added SYS_PowerDown_MinCurrent sample code.
5. Updated SPI_Loopback sample code.
6. Modified SYS_UnLockReg() time-out handler.
7. Fixed return in main() cause hardfault issue.
8. Modified setting of Third-Party Driver for IAR Project.
9. Added infinite loop timeout handler.
10. Revised CLK, GPIO, SYS and UART driver comment description.
11. Added I2C hang up & recover mechanism for I2C Master and Slave sample code.
12. I2C pins enable schmitt trigger.

Revision 3.00.004 (Released 2021-01-17)

1. Fixed USCI_SPI driver issue.
2. Fixed warnings of adc and pwm driver.
3. Fixed USPI_SET_SS_HIGH macro.
4. Corrected adc definitions for conversion sampling time selection.
5. Added Apache-2.0 license declaration in driver source.
6. Added README.md file.
7. Added SPI_TRIGGER_TX_RX_PDMA and SPI_DISABLE_TX_RX_PDMA API.
8. Fixed build error of FMC_IAP IAR sample code.

Revision 3.00.003 (Released 2019-11-12)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Added FMCIDLE_MODULE definition in CLK driver.
4. Added SYS_VoltageDetector sample code.
5. Fixed UART_Wakeup sample code comment typo.

Revision 3.00.002 (Released 2017-05-05)

1. Update multi-function pin name definitions.
2. Add single byte and multi byte write/read APIs for I2C.

Revision 3.00.001 (Released 2016-10-17)

1. Initial Release

Important Notice

Nuvoton Products are neither intended nor warranted for usage in systems or equipment, any malfunction or failure of which may cause loss of human life, bodily injury or severe property damage. Such applications are deemed, "Insecure Usage".

Insecure usage includes, but is not limited to: equipment for surgical implementation, atomic energy control instruments, airplane or spaceship instruments, the control or operation of dynamic, brake or safety systems designed for vehicular use, traffic signal instruments, all types of safety devices, and other applications intended to support or sustain life.

All Insecure Usage shall be made at customer's risk, and in the event that third parties lay claims to Nuvoton as a result of customer's Insecure Usage, customer shall indemnify the damages and liabilities thus incurred by Nuvoton.

*Please note that all data and specifications are subject to change without notice.
All the trademarks of products and companies mentioned in this datasheet belong to their respective owners.*