

**ARM<sup>®</sup> Cortex<sup>®</sup>-M**  
**32-bit Microcontroller**

**NuMicro<sup>™</sup> Family**  
**M058S Series BSP**  
**Revision History**

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### Revision 3.00.003 (Released 2017-10-20)

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1. Fixed clear Receive Line Status interrupt flag bug in UART\_ClearIntFlag().
2. Fixed PLL clock source selection bug in CLK\_SetCoreClock().
3. Fixed UART\_SelectLINMode() clear enable bit setting bug.
4. Add CLK\_SysTickLongDelay() for longer delay time than CLK\_SysTickDelay().
5. Modified to ignore debug message when enabling semihost without NuLink connected.
6. Add constant define "CLK\_CLKSEL0\_STCLK\_S\_HCLK" in CLKSEL0 constant definitions for CLK\_EnableSysTick() function to select HCLK as sysTick clock source.
7. Add new function to control systick and select systick clock source, CLK\_EnableSysTick() and CLK\_DisableSysTick().
8. Add ADC\_MeasureAVDD sample code.

### Revision 3.00.002 (Released 2015-05-07)

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1. Fixed UART, I2C, SYS definition error in header file.
2. Fixed reset entry pointer setting of IAR projects.
3. Add VECMAP relative API to FMC driver.
4. Add MCUIRQ control register to GCR\_INT\_T.
5. Add CAPIC control register to PWM\_T.
6. Add ISPSTA control register to FMC\_T.

### Revision 3.00.001 (Released 2015-04-08)

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1. First Release.

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