ARM® Cortex®-M

32-bit Microcontroller

NuMicro™ Family
Nu-Link Driver for IAR EWARM
Revision History

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Revision 3.00.004 (Released 2019-11-07)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Revised to do ADC_POWER_ON() before ADC_Open() for ADC StdDriver sample codes.
3. Added GCC projects.
4. Fixed PWM_DisableCaptureInt error.
5. Fixed CLK driver CLK_SetHCLK() bug: to enable HIRC before switch HCLK and disable HIRC if it is not enabled before.
6. Fixed CLK_EnablePLL() wrong PLL default setting value.
7. Revised MFP coding style and deleted useless files.
8. Revised multi-function pins settings:
   1. To avoid overwrite the previous correct settings.
   2. Impact on SPI sample codes.

Revision 3.00.003 (Released 2017-10-20)

1. Fixed clear Receive Line Status interrupt flag bug in UART_ClearIntFlag().
2. Fixed PLL clock source selection bug in CLK_SetCoreClock().
3. Fixed UART_SelectLINMode() clear enable bit setting bug.
4. Add CLK_SysTickLongDelay() for longer delay time than CLK_SysTickDelay().
5. Modified to ignore debug message when enabling semihost without NuLink connected.
6. Add constant define "CLK_CLKSEL0_STCLK_S_HCLK" in CLKSEL0 constant definitions for CLK_EnableSysTick() function to select HCLK as sysTick clock source.
7. Add new function to control systick and select systick clock source, CLK_EnableSysTick() and CLK_DisableSysTick().
8. Add ADC_MeasureAVDD sample code.

Revision 3.00.002 (Released 2015-05-07)

1. Fixed UART, I2C, SYS definition error in header file.
2. Fixed reset entry pointer setting of IAR projects.
3. Add VECMAP relative API to FMC driver.
4. Add MCUIRQ control register to GCR_INT_T.
5. Add CAPIC control register to PWM_T.
6. Add ISPSTA control register to FMC_T.

Revision 3.00.001 (Released 2015-04-08)

1. First Release.

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