

# ARM® Cortex®-M0 32-bit Microcontroller

## NuMicro<sup>®</sup> Family M071R/M071S Series BSP Revision History

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#### Revision 3.00.003 (Released 2024-11-11)

- 1. Update USBD HID keyboard sample code to support LED status.
- 2. Modify to pass USB-IF CV Chapter 9 & Class test.
- 3. Fix USBD\_MassStorage\_CDROM crash on Linux.
- 4. Modify HID polling interval bInterval to 10.
- 5. Add SPI\_TRIGGER\_TX\_RX\_PDMA and SPI\_DISABLE\_TX\_RX\_PDMA API in SPI driver.
- 6. Enable LVR in SYS\_PowerDown\_MinCurrent sample code to prevent power on/off fail.
- 7. Modify UART\_RS485\_Master/Slave sample code.
- 8. Add timeout handler for infinite loop.
- 9. Fix infinite loop.

#### Revision 3.00.002 (Released 2023-05-19)

- 1. Modify VerdorName of Nu\_DFU.inf.
- 2. Set FlashSelect of ISP\_DFU sample code to LDROM.
- 3. Fix SPI\_Loopback GCC sample code issue.
- 4. Fix I2C\_EEPROM and I2C\_GCMode failed in GCC.
- 5. Modify startup M071M.s in IAR/ARM file.
- 6. Modify sample code for keil compiler 6.
- 7. Add SYS\_PowerDown\_MinCurrent sample code.
- 8. Update SPI Loopback sample code.

#### **Revision 3.00.001** (Released 2020-07-10)

1. Initial Release.



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