

NuMicro[®] Family
Arm[®] Cortex[®]-M0-based Microcontroller

M0A21 CMSIS BSP
Revision History

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Revision 3.02.000 (Released 2023-11-14)

1. Enabled CRVEN when configuring CRV voltage in ACMP driver.
2. Enabled I²C pin Schmitt trigger for USCI_I2C sample code.
3. Removed ADC_1411ksps_ContinuousScanMode sample code.
4. Added BlingBling sample code.
5. Minor changes for sample code.
6. Minor bug fix.

Revision 3.01.000 (Released 2021-04-27)

1. Added GPIO_SET_DEBOUNCE_ICLKON() and GPIO_CLR_DEBOUNCE_ICLKON() macros.
2. Added USPI_TRIGGER_TX_RX_PDMA() and USPI_DISABLE_TX_RX_PDMA() macros.
3. Added ISP CAN sample code.
4. Minor changes for sample code.
5. Minor bug fix.

Revision 3.00.000 (Released 2020-07-09)

1. Primary release version.

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