			NuMaker					NuMaker-loT						NuMaker				NuMaker-IoT		
P128 PIN NA		LCD WIFI	SD Card US	SB ETM UNO	Other	mikroBUS	WIFI LoRa	UNO	CAN&485	Other		PIN NAME	LCD WIFI	SD Card USB E	TM UNO Other	mikroBUS	WIFI USB	UNO	CAN&485 BME68	
128 PB.				EADC0_CH6				PB6_NU2_A1			81 65	PF.0			ICE_DAT					ICE_D
PB.			SD0_DAT3					PB5_NU2_A2			82 66	PF.1			ICE_CLK	-				ICE_C
PB.			SD0_DAT2					PB4_NU2_A3			83 67		LCD_COM7/SEG43							
	ss										84 68		LCD_COM6/SEG42							
	B.3		SD0_DAT1					PB3_NU3_D1/UART1_TX			85 69	PC.5	LCD_COM5/SEG41					PC5_NU3_D3		
	B.2		SD0_DAT0					PB2_NU3_D0/UART1_RX			86 70	PC.4	LCD_COM4/SEG40					PC4_NU3_D2		
PC.				EPWM1_CH0			SPI3_MISO				87 71	PC.3	LCD_COM3					PC3_NU3_D4		
PC.				EPWM1_CH1			SPI3_MOSI				88 72	PC.2	LCD_COM2					PC2_NU3_D5		
7 PC.	C.10			EPWM1_CH2			SPI3_CLK				89 73	PC.1	LCD_COM1		EPWM1_CH4			PC1_NU4_I2C0_SCL		
8 PC.	C.9			EPWM1_CH3			SPI3_SS			9	90 74	PC.0	LCD_COM0		EPWM1_CH5			PC0_NU4_I2C0_SDA		
9 PB.	B.1		SD0_CLK			I2C1_SCL		PB1_NU2_A5/I2C1_SCL/UART2_TX			91 75(2)	V _{ss}								
10 PB.	B.0		SD0 CMD			I2C1 SDA		PB0_NU2_A4/I2C1_SDA/UART2_RX		9	92 75(2)	Vss								
1(2) V _S	l _{ss}										93 76(2)	V _{nn}								
1(2) V _S	/88										94 76(2)	V _{nn}								
12 V _n	'nn										95 77	PG.9	LCD_SEG21							
13 PA.	.11							PA11_NU5_USCI0_CLK			96 78		LCD_SEG20							
14 PA.	10							PA10_NU5_USCI0_DATA0_MOSI			97 79		LCD_SEG19			1				
	A.9			UART1_TXD	+						98 80	PG.12	LCD_SEG18	 				1		
	A.8			UART1 RXD			LoRa_TCXO_EN	PA9_NU5_USCI0_DATA1_MISO			99 81	PG.13	LCD_SEG17	 			 	+	 	
17 PC.				UARTI_RAD	+	+	LUKA_TCXO_EN				100 82	PG.14		 		 	 	1		
17 PC.		RST_ESP12					l	PC13_NU5_USCI0_CTL0_SS			101 83	PG.14 PG.15	LCD_SEG16							_
		IO0_ESP12					LoRa_DIO5						LCD_SEG15			 				_
19 PD.			 		UART1_TXD / CAN0TX	+	 	+	CANOTX		102 84	PD.7	LCD_SEG14	+		1	 		+	
20 PD.	0.10				UART1_RXD / CANORX				CANORX	1	103 85	PD.6	LCD_SEG13	+ + +	+				+ + + + + + + + + + + + + + + + + + + +	
V _S	SS									1	104 86	PD.5			I2C1_SCL	MBUS_INT				
V _D	DD									1	105 87	PD.4			I2C1_SDA	MBUS_ST				
	G.2 L(.CD_SEG39		I2C1_SCL						1	106 88	PD.3			LEDY				USCI0_CTL1	
22 PG.		.CD_SEG38		I2C1_SDA						1	107 89	PD.2			LEDG				USCI0_DAT1	
23 PG.		.CD_SEG37								1	108 90	PD.1	LCD_DH1						USCI0_DAT0	$_{\perp}$
24 PF.					SW2_Button		LoRa_DIO4			1	109 91		LCD_DH2							PDO
25 PF.	.10				T	1	LoRa_DIO3				110 92			 						1.2
26 PF.						1	LoRa_DIO2				111									
	F.8		 	+ +		_	LoRa_DIO1	+			12 93	PA.12		USB_VBUS			USB_VBUS			
28 PF.					+		LoPa DION				13 94	PA.13								_
29 PF.					+	+	LoRa_DIO0							USB_D-		1	USB_D-		 	
29 FF.	r.0						LoRa_NRST				95 115 96	PA.14		USB_D+		 	USB_D+			_
U(2) V _{BA}	BAT									1	15 96	PA.15		USB_ID		_	USB_ID			
0(2) V _{B,s}	BAT									1	97	PE.7	LCD_SEG12		EPWM0_CH0	UART5_TXD				
31 PF.					X32_IN					X32_IN 1	117 98	PE.6	LCD_SEG11		EPWM0_CH1	UART5_RXD				
32 PF.	F.4				X32_OUT					X32_OUT 1	118 99	PE.5	LCD_SEG10							
										1	119 100	PE.4	LCD_SEG9							
FCPT	PTST									1:	101	PE.3	LCD_SEG8							
33 PH.	H.4 L0	.CD_SEG36								1:	102		LCD_SEG7			EPWM0_CH5				
34 PH.		.CD_SEG35								1:	122 103									
35 PH.		.CD_SEG34								1	123 104	V _{pp}								
36 PH.		.CD_SEG33								1	124 105	PE.1	LCD_SEG6		SPI1_MISO	QSPI0_MISO0				
37 PF.	F.3				XT1 IN					XT1_IN 1	125 106	PE.0	LCD_SEG5		SPI1_MOSI	QSPI0_MOSI0				
38 PF.					XT1_IN XT1_OUT	1				XT1_OUT 1	126 107	PH.8	LCD_SEG4	1 1	SPI1_CLK	QSPI0_CLK		1		
39 V _S										4	127 108	PH.9	LCD_SEG3	 	SPI1_SS	QSPI0_SS				
40 ¥s	00										128 109	PH.10		+ + +		40/10_00			+ + + + + + + + + + + + + + + + + + + +	-
41 PE.	E 8	OD 05000		TRACE_D3							129 110	PH.10	LCD_SEG2	+ + +		 	 	1	+ + + + + + + + + + + + + + + + + + + +	-
		.CD_SEG32 .CD_SEG31		TRACE_D3			 			1	130 111		LCD_SEG1 LCD SEG0	+ + +		+	 	+	+ +	-+
				TRACE_D2		+	 	+		1			LUD_SEG0							
		.CD_SEG30		TRACE_D1	+		 	+		1	112(5)									
44 PE.			-	TRACE_D0	+	+	 	+			112(5)									
45 PE.	.12			TRACE_CLK			WIFI_RST				112(5)									
			\bot								134 112(5)									
46 PE.		UART4_nRTS					UART4_nRTS				135 112(5)	V _{SS}								
	C.8	UART4_nCTS					UART4_nCTS				136 113	Vsw								
48 PC.	C.7	UART4_TXD					UART4_TXD			1	114(4)	V _{DD}								
										1	138 114(4)	V _{DD}								
49 PC.	C.6	UART4_RXD					UART4_RXD				139 114(4)	V _{DD}								
											140 114(4)	V _{nn}								
50 PA.	A.7				UART0_TXD			PA7_NU3_D6			141 115(3)	LDO_CAP								
					1			1			142 115(3)	LDO CAP								
51 PA.	A.6				UART0_RXD	1		PA6_NU3_D7			143 115(3)	LDO CAP								
2(2) V _S											144 116	PB.15		USB_VBUS_EN			USB_VBUS_EN			
2(2) V _S											145 117				 	1			+ + + + + + + + + + + + + + + + + + + +	-
3(2) V _S	33									1	145 117			USB_VBUS_ST	 	 	USB_VBUS_ST	+		-
2(2) V _D	00													1000 -000		+	 	+	12C2_SCL	
3(∠) V _D	DD									1	119	PB.12		SD0_nCD					I2C2_SDA	\
54 LDO_	_CAP										148 120(2)	AV _{DD}								
55 PA.								PA5_NU4_D8		1-	120(2)	AV _{DD}								
56 PA.	A.4 L(.CD_SEG28						PA4_NU4_D9		1	150 121(3)	V _{REF}								
57 PA.	A.3 L	CD SEG27		SPI0_SS				PA3_NU4_D10/SPI0_SS		1	151 121(3)	V _{REF}								
58 PA.	A.2 L(.CD SEG26		SPI0_CLK				PA2_NU4_D13/SPI0_CLK			152 121(3)	V _{REF}								
59 PA.	A.1 1	.CD SEG25		SPI0_MISO		1		PA1_NU4_D12/SPI0_MISO			153 122(3)	AV _{SS}								
60 PA.	A.0 1.	CD SEG24		SPI0_MOSI		1		PA0_NU4_D11/SPI0_MOSI		1	154 122(3)	AV								
61 V _{DD}		.00_02024		OF IO_MOSI				I AU_NOT_DI I/OF IU_HIOOI			155 122(3)	AV								
00	JUNO														EADCO CHIL			DD44 NUE CD10		
co ==						+		1			156 123		 	+ + +	EADC0_CH11		 	PB11_NU5_GPIO	+ + + + + + + + + + + + + + + + + + + +	
62 PE.	:.14 L(CD_SEG23			+	+	 	+				PB.10		+	EADC0_CH10	EADC0_CH10	 		+ + + + + + + + + + + + + + + + + + + +	-
	:.15 L(.CD_SEG22						+				PB.9			EADC0_CH9					UAF
63 PE.			1 1	1 1	1		1	1	1	1 4	159 126	PB.8		1 1	EADC0_CH8		1 1	1	1 1	UAF
63 PE. 64 nRES											160 127				EADC0_CH7					