Arm® Cortex®-M23

32-bit Microcontroller

M253 Series CMSIS BSP
Revision History

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Revision 3.00.004 (Released 2022-1-24)

- ■ Standard driver
  - ● Improved infinite loop prevention with timeout counter and error code.
  - ● CANFD: CANFD_TransmitTxMsg changed return type.
  - ● RTC: RTC_Open added return value.
  - ● TIMER: TIMER_Delay and TIMER_ResetCounter added return value.
- ■ Sample code
  - ● EADC_Timer_Trigger: Fixed wrong timer period.
  - ● I2C and USCI_I2C samples: Enabled schmitt trigger of I2C pins.
  - ● I2C samples: Removed redundant code.
  - ● ISP_I2C, ISP_RS485, ISP_UART samples: Added GCC project.
  - ● ISP_MSC: Added.
  - ● SPI_HalfDuplex_Master, SPI_HalfDuplex_Slave: Replaced with SPI_HalfDuplex.
  - ● SPI_PDMA_LoopTest: QSPI0 replaced with USPI0.
  - ● USBD HID samples: Unified INT_IN interval.
  - ● USBD_Mass_Storage_CDROM: Fixed Linux and Mac compatibility issue.

Revision 3.00.003 (Released 2021-5-20)

- ■ system_M253.h: __HXT and __LXT added redefinition check.
- ■ Standard driver
  - ● TIMER: TIMER_CAPTURE_FROM_ACMP0, TIMER_CAPTURE_FROM_ACMP1, TIMER_CAPTURE_FROM_HXT, TIMER_CAPTURE_FROM_LXT, TIMER_CAPTURE_FROM_HIRC, TIMER_CAPTURE_FROM_LIRC, and TIMER_CAPTURE_FROM_MIRC added.
- ■ Sample code
  - ● TIMER_FreeCountingMode: Saved the first captured data.
  - ● UART_Wakeup: Provided more wake-up methods.

Revision 3.00.002 (Released 2021-2-24)

- ■ MCAN renamed to CANFD.
- ■ Sample code
  - ● ISP_CAN, SPI_SlaveFIFOModeINT, USCI_SPI_SlaveModeINT: Added.
  - ● ISP_HID, ISP_I2C, ISP_RS485, ISP_UART: Enabled GPIO and Systick clocks.
  - ● USBD VCOM samples: Fixed potential UART Tx FIFO overflow issue.

- USBD_VCOM_DaulPort, USBD_VCOM_MultiPort, USBD_VCOM_MultiPort_CMD: Fixed power-down blocked issue and wrong baud rate issue.

Revision 3.00.001 (Released 2020-12-25)

■ Initial release.

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