NuMicro® Family

Arm® Cortex®-M0-based Microcontroller

M471 CMSIS BSP
Revision History

For additional information or questions, please contact: Nuvoton Technology Corporation.

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Revision 3.03.000 (Released 2024-12-17)

1. Upgraded CMSIS from V5.1.1 to V6.1.0.
2. Supported VSCode for AC6 and GCC.
3. Updated setting order of scatter-gather mode for PDMA driver.
4. Minor changes for sample code.
5. Minor bug fix.

Revision 3.02.000 (Released 2024-10-30)

6. Added EADC_BandGapCalculateAVDD sample code.
7. Added FMC IRQHandler definition for GCC and IAR startup codes.
8. Added .uvprojx project file for Keil5.
9. Updated I^{2}C driver to support 10 bits slave address mode.
10. Updated SPI driver to support slave 3-wire mode.
11. Removed .uvproj project file for Keil4.
12. Minor changes for sample code.
13. Minor bug fix.

Revision 3.01.000 (Released 2023-05-18)

1. Added BPWM_GET_PRESCALER(), GPIO_SET_DEBOUNCE_ICLKON() and GPIO_CLR_DEBOUNCE_ICLKON() macro definitions.
2. Added EADC_SetVRef() API.
3. Added EADC_Accumulate, EADC_Average and EADC_Vref sample code.
4. Added DFMC_NonBlocking sample code.
5. Added ACMP Calibration API.
6. Added CPSCBUF[] and IFACNT[] registers definition in EPWM_T structure.
7. Added V_{REF} register definition in DAC_T structure.
8. Added V_{REF} register definition in EADC_T structure.
9. Removed INTERNAL register definition in SPI_T structure.
10. Removed ACMP_CTL_HYSTERESIS_50MV, ACMP_CTL_HYSTERESIS_30MV and ACMP_CTL_HYSTERESIS_10MV definitions.
11. Set DFMC access cycle according to HCLK frequency.
12. Enable I^{2}C pin Schmitt trigger in I^{2}C sample code.
13. Enable GPIO ICLKON bit for power consumption in SystemInit().
14. Minor changes for sample code.
15. Minor bug fix.

Revision 3.00.000 (Released 2020-08-18)

1. Preliminary release version.

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