NuMicro® Family

Arm® Cortex®-M0-based Microcontroller

# M471 CMSIS BSP
# Revision History



For additional information or questions, please contact: Nuvoton Technology Corporation.

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## Revision 3.02.000 (Released 2024-10-30)

1. Added EADC_BandGapCalculateAVDD sample code.
2. Added FMC IRQHandler definition for GCC and IAR startup codes.
3. Added .uvprojx project file for Keil5.
4. Updated $I^2C$ driver to support 10 bits slave address mode.
5. Updated SPI driver to support slave 3-wire mode.
6. Removed .uvproj project file for Keil4.
7. Minor changes for sample code.
8. Minor bug fix.

## Revision 3.01.000 (Released 2023-05-18)

1. Added BPWM_GET_PRESCALER(), GPIO_SET_DEBOUNCE_ICLKON() and GPIO_CLR_DEBOUNCE_ICLKON() macro definitions.
2. Added EADC_SetVRef() API.
3. Added EADC_Accumulate, EADC_Average and EADC_Vref sample code.
4. Added DFMC_NonBlocking sample code.
5. Added ACMP Calibration API.
6. Added CPSCBUF[] and IFACNT[] registers definition in EPWM_T structure.
7. Added $V_{REF}$ register definition in DAC_T structure.
8. Added $V_{REF}$ register definition in EADC_T structure.
9. Removed INTERNAL register definition in SPI_T structure.
10. Removed ACMP_CTL_HYSTERESIS_50MV, ACMP_CTL_HYSTERESIS_30MV and ACMP_CTL_HYSTERESIS_10MV definitions.
11. Set DFMC access cycle according to HCLK frequency.
12. Enable $I^2C$ pin Schmitt trigger in $I^2C$ sample code.
13. Enable GPIO ICLKON bit for power consumption in SystemInit().
14. Minor changes for sample code.
15. Minor bug fix.

## Revision 3.00.000 (Released 2020-08-18)

1. Preliminary release version.

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