

# M471 Series CMSIS BSP Guide

Directory Introduction for 32-bit NuMicro® Family

## Directory Information

<b>Document</b>	Driver reference guide and revision history.
<b>Library</b>	Driver header and source files.
<b>SampleCode</b>	Driver sample code.

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## 1 Document

CMSIS.html	Document of CMSIS version 5.1.1.
NuMicro M471 Series CMSIS BSP Driver Reference Guide.chm	This document describes the usage of drivers in M471 BSP.
NuMicro M471 Series CMSIS BSP Revision History.pdf	This document shows the revision history of M471 BSP.

## 2 Library

<b>CMSIS</b>	Cortex® Microcontroller Software Interface Standard (CMSIS) V5.1.1 definitions by Arm® Corp.
<b>Device</b>	CMSIS compliant device header file.
<b>StdDriver</b>	All peripheral driver header and source files.

### 3 SampleCode

<b>CortexM4</b>	Cortex®-M4 sample code.
<b>Hard_Fault_Sample</b>	<p>Show hard fault information when hard fault happened.</p> <p>The hard fault handler shows some information including program counter, which is the address where the processor is executing when the hard fault occurs. The listed file (or map file) can show what function and instruction that was.</p> <p>It also shows the Link Register (LR), which contains the return address of the last function call. It can show the status where CPU comes from to get to this point.</p>
<b>ISP</b>	Sample codes for In-System-Programming.
<b>Semihost</b>	Show how to print and get character through IDE console window.
<b>StdDriver</b>	Sample code to demonstrate the usage of M471 series MCU peripheral driver APIs.
<b>Template</b>	A project template for M471 series MCU.
<b>XOM</b>	Demonstrate how to create XOM library and use it.

## 4 SampleCode\CortexM4

BitBand	Demonstrate the usage of Cortex®-M4 Bit-band.
DSP_FFT	Demonstrate how to call ARM CMSIS DSP library to calculate FFT.
MPU	Demonstrate the usage of Cortex®-M4 MPU.

## 5 SampleCode\ISP

ISP_I2C	In-System-Programming Sample code through an I <sup>2</sup> C interface.
ISP_RS485	In-System-Programming Sample code through a RS485 interface.
ISP_SPI	In-System-Programming Sample code through a SPI interface.
ISP_UART	In-System-Programming Sample code through a UART interface.

## 6 SampleCode\StdDriver

### System Manager (SYS)

<b>SYS_BODWakeup</b>	Demonstrate how to wake up system from Power-down mode by brown-out detector interrupt.
<b>SYS_PLLClockOutput</b>	Change system clock to different PLL frequency and output system clock to CLK0 pin.
<b>SYS_PowerDown_MinCurrent</b>	Demonstrate how to minimize power consumption when entering power down mode.
<b>SYS_TrimHIRC</b>	Demonstrate how to use LXT to trim HIRC.

### Clock Controller (CLK)

<b>CLK_ClockDetector</b>	Demonstrate the usage of clock fail detector and clock frequency range detector function.
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### Flash Memory Controller (FMC)

<b>FMC_CRC32</b>	Demonstrate how to use FMC CRC32 ISP command to calculate the CRC32 checksum of APROM and LDROM.
<b>FMC_Dual_Bank</b>	Demonstrate how dual processes work in dual bank Flash architecture.
<b>FMC_DualBankFwUpdate</b>	Implement a firmware update mechanism based on dual bank Flash architecture.
<b>FMC_ExecInSRAM</b>	Implement a code and execute in SRAM to program embedded Flash.
<b>FMC_IAP</b>	Demonstrate FMC IAP boot mode and show how to use vector remap function. LDROM image is embedded in APROM image and will be programmed to LDROM Flash at run-time. This sample also shows how to branch between APROM and LDROM.
<b>FMC_MultiWordProgram</b>	Show how to use FMC multi-word program ISP command to program APROM 0x10000~0x20000 area.



<b>FMC_ReadAllOne</b>	Demonstrate how to use FMC Read-All-One ISP command to verify if APROM/LDROM pages are all 0xFFFFFFFF.
<b>FMC_RW</b>	Show FMC read Flash IDs, erase, read, and write functions.

## Data Flash Memory Controller (DFMC)

<b>DFMC_CRC32</b>	Demonstrate how to use DFMC CRC32 ISP command to calculate the CRC32 checksum of Data Flash.
<b>DFMC_NonBlocking</b>	Demonstrate how to program Data Flash in non-blocking mode.
<b>DFMC_ReadAllOne</b>	Demonstrate how to use DFMC Read-All-One ISP command to verify if Data Flash pages are all 0xFFFFFFFF.
<b>DFMC_RW</b>	Show DFMC read Flash IDs, erase, read, and write functions.

## General Purpose I/O (GPIO)

<b>GPIO_EINTAndDebounce</b>	Show the usage of GPIO external interrupt function and de-bounce function.
<b>GPIO_INT</b>	Show the usage of GPIO interrupt function.
<b>GPIO_OutputInput</b>	Show how to set GPIO pin mode and use pin data input and output control.
<b>GPIO_PowerDown</b>	Show how to wake up system from Power-down mode by GPIO interrupt.

## I<sup>2</sup>C Serial Interface Controller (I<sup>2</sup>C)

<b>I2C_EEPROM</b>	Show how to use I <sup>2</sup> C interface to access EEPROM.
<b>I2C_GCMode_Master</b>	Show how a master uses I <sup>2</sup> C address 0x0 to write data to a slave. This sample code needs to work with I2C_GCMode_Slave.

<b>I2C_GCMode_Slave</b>	Show how a slave receives data from a master in GC (General Call) mode. This sample code needs to work with I2C_GCMode_Master.
<b>I2C_Loopback</b>	Demonstrate how to set I <sup>2</sup> C Master mode and Slave Mode, and show how a master accesses a slave on a chip.
<b>I2C_Master</b>	Show how a master accesses a slave. This sample code needs to work with I2C_Slave.
<b>I2C_MultiBytes_Master</b>	Show how to set I <sup>2</sup> C Multi bytes API Read and Write data to Slave. This sample code needs to work with I2C_Slave.
<b>I2C_PDMA_TRX</b>	Demonstrate I <sup>2</sup> C PDMA mode and need to connect I2C0 (master) and I2C1 (slave).
<b>I2C_SingleByte_Master</b>	Show how to use I <sup>2</sup> C Single byte API Read and Write data to Slave. This sample code needs to work with I2C_Slave.
<b>I2C_Slave</b>	Demonstrate how to set I <sup>2</sup> C in Slave mode to receive 256 bytes data from a master. This sample code needs to work with I2C_Master.
<b>I2C_SMBus</b>	Show how to control SMBus interface and use SMBus protocol between Host and Slave.
<b>I2C_Wakeup_Slave</b>	Show how to wake up MCU from Power-down mode via the I <sup>2</sup> C interface. This sample code needs to work with I2C_Master.

## PDMA Controller (PDMA)

<b>PDMA_BasicMode</b>	Use PDMA channel 1 to transfer data from memory to memory.
<b>PDMA_ScatterGather</b>	Use PDMA channel 1 to transfer data from memory to memory by scatter-gather mode.
<b>PDMA_ScatterGather_PingPongBuffer</b>	Use PDMA to implement Ping-Pong buffer by scatter-gather mode (memory to memory).

## Real Time Clock (RTC)

<b>RTC_Alarm_Test</b>	Demonstrate the RTC alarm function. It sets an alarm 10 seconds after execution.
<b>RTC_Alarm_Wakeup</b>	Use RTC alarm interrupt event to wake up system.
<b>RTC_Time_Display</b>	Demonstrate the RTC function and displays current time to the UART console.

## Timer Controller (TIMER)

<b>TIMER_ACMPTrigger</b>	Use ACMP to trigger Timer0 counter reset mode.
<b>TIMER_CaptureCounter</b>	Show how to use the Timer capture function to capture Timer counter value.
<b>TIMER_Delay</b>	Demonstrate the usage of TIMER_Delay API to generate a 1 second delay.
<b>TIMER_EventCounter</b>	Use TM0 pin to demonstrate Timer event counter function.
<b>TIMER_FreeCountingMode</b>	Use the timer TM0_EXT pin to demonstrate timer free counting mode function, and display the measured input frequency to UART console.
<b>TIMER_InterTimerTriggerMode</b>	Use the timer TM0 pin to demonstrate inter timer trigger mode function, and display the measured input frequency to UART console.
<b>TIMER_Periodic</b>	Use the Timer periodic mode to generate Timer interrupt every 1 second.
<b>TIMER_PeriodicINT</b>	Implement Timer counting in periodic mode.
<b>TIMER_PWM_ChangeDuty</b>	Change duty cycle and period of output waveform in PWM up count type.
<b>TIMER_PWM_OutputWaveform</b>	Demonstrate output different duty waveform in Timer0~3 PWM.
<b>TIMER_TimeoutWakeup</b>	Use timer to wake up system from Power-down mode periodically.

TIMER_ToggleOut	Demonstrate the Timer0 toggle out function on TM0 pin.
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## Watchdog Timer (WDT)

WDT_TimeoutWakeupAndReset	Implement WDT time-out interrupt event to wake up system and generate time-out reset system event while WDT time-out reset delay period expired.
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## Window Watchdog Timer (WWDT)

WWDT_ReloadCounter	Show how to reload the WWDT counter value.
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## Basic PWM Generator and Capture Timer (BPWM)

BPWM_Capture	Capture the BPWM0 Channel 2 waveform by BPWM1 Channel 0.
BPWM_DoubleBuffer	Change duty cycle and period of output waveform by PWM Double Buffer function.
BPWM_SwitchDuty	Change duty cycle of output waveform by configured period.
BPWM_OutputWaveform	Demonstrate how to use BPWM counter output waveform.
BPWM_SyncStart	Demonstrate how to use BPWM counter synchronous start function.

## Enhanced PWM Generator and Capture Timer (EPWM)

EPWM_AccumulatorINT_TriggerPDMA	Demonstrate how to use EPWM accumulator interrupt trigger PDMA.
EPWM_Brake	Demonstrate how to use EPWM brake function.
EPWM_Capture	Capture the EPWM1 Channel 0 waveform by EPWM1 Channel 2.
EPWM_DeadTime	Demonstrate how to use EPWM Dead Time function.

<b>EPWM_DoubleBuffer</b>	Change duty cycle and period of output waveform by EPWM Double Buffer function.
<b>EPWM_OutputWaveform</b>	Demonstrate how to use EPWM counter output waveform.
<b>EPWM_PDMA_Capture</b>	Capture the EPWM1 Channel 0 waveform by EPWM1 Channel 2, and use PDMA to transfer captured data.
<b>EPWM_SwitchDuty</b>	Change duty cycle of output waveform by configured period.
<b>EPWM_SyncStart</b>	Demonstrate how to use EPWM counter synchronous start function.

## Serial Peripheral Interface (SPI)

<b>SPI_Loopback</b>	SPI read/write demo connecting SPI MISO and MOSI pins.
<b>SPI_MasterFIFOmode</b>	Configure SPI as Master mode and demonstrate how to communicate with an off-chip SPI slave device with FIFO mode. This sample code needs to work with SPI_SlaveFIFOmode.
<b>SPI_PDMA_LoopTest</b>	SPI read/write demo in PDMA mode. Connecting SPI MISO and MOSI pins. Both TX PDMA function and RX PDMA function will be enabled.
<b>SPI_SlaveFIFOmode</b>	Configure SPI as Slave mode and demonstrate how to communicate with an off-chip SPI master device with FIFO mode. This sample code needs to work with SPI_MasterFIFOmode.
<b>SPII2S_Master</b>	Configure SPI in I <sup>2</sup> S Master mode and demonstrate how I <sup>2</sup> S works in Master mode.
<b>SPII2S_PDMA_NAU8822</b>	An I <sup>2</sup> S demo with PDMA function connected to audio codec NAU8822.
<b>SPII2S_PDMA_Play</b>	An I <sup>2</sup> S demo for playing data and demonstrating how I <sup>2</sup> S works with PDMA.

<b>SPII2S_PDMA_PlayRecord</b>	An I <sup>2</sup> S demo for playing and recording data with PDMA function.
<b>SPII2S_PDMA_Record</b>	An I <sup>2</sup> S demo for recording data and demonstrating how I <sup>2</sup> S works with PDMA.
<b>SPII2S_Slave</b>	Configure SPI as I <sup>2</sup> S Slave mode and demonstrate how I <sup>2</sup> S works in Slave mode. This sample code needs to work with I2S_Master.

## UART Interface Controller (UART)

<b>UART_AutoBaudRate</b>	Show how to use auto baud rate detection function.
<b>UART_AutoFlow</b>	Transmit and receive data using auto flow control.
<b>UART_IrDA</b>	Transmit and receive UART data in UART IrDA mode.
<b>UART_LIN</b>	Demonstrate how to send data to LIN bus.
<b>UART_PDMA</b>	Demonstrate UART transmit and receive function with PDMA.
<b>UART_RS485</b>	Transmit and receive data in UART RS485 mode.
<b>UART_SingleWire</b>	Transmit and receive data in UART single-wire mode.
<b>UART_TxRxFunction</b>	Transmit and receive data from PC terminal through RS232 interface.
<b>UART_Wakeup</b>	Show how to wake up system from Power-down mode by UART interrupt.

## Pseudo Random Number Generator (PRNG)

<b>PRNG_KeyGeneration</b>	Generate random numbers using PRNG.
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## Customize IR Receiver (CIR)

<b>CIR_DataMatchWakeup</b>	Use CIR to wake up system from Power-down mode while first 8 bits data matched.
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CIR_TPWM_TRX	Demonstrate how to use CIR to receive data from an IR LED that was driven by timer PWM.
CIR_TV_Remote_Controller	Demonstrate how to use CIR APIs to convert the output signal of an IR receiver.

## CRC Controller (CRC)

CRC_CCITT	Implement CRC in CRC-CCITT mode and get the CRC checksum result.
CRC_CRC32_PDMA	Implement CRC in CRC-32 mode and get the CRC checksum result.
CRC_CRC8	Implement CRC in CRC-8 mode and get the CRC checksum result.

## Enhanced Analog-to-Digital Converter (EADC)

EADC_Accumulate	Demonstrate how to get accumulate conversion results.
EADC_ADINT_Trigger	Use ADINT interrupt to do the EADC continuous scan conversion.
EADC_Average	Demonstrate how to get average conversion results.
EADC_BandGap	Convert Band-gap (channel 24) and print conversion result.
EADC_EPWM_Trigger	Demonstrate how to trigger EADC by EPWM.
EADC_PDMA_EPWM_Trigger	Demonstrate how to trigger EADC by EPWM and transfer conversion data by PDMA.
EADC_Pending_Priority	Demonstrate how to trigger multiple sample modules and got conversion results in order of priority.
EADC_ResultMonitor	Monitor the conversion result of channel 2 by the digital compare function.
EADC_SwTrg_Trigger	Trigger EADC by writing EADC software trigger register.
EADC_TempSensor	Convert temperature sensor (channel 25) and print

	conversion result.
<b>EADC_Timer_Trigger</b>	Show how to trigger EADC by Timer.
<b>EADC_Vref</b>	Demonstrate how to select EADC reference voltage source and trigger delay time.

## Digital-to-Analog Converter (DAC)

<b>DAC_PDMA_TimerTrigger</b>	Show how Timer triggers DAC to fetch data with PDMA and convert sine wave outputs.
<b>DAC_SoftwareTrigger</b>	Demonstrate how software triggers DAC to convert sine wave outputs.
<b>DAC_TimerTrigger</b>	Demonstrate how Timer triggers DAC to convert sine wave outputs.

## Analog Comparator Controller (ACMP)

<b>ACMP_ComapreDAC</b>	Demonstrate how ACMP compares DAC output with ACMP1_N1 value.
<b>ACMP_CompareVBG</b>	Demonstrate analog comparator (ACMP) comparison by comparing ACMP1_P1 input and VBG voltage and shows the result on UART console.
<b>ACMP_Wakeup</b>	Use ACMP to wake up system from Power-down mode while comparator output changes.
<b>ACMP_WindowCompare</b>	Show how to monitor ACMP input with window compare function.
<b>ACMP_WindowLatch</b>	Demonstrate how to use ACMP window latch mode.



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