

# N9H20 Non-OS BSP User Manual

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## 1 Introduction to N9H20 Non-OS BSP

This BSP supports Nuvoton N9H20 family processors. The N9H20 series targeted for general purpose 32-bit microcontroller embeds an outstanding CPU core ARM926EJ-S, a RISC processor designed by Advanced RISC Machines Ltd., runs up to 200 MHz, with 8 KB I-cache, 8 KB D-cache and MMU, 8KB embedded SRAM and 16 KB IBR (Internal Boot ROM) for booting from NAND, SD and SPI FLASH.

The N9H20 series integrates with JPEG codec, BitBLT accelerator, & USB2.0 HS Device, 32-channel SPU (Sound Processing Unit), ADC, DAC, & TV encoder, for meeting various kinds of application needs while saving the BOM cost.

### 1.1 Develop Environment

Keil IDE is used as Non-OS BSP develop environment, and use J-Link ICE for debug. The IDE does not belong to the content of this document. Please refer to official Keil website <http://www.keil.com/> for the user manual of Keil IDE.

N9H20 supports J-TAG debug interface. Users could use this interface to download programs to DRAM and debug. It is recommended to booting on recovery mode for ICE debugging.

## 1.2 Eclipse Develop Environment

This section introduces the installation steps of Eclipse develop environment. First download Eclipse IDE for C/C++ Developers Tool from Eclipse official website <https://www.eclipse.org/downloads/>, select proper version according to your operating system. Since Eclipse is a Java based application, please download JRE from Java website and install it.

The cross compile - GNU ARM Embedded Toolchain can be downloaded from <https://gnu-mcu-eclipse.github.io/plugins/install/>. After installed the software packages mentioned above, execute Eclipse and select Help -> Eclipse Marketplace.

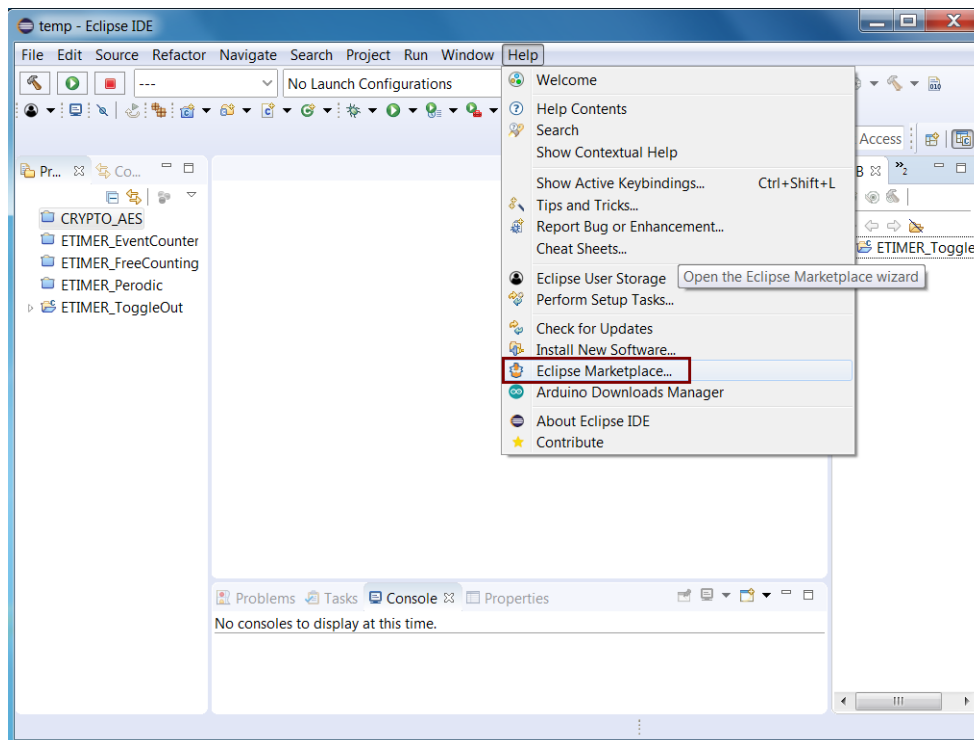


Figure 1-1 Select Eclipse Marketplace

Input gnu mcu exlipse in Find field, and then the search result will be shown as Figure 1-2. Select latest version and click Install button to install the required plug-in.

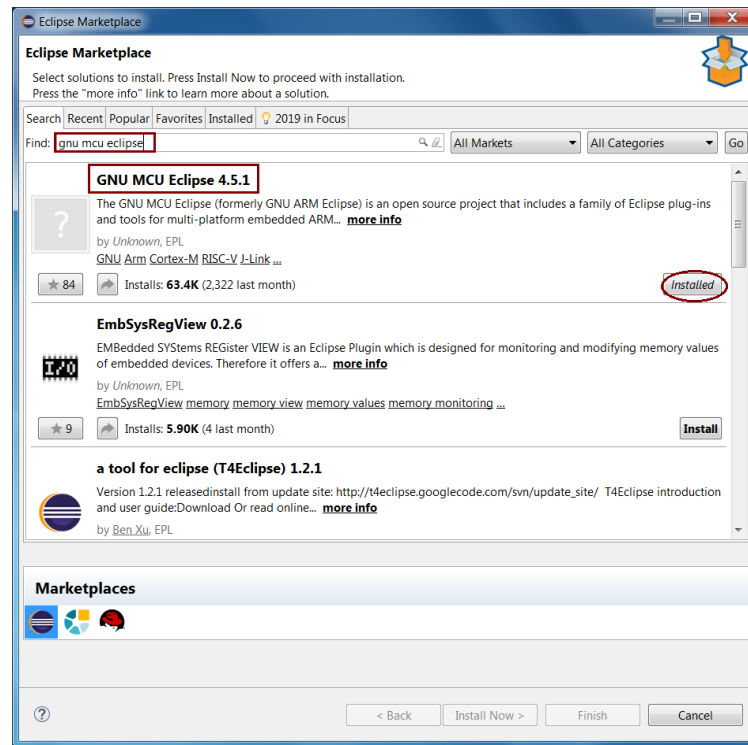


Figure 1-2 Install Plug-in

Click Help -> Install New Software to install CDT to support C/C++ development.

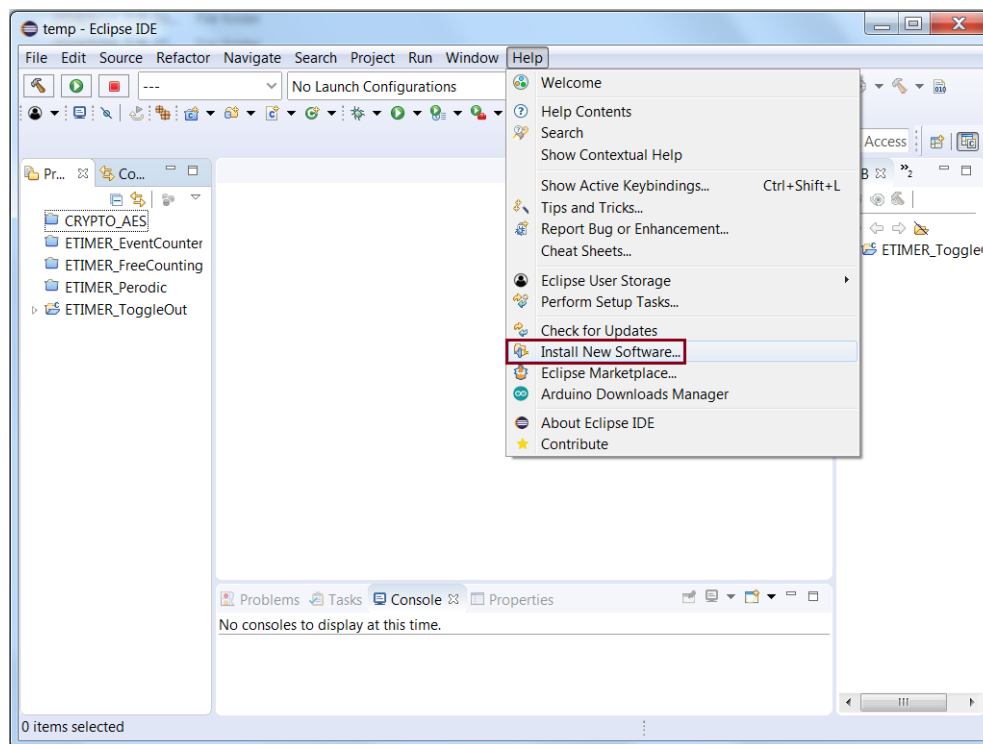


Figure 1-3 Install New Software

Input CDT in Work with field.

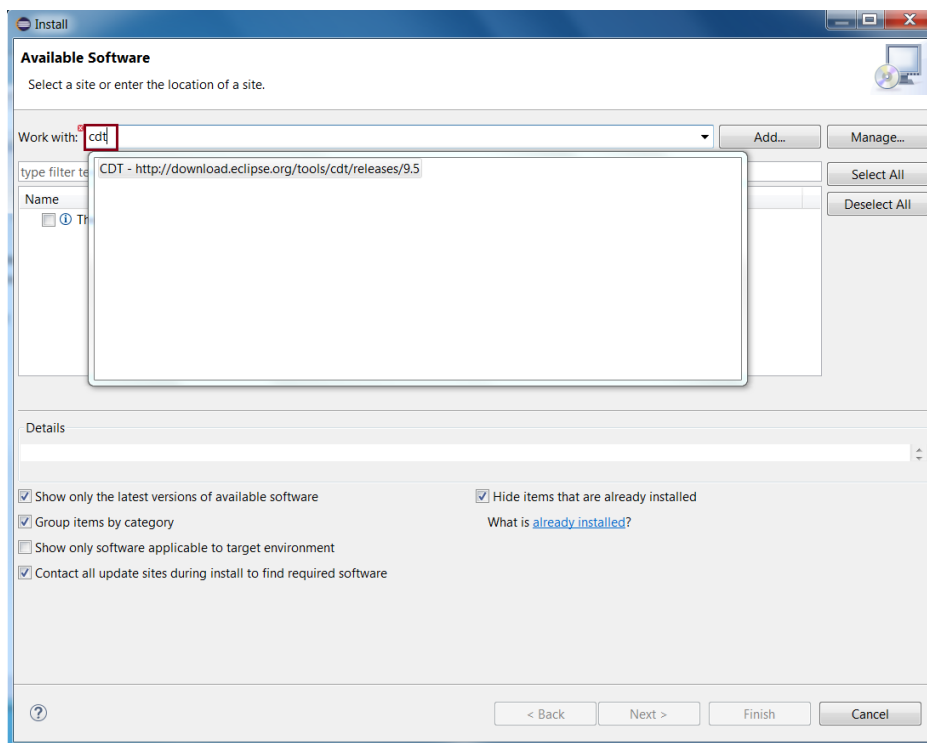


Figure 1-4 Search for CDT

Select CDT Main Features and CDT Optional Features as shown in Figure 1-5. User can also select other packages if necessary.

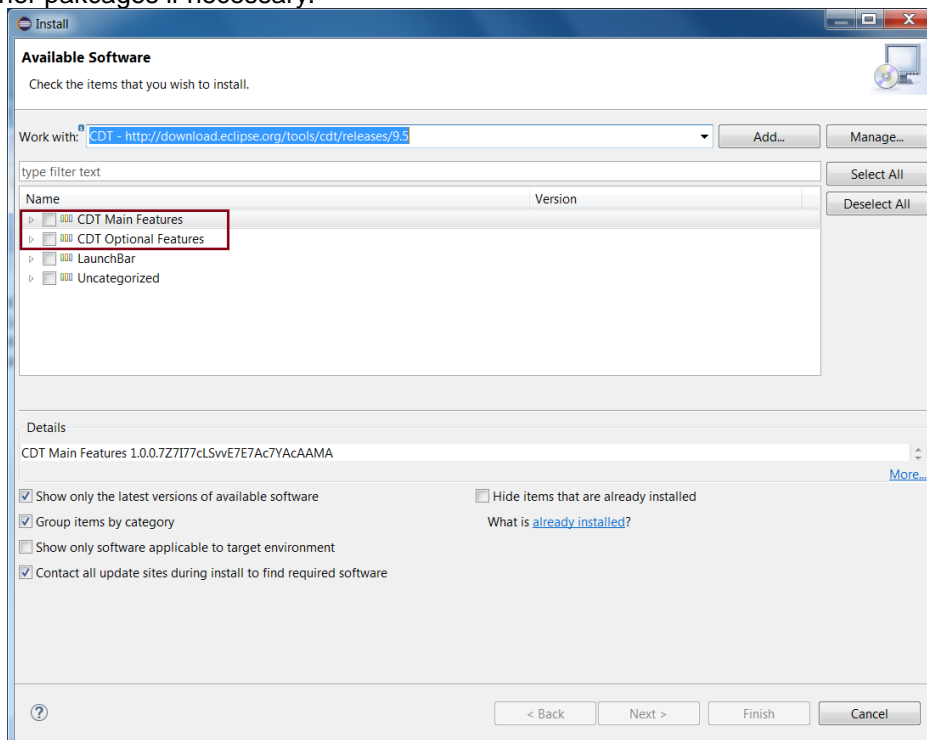


Figure 1-5 Select CDT

After installed CDT, re-start Eclipse to import Eclipse project. Eclipse supports debugging using J-Link ICE. Download and install J-Link plug-in from the website <http://gnuarmclipse.github.io/plugins/install/> before start debugging. After installation, set J-Link path in Preference->MCU-> Global SEGGER J-Link, and them press Apply button.

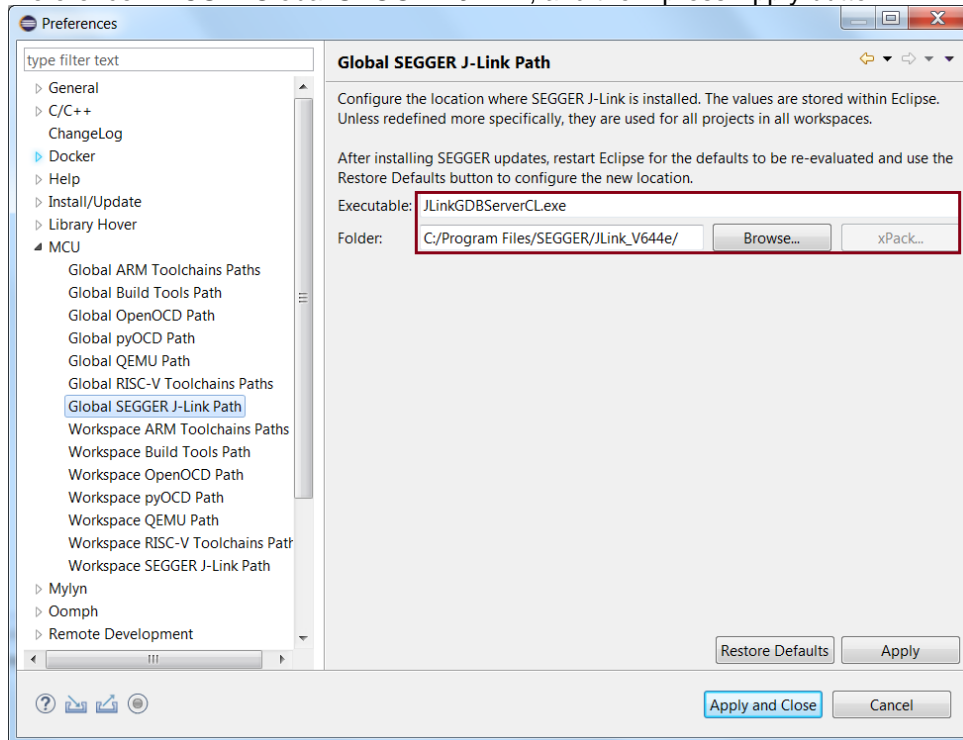


Figure 1-6 Global SEGGER J-Link Path Setting

The next step is to set GDB SEGGER J-Link Debugging options. Click Run-> Debug Configurations and then expand GDB SEGGER J-Link Debugging configuration.

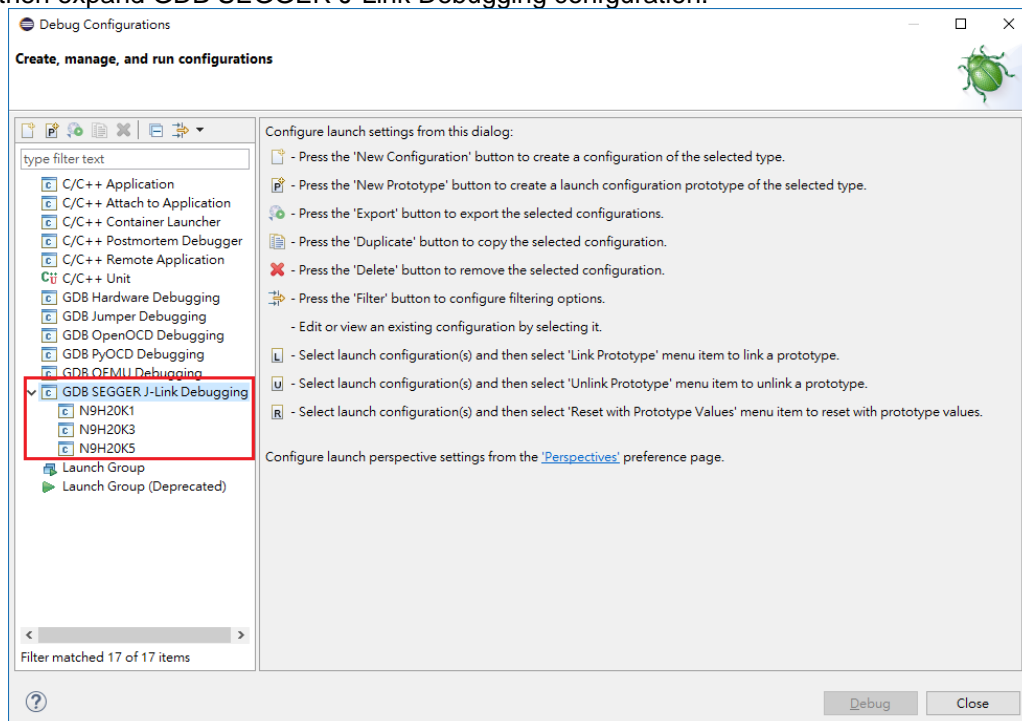


Figure 1-7 GDB SEGGER J-Link Debug

Select corresponding configuration which match your device. Goto Debugger tab, this configure is as Figure 1-8.

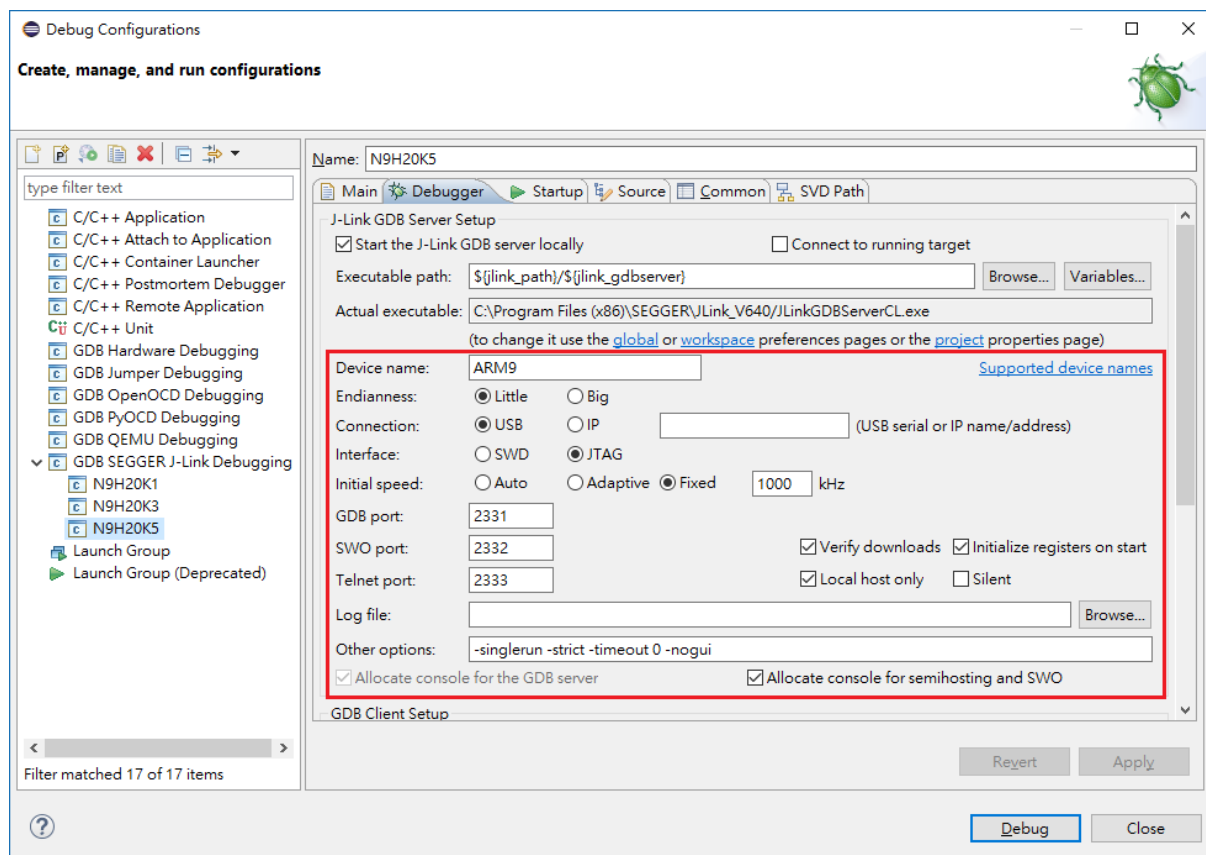


Figure 1-8 J-Link Debugger Setting



Goto Startup tab, this configure is as Figure 1-9.

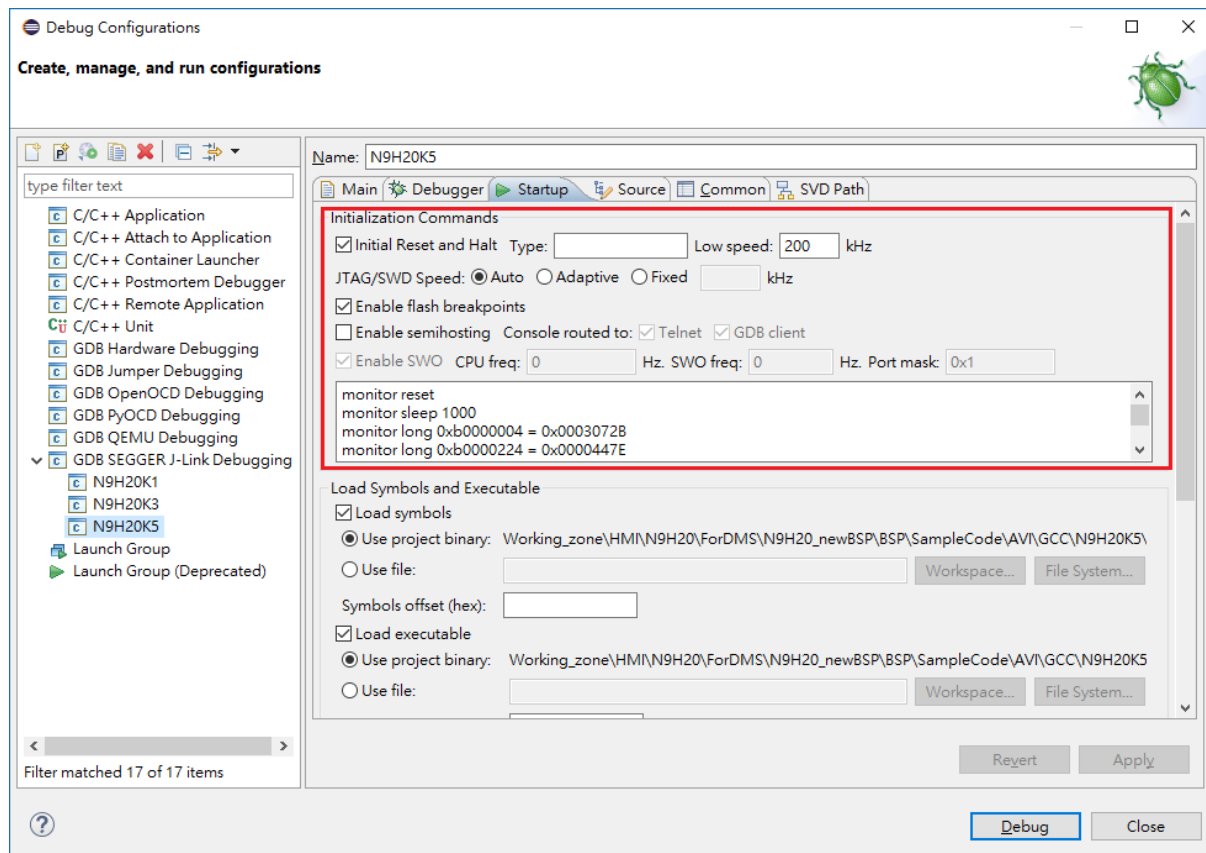


Figure 1-9 J-Link Startup Setting

After complete the setting, click Debug button to start debug with J-Link.

## 2 BSP Content

### 2.1 Non-OS BSP directory structure

Non-OS BSP contains four directories. The content of each directory listed in the table below.

Directory Name	Content
BSP	Folldr contains Non-OS driver, third party software and sample applications.
Documents	BSP related documents
Loader	Contain source code and binary file for different loader in booting path.
Tools	Tools include PC tools and the Mass production tools.

### 2.2 BSP content

The BSP directory shows following content.

Directory Name	Content
Driver	N9H20 peripheral drivers. Please refer to N9H20 Non-OS Library Reference Guide.pdf under Documents directory for the usage of driver APIs.
Library	N9H20 Keil libraries, including IP, AVI, GNAND, NVTFAT and USB Hos. The IP library source code can be found in Driver\Source folder.
Library_GCC	N9H20 GCC libraries, including IP, AVI, GNAND, NVTFAT and USB Hos. The IP library source code can be found in Driver\Source folder.
SampleCode	Driver sample application.
Script	Link script and debug initialization file for Keil.
ThirdParty	Third party software. Including emWin and FreeRTOS.

The folders under Script are for N9H20 different part number IC as below.

Directory Name	Description
N9H20K1	<ul style="list-style-type: none"> <li>N9H20K1.ini is for IP usage example which execution address at 0x0.</li> <li>N9H20K1_loader.ini is for NandLoader, SDLoader and SPILoader under LOADER folder which execution address is at 0x180000.</li> <li>N9H20K1_NVTloader.ini is for NVT LOADER which execution address is at 0x60000.</li> </ul>
N9H20K3	<ul style="list-style-type: none"> <li>N9H20K3.ini is for IP usage example which execution address at 0x0.</li> <li>N9H20K3_loader.ini is for NandLoader, SDLoader and SPILoader under LOADER folder which execution address is at 0x700000.</li> <li>N9H20K3_NVTloader.ini is for NVT LOADER which execution address is at 0x600000.</li> </ul>
N9H20K5	<ul style="list-style-type: none"> <li>N9H20K5.ini is for IP usage example which execution address at 0x0.</li> <li>N9H20K5_loader.ini is for NandLoader, SDLoader and SPILoader under LOADER folder which execution address is at 0x900000.</li> <li>N9H20K5_NVTloader.ini is for NVT LOADER which execution address is at 0x800000.</li> </ul>

### 2.3 Loader content

The booting sequence is IBR → Loader → NVTLoader(option). These folders provide the

reference sample code for it. In most cases, this code is unnecessary to modify it.

Directory Name	Content
Binary	Contains the pre-build binary file for NandLoader, SDLoader, SPILoader and NVTLoader.
NANDLoader	Source code of NandLoader. Please refer to N9H20 NAND Loader Reference Guide.pdf under Documents directory for the usage.
NVTLoader	Source code of NVTLoader. Please refer to N9H20 NVTLoader Reference Guide.pdf under Documents directory for the usage.
SDLoader	Source code of SDLoader. Please refer to N9H20 SD Loader Reference Guide.pdf under Documents directory for the usage.
SPILoader	Source code of SPI Loader. Please refer to N9H20 SPI Loader Reference Guide.pdf under Documents directory for the usage.
SPILoader_gzip	Source code of SPI Loader with gzip. Please refer to N9H20 SPI Loader Reference Guide.pdf under Documents directory for the usage.

## 2.4 Tools content

This directory contains PC\_tools and MassProduction\_tools

**PC\_tools contain:**

Directory Name	Description
AutoWriter V3.xx.xxx_N9H20Kx	Please refer the AutoWriter User Guide.pdf for detail.
TurboWriter V2.xx.xxx_N9H20Kx	Please refer the TurboWriter Tool User Guide.pdf for detail.

**MassProduction\_tool contain:**

User must prepare the content which want to program on storage on SDx in advance. This tool can copy the prepared data to NAND/SD/SPI through SD booting

Directory Name	Description
NandWriter	Please refer NandWriter User Guide.pdf under NandWriter\Doc folder for detail.
NRomMaker	Please refer NRomMaker Tool User Guide.pdf under NRomMaker folder for detail.
SDWriter	Please refer SDWriter User Guide.pdf under SDWriter\Doc folder for detail.
SPIWriter	Please refer SPIWriter User Guide.pdf under SPIWriter\Doc folder for detail.

### 3 Revision History

Version	Date	Description
1.00.000	May. 4, 2018	Initial release
1.00.002	Aug. 17, 2018	Minor update
1.01.000	Sep. 21, 2018	BSP folder structure change
1.02.000	Aug. 29, 2019	Added Eclipse IDE description
1.03.000	Sep. 15, 2020	Added folder for Library_GCC and NRomMaker

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