

N9H26
NAND Loader
Reference Guide
V1.0

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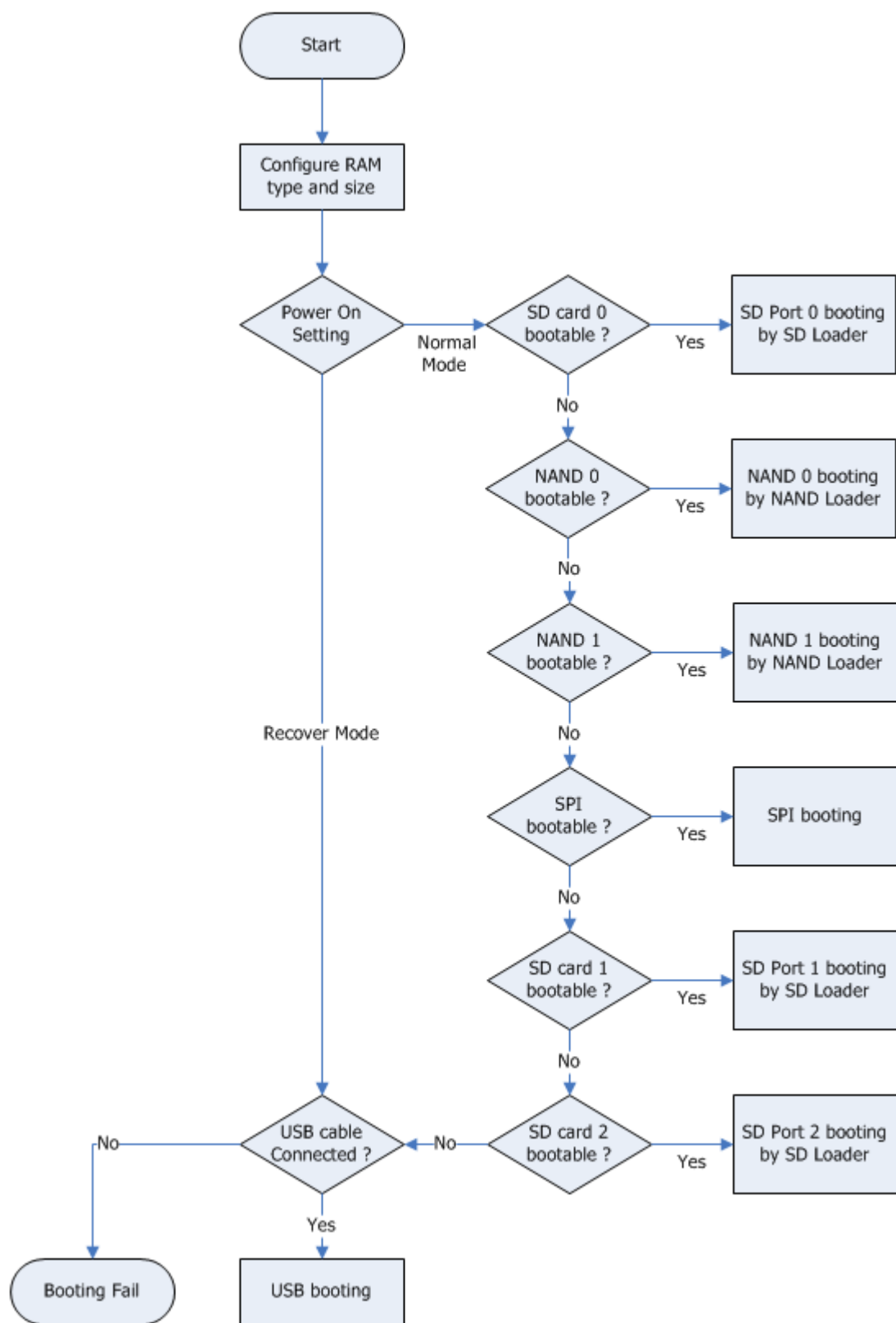
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1. General Description

N9H26 Non-OS library consists of a sets of libraries. These libraries are built to access those on-chip functions such as VPOST, SPU, SIC, USBH, USBD, GPIO, I2C, SPI and UART, as well as File System (NVT FAT), USB MassStorage devices (UMAS) and NAND Flash devices (GNAND). This document describes the basic function of NAND Loader. With this introduction, user can quickly understand the NAND Loader on N9H26 microprocessor.

2. NAND Loader Overview

N9H26 built-in 16K bytes IBR (Internal Booting ROM) where stored the boot loader to initial chip basically when power on, and then try to find out the next stage boot loader from different type of storage. It could be SD card, NAND, SPI Flash, or USB storage. The search sequence by IBR as below



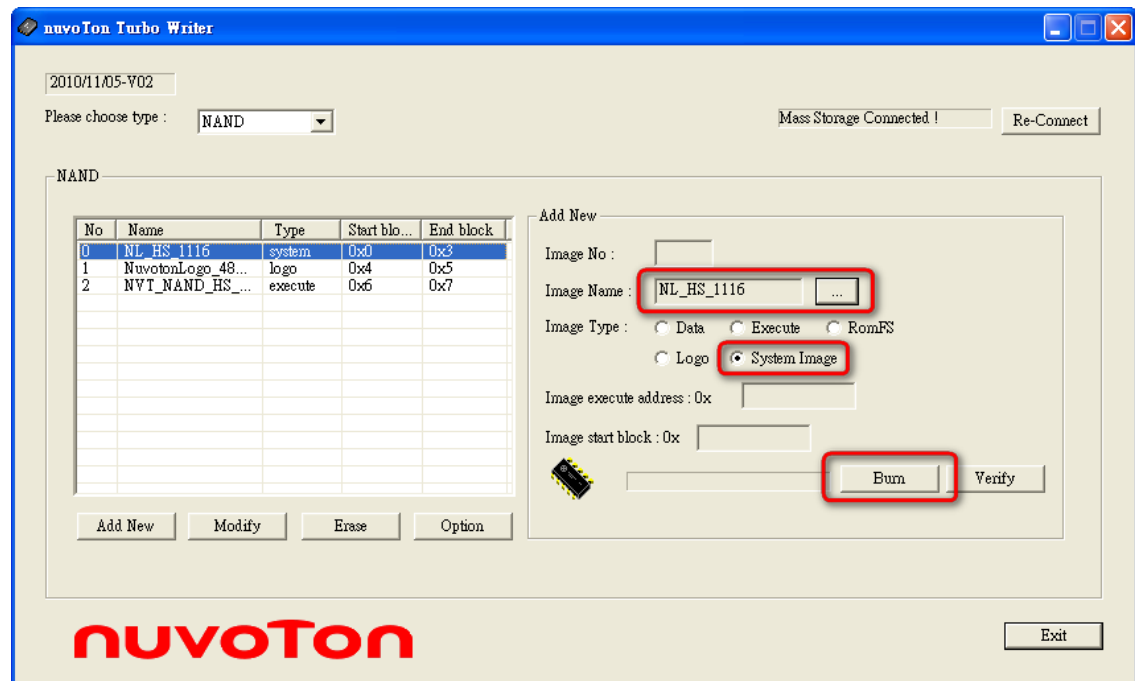
The boot loader in IBR will hand over the chip controlling to NAND Loader if SD card 0 is invalid. NAND Loader is a firmware stored at NAND chip block 0 ~ 3.

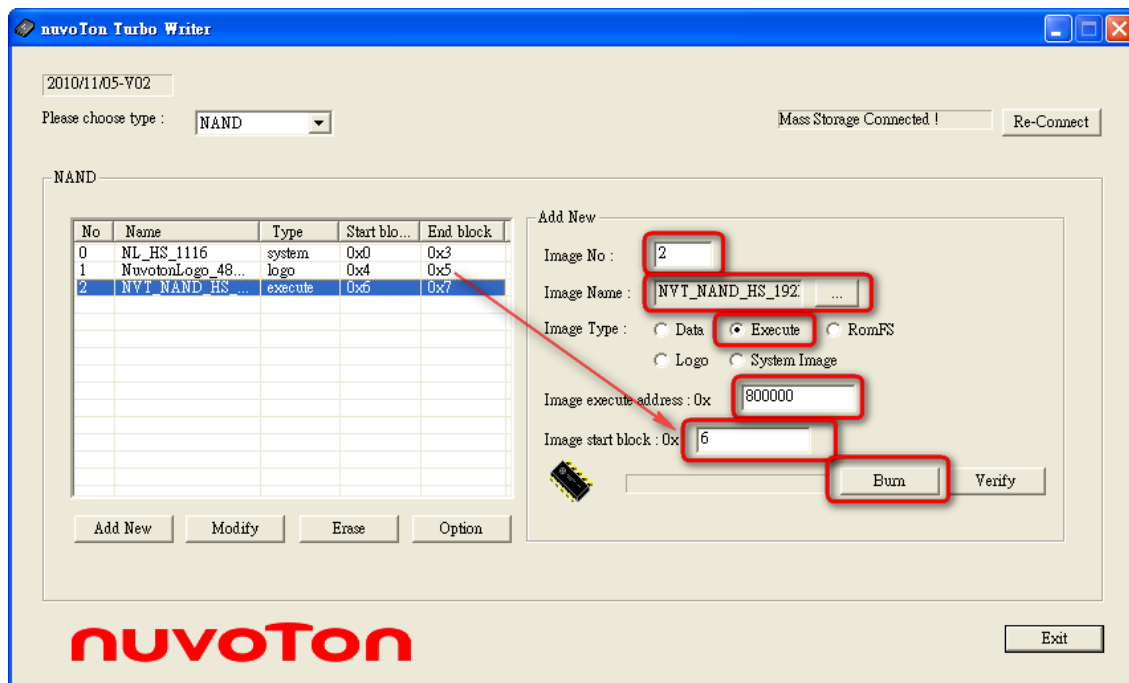
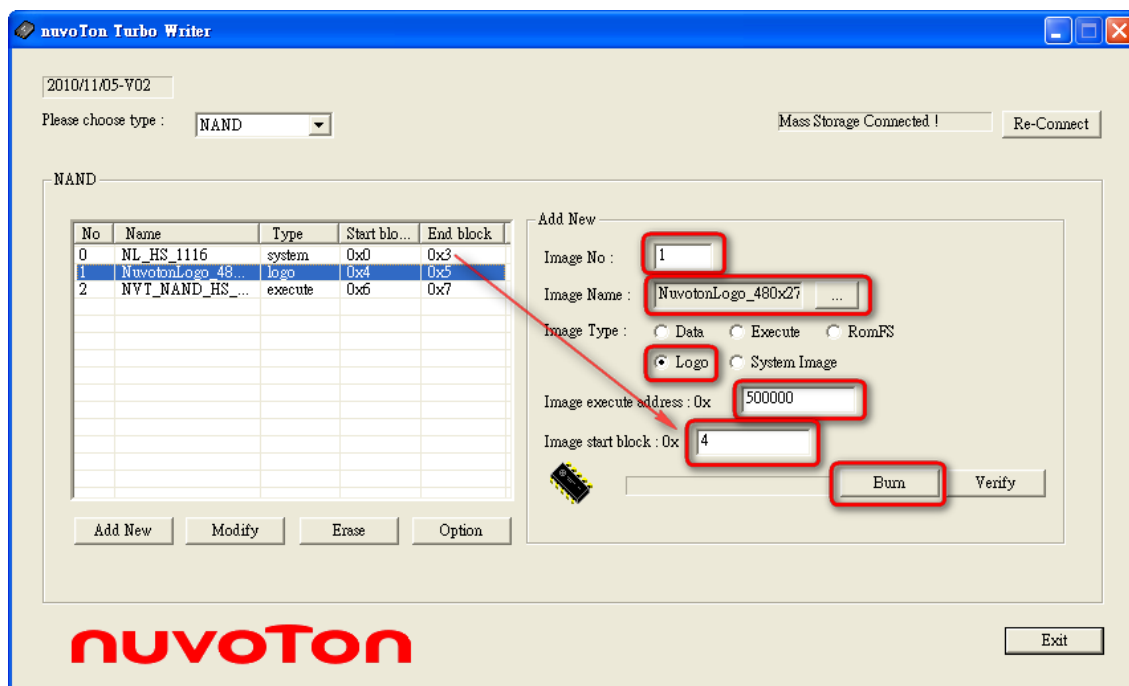
The NAND Loader has the following features:

- Initial more modules such as SPU, VPOST and so on
- Check, load, and display Logo image if it existed at NAND
- Check and load next firmware if it existed at NAND
- Hand over chip controlling to next firmware. Normally, it should be NVT Loader

All image files have to located at top of NAND with correct parameters. You can add or modify them by TurboWrite utility. Please follow the configuration below to make sure the NAND Loader work fine.

	NAND Loader	Logo Image	Next Firmware
Image No.	0	1	2
Image Name	<i>File name for NAND Loader on host</i>	<i>File name for Logo image on host</i>	<i>File name for next firmware on host</i>
Image Type	System Image	Logo	Execute
Image execute address	<i>Default value</i>	500000	800000
Image start block	<i>Default value</i>	<i>Behind NAND Loader</i>	<i>Behind Logo Image</i>





3. Revision History

Version	Date	Description
V1.0	May,2018	● Created

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