

ARM[®] Cortex[®]-M0
32-bit Microcontroller

NuMicro[®] Family
M0519 Series BSP
Revision History

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Revision 3.00.002 (Released 2017-10-23)

1. Fixed UART_SelectLINMode() clear enable bit setting bug.
2. Fixed UART baud rate caculation bug in UART_Open(), UART_SetLine_Config() and UART_SelectIrDAMode().
3. Fixed GPIO_ENABLE_DOUT_MASK() and GPIO_DISABLE_DOUT_MASK() definitions.
4. Fixed bug in CLK_SetCoreClock().
5. Fixed clear RS-485 address byte detection flag bug.
6. Fixed clear flag bug to clear one flag in one time in UART_ClearIntFlag().
7. Fixed clear Receive Line Status interrupt flag bug in UART_ClearIntFlag().
8. Fixed PLL clock source selection bug in CLK_SetCoreClock().
9. Fixed CLK_SysTickDelay() bug.
10. Added more wake up sample codes.
11. Added CLK_SysTickLongDelay() for long delay.
12. Modified to ignore debug message when enabling semihost without NuLink connected.
13. Modified CLK_EnableModuleClock() and CLK_DisableModuleClock() for
14. Modified void CLK_WaitClockReady(uint32_t u32ClkMask) to uint32_t CLK_WaitClockReady(uint32_t u32ClkMask).
15. Removed CLKO divide 1 function in CLK_EnableCKO() for NUC029xAN does not have this function.
16. Added "ISP_MODULE", "EBI_MODULE" and "HDIV_MODULE" constant define in MODULE constant definitions for CLK_EnableModuleClock() and CLK_DisableModuleClock().
17. Added constant define "CLK_CLKSEL0_STCLK_S_HCLK" in CLKSEL0 constant definitions for CLK_EnableSysTick().
18. Added new function to control systick and select systick clock source, CLK_EnableSysTick() and CLK_DisableSysTick().
19. Added missing macro, SYS_IS_LVR_RST().

Revision 3.00.001 (Released 2014-05-08)

1. First release.

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