

**ARM® Cortex®-M0  
32-bit Microcontroller**

**NuMicro® Family  
NUC029xDE Series BSP  
Revision History**

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**Revision 3.00.004 (Released 2024-06-18)**

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1. Revised IROM and IRAM size of KEIL Projects.
2. Used "volatile" with the function pointer to disable compiler optimizations in I2C sample code.
3. Enabled LVR in SYS\_PowerDown\_MinCurrent sample code to prevent power on/off fail.
4. Added timeout handler for infinite loop.
5. Added SYS\_PowerDown\_MinCurrent sample code.
6. Added I2C hang up & recover mechanism for I2C Master and Slave sample code.

**Revision 3.00.003 (Released 2021-01-20)**

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1. Fixed Semihost sample code.
2. Added Apache-2.0 license declaration in driver source.
3. Added README.md file.

**Revision 3.00.002 (Released 2019-11-11)**

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1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.

**Revision 3.00.001 (Released 2018-06-28)**

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1. First Release

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