

ARM® Cortex®-M0 32-bit Microcontroller

NuMicro[®] Family NUC029xGE Series BSP Revision History

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Revision 3.00.005 (Released 2024-07-30)

- Corrected UI2C EEPROM/ SingleByte/ MultiBytes sample code not displaying execution results.
- 2. Fixed the issue causing execution failure in the USCI-I2C sample code by correcting the erroneous status return.
- 3. Added FMC_SPROM sample code.
- 4. Added slew rate control for SPI sample codes.
- 5. Removed USBD LPM function.
- 6. Used "volatile" with the function pointer to disable compiler optimizations in I2C sample code.
- 7. Modified RegBased/FMC_IAP sample code.
- 8. Modified USCI UART TxRxFunction sample code.
- 9. Updated USBD HID keyboard sample code to support LED status.
- 10. Added driver and sample code related to USCI_SPI PDMA function.
- 11. Added SYS_PowerDown_MinCurrent sample code.
- 12. Updated SPI_Loopback sample code.
- 13. Modified SYS_UnLockReg() time-out handler.
- 14. Called UART FIFO size from uart.h in USBD VCOM sample code.
- 15. Added GPIO MFP macros.

Revision 3.00.004 (Released 2022-08-17)

- 1. Fixed return in main() cause hardfault issue.
- 2. Fixed myretarget.c for IAR 8.42.1 and later.
- 3. Added Blinky sample code for NuMaker board.
- 4. Added macros for multi-function pin in Library/StdDriver/inc/sys.h.
- 5. Added timeout handler for infinite loop.
- 6. Fixed USBD MassStorage CDROM crash on Linux.
- 7. Added I2C hang up and recover mechanism for I2C Master and Slave sample code.
- 8. Modified GPIO number and MFP in Library/Device/Nuvoton/NUC029xGE/Include/NUC029xGE.h.
- 9. Enabled schmitt trigger of I2C sample code.
- 10. Fixed UART TX FIFO control issue in USBD VCOM sample code.

Revision 3.00.003 (Released 2021-01-20)

- 1. Modified to pass USB-IF CV-Chapter 9 & Class test of all USBD Sample code.
- 2. Fixed Semihost sample code.
- 3. Added SPI TRIGGER TX RX PDMA and SPI DISABLE TX RX PDMA API.
- 4. Added Apache-2.0 license declaration in driver source.
- 5. Added README.md file.

Revision 3.00.002 (Released 2019-11-12)

- 1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
- 2. Supports GNU GCC.
- 3. Added FMCIDLE MODULE definition in CLK driver.

Revision 3.00.001 (Released 2018-09-21)

1. Initial Release



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