

Arm[®] Cortex[®]-M 32-bit Microcontroller

NuMicro™ Family NUC121 Series BSP Revision History

The information described in this document is the exclusive intellectual property of Nuvoton Technology Corporation and shall not be reproduced without permission from Nuvoton.

Nuvoton is providing this document only for reference purposes of NuMicro microcontroller based system design. Nuvoton assumes no responsibility for errors or omissions.

All data and specifications are subject to change without notice.

For additional information or questions, please contact: Nuvoton Technology Corporation.

www.nuvoton.com



Revision 3.00.003 (Released 2017-12-13)

- 1. NUC121.h
 - TIMER_CTL_TRGDAC_Pos, TIMER_CTL_TRGDAC_Msk: Removed.
- 2. I2C driver
 - I2C_ReadByteTwoRegs, I2C_ReadMultiBytesTwoRegs,
 I2C_WriteByteTwoRegs, I2C_WriteMultiBytesTwoRegs: Fixed high byte lost issue.
- SPI driver
 - I2S Open: Improved I2S sample rate accuracy.
- 4. TIMER driver
 - TIMER_TRG_TO_EADC: Replaced with TIMER_TRG_TO_ADC
- 5. USBD driver
 - USBD_L1RESUME: Replaced with USBD_STATE_L1RESUME.
 - USBD_L1SUSPEND: Replaced with USBD_STATE_L1SUSPEND.
- 6. USCI-I2C driver
 - UI2C_ReadByteTwoRegs, UI2C_ReadMultiBytesTwoRegs, UI2C_WriteByteTwoRegs, UI2C_WriteMultiBytesTwoRegs: Fixed high byte lost issue.
- 7. USCI-UART driver
 - UUART_Open, UUART_SetLine_Config: Fixed wrong baud rate issue.

Revision 3.00.002 (Released 2017-03-09)

- 1. NUC121.h
 - BPWM0 IRQn, BPWM1 IRQn: New definition.
- 2. BPWM driver
 - BPWM ConfigOutputChannel: Fixed 100% duty issue.
- 3. CLK driver
 - CLK_CLKSEL1_ADCSEL_HIRC_DIV2: Replaced with CLK_CLKSEL1_ADCSEL_HIRC.
 - GPIOA_MODULE, GPIOB_MODULE, GPIOC_MODULE, GPIOD_MODULE, GPIOE_MODULE, GPIOF_MODULE: Fixed definition.
- 4. I²C driver
 - I2C CLEAR WAKEUP WR STATUS: Redundant, removed.
- 5. PWM driver
 - PWM_ConfigOutputChannel: Fixed 100% duty issue.
- 6. SYS driver
 - SYS_GPD_MFPL_PD3MFP_UART0_CTS: Replaced with SYS_GPD_MFPL_PD3MFP_UART0_nCTS.
 - SYS_GPD_MFPL_PD3MFP_UART0_RTS: Replaced with SYS_GPD_MFPL_PD3MFP_UART0_nRTS.
 - SYS_GPF_MFPL_PF0MFP_XT1_OUT,
 SYS_GPF_MFPL_PF0MFP_X32_OUT, SYS_GPF_MFPL_PF1MFP_XT1_IN,
 SYS_GPF_MFPL_PF1MFP_X32_IN: New definition for backward compatible.
- 7. Sample code
 - BPWM_DutySwitch: Fixed 100% duty issue.
 - FMC_IAP: Fixed wrong project setting.
 - LED_Toggle: New sample code.
 - PWM_DutySwitch: Fixed 100% duty issue.
 - UART_Wakeup_LXT: New sample code split from UART_Wakeup due to NUC121-B tiny board removed LXT.



- USBD_Audio_HID_NAU8822: Fixed PD11 issue.
- All USB sample codes: Replied USB 2.1 version only if SUPPORT_LPM defined.
- USCI_UART_TxRx_Function: Fixed compilation error.

Revision 3.00.001 (Released 2016-10-07)

1. Primary release version to support NUC121xC1AE and NUC125xC1AE.



Important Notice

Nuvoton Products are neither intended nor warranted for usage in systems or equipment, any malfunction or failure of which may cause loss of human life, bodily injury or severe property damage. Such applications are deemed, "Insecure Usage".

Insecure usage includes, but is not limited to: equipment for surgical implementation, atomic energy control instruments, airplane or spaceship instruments, the control or operation of dynamic, brake or safety systems designed for vehicular use, traffic signal instruments, all types of safety devices, and other applications intended to support or sustain life.

All Insecure Usage shall be made at customer's risk, and in the event that third parties lay claims to Nuvoton as a result of customer's Insecure Usage, customer shall indemnify the damages and liabilities thus incurred by Nuvoton.

Please note that all data and specifications are subject to change without notice.

All the trademarks of products and companies mentioned in this datasheet belong to their respective owners