

Arm® Cortex®-M0
32-bit Microcontroller

NUC121 Series CMSIS BSP
Revision History

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Revision 3.01.001 (Released 2018-06-27)

1. NuMicro.h
 - New header file.
2. NUC121.h
 - Separated register declarations of each IP into distinct header files.
3. USB driver
 - Fixed the issue of USB Control-In packet with the same size as endpoint limit.
4. Sample code
 - Added Eclipse projects for GCC toolchain.
 - CLK_ClockDetector, HIRC_Trim, UART_Wakeup_LXT, ADC samples: Set analog pins as input mode and disabled digital input function.
 - PDMA_ScatterGather_PingPongBuffer: Added PDMA error handling.
 - UART_PDMA: Added UART error handling.
 - USCI_I2C_Monitor: New sample code.

Revision 3.00.003 (Released 2017-12-13)

1. NUC121.h
 - TIMER_CTL_TRGDAC_Pos, TIMER_CTL_TRGDAC_Msk: Removed.
5. I2C driver
 - I2C_ReadByteTwoRegs, I2C_ReadMultiBytesTwoRegs, I2C_WriteByteTwoRegs, I2C_WriteMultiBytesTwoRegs: Fixed high byte lost issue.
6. SPI driver
 - I2S_Open: Improved I2S sample rate accuracy.
7. TIMER driver
 - TIMER_TRG_TO_EADC: Replaced with TIMER_TRG_TO_ADC
8. USB driver
 - USBD_L1RESUME: Replaced with USBD_STATE_L1RESUME.
 - USBD_L1SUSPEND: Replaced with USBD_STATE_L1SUSPEND.
9. USCI-I2C driver
 - UI2C_ReadByteTwoRegs, UI2C_ReadMultiBytesTwoRegs, UI2C_WriteByteTwoRegs, UI2C_WriteMultiBytesTwoRegs: Fixed high byte lost issue.
10. USCI-UART driver
 - UUART_Open, UUART_SetLine_Config: Fixed wrong baud rate issue.

Revision 3.00.002 (Released 2017-03-09)

1. NUC121.h
 - BPWM0_IRQn, BPWM1_IRQn: New definition.
2. BPWM driver
 - BPWM_ConfigOutputChannel: Fixed 100% duty issue.
3. CLK driver
 - CLK_CLKSEL1_ADCSEL_HIRC_DIV2: Replaced with CLK_CLKSEL1_ADCSEL_HIRC.
 - GPIOA_MODULE, GPIOB_MODULE, GPIOC_MODULE, GPIOD_MODULE, GPIOE_MODULE, GPIOF_MODULE: Fixed definition.
4. I²C driver
 - I2C_CLEAR_WAKEUP_WR_STATUS: Redundant, removed.
5. PWM driver

- PWM_ConfigOutputChannel: Fixed 100% duty issue.
- 6. SYS driver
 - SYS_GPD_MFPL_PD3MFP_UART0_CTS: Replaced with SYS_GPD_MFPL_PD3MFP_UART0_nCTS.
 - SYS_GPD_MFPL_PD3MFP_UART0_RTS: Replaced with SYS_GPD_MFPL_PD3MFP_UART0_nRTS.
 - SYS_GPF_MFPL_PF0MFP_XT1_OUT, SYS_GPF_MFPL_PF0MFP_X32_OUT, SYS_GPF_MFPL_PF1MFP_XT1_IN, SYS_GPF_MFPL_PF1MFP_X32_IN: New definition for backward compatible.
- 7. Sample code
 - BPWM_DutySwitch: Fixed 100% duty issue.
 - FMC_IAP: Fixed wrong project setting.
 - LED_Toggle: New sample code.
 - PWM_DutySwitch: Fixed 100% duty issue.
 - UART_Wakeup_LXT: New sample code split from UART_Wakeup due to NUC121-B tiny board removed LXT.
 - USBD_Audio_HID_NAU8822: Fixed PD11 issue.
 - All USB sample codes: Replied USB 2.1 version only if SUPPORT_LPM defined.
 - USCI_UART_TxRx_Function: Fixed compilation error.

Revision 3.00.001 (Released 2016-10-07)

1. Primary release version to support NUC121xC1AE and NUC125xC1AE.

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