

# Arm<sup>®</sup> Cortex<sup>®</sup>-M0 32-bit Microcontroller

# NUC121/NUC125 Series CMSIS BSP Revision History

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#### Revision 3.01.010 (Released 2024-10-09)

- CMSIS: Upgraded to v6.1.0.
- Keil startup\_NUC121.c: Instead of ASM code.
- Sample code
  - IAR projects: Upgraded for IAR v8.
  - Keil projects: Upgraded for CMSIS v6.1.0 and Arm Compiler v6.
  - USCI\_I2C\_Wakeup\_Master: Removed.

#### **Revision 3.01.009** (Released 2024-03-21)

- Library
  - retarget.c: Fixed \_\_ARM\_use\_no\_argv multiple definition issue in newer KEIL versions.
  - BPWM: BPWM\_ConfigOutputChannel fixed duty issue.
  - CLK
    - > CLK\_PowerDown cleared CPU deep sleep mode select after wakeup.
    - CLK\_SysTickDelay Improved robustness.
  - LDROM.icf and LDROM.sct: Fixed LDROM address.
  - PWM: PWM ConfigOutputChannel fixed duty issue.
- Sample code
  - BPWM SwitchDuty, PWM SwitchDuty: Fixed 100% duty issue.
  - ISP samples: Fixed LDROM download failure in IAR and KEIL.
  - ISP DFU: Updated Windows driver with Nuvoton vendor name.
  - SYS\_PowerDown\_MinCurrent: Removed LVR disable setting.
  - USBD samples
    - Removed EP1 stall while setup error occurred.
    - Removed LPM support.
  - USBD HID samples: Supported LED status.

#### Revision 3.01.008 (Released 2023-02-13)

- Standard driver
  - retarget.c: Fixed print issue with IAR EWARM V8 and above
- Sample code
  - I2C samples: Improved error handling.
  - ISP\_DFU, ISP\_HID, and ISP\_MSC: Enabled USB trim function.
  - ISP\_SPI: Added GCC project.
  - ISP samples



- Added IAR projects.
- Added scatter file to check image size.
- Enabled function section linking and image size optimization in KEIL projects.
- SYS\_PowerDown\_MinCurrent: Added.
- USBD\_HID\_Mouse2: Avoided wakeup failure.

## Revision 3.01.007 (Released 2022-01-26)

- Standard driver
  - Improved infinite loop prevention with timeout counter and error code.
  - TIMER: TIMER\_Delay added return value.
- Sample code
  - I2C samples: Removed reduntant code.
  - I2C and USCI\_I2C samples: Enabled schmitt trigger of I2C pins.
  - ISP\_I2C, ISP\_RS485, and ISP\_UART: Added GCC project.
  - ISP MSC: Added.
  - USBD samples: Added software boundary of USB trim.
  - USBD HID samples: Unified INT IN interval.
  - USBD\_MassStorage\_CDROM: Fixed Linux and Mac compatibility issue.

#### **Revision 3.01.006** (Released 2020-05-21)

- IAR startup\_NUC121.s: Fixed build issue with IAR EWARM V7.5.
- Standard driver
  - UART: UART\_DisableInt and UART\_EnableInt removed NVIC IRQ control.
- Sample code
  - I2C\_PDMA\_TRX: Removed unused \_\_ALIGNED.
  - PDMA, PDMA\_Scatter\_Gather: Added word alignment.
  - UART samples: Added NVIC IRQ control.
  - USBD\_MassStorage\_CDROM: Removed 2KB file size limit.
  - USBD VCOM samples: Fixed potential UART Tx FIFO overflow issue.
  - USBD\_VCOM\_DaulPort: Fixed wrong baudrate issue.
  - USCI\_SPI\_SlaveModeINT: Added.



#### Revision 3.01.005 (Released 2020-09-14)

- Added Apache-2.0 license declaration.
- Standard driver
  - retarget.c
    - write and SendChar ToUART fixed '\r' issue.
    - SendChar fixed uninitialized pointer.
  - SPI: SPI\_DISABLE\_TX\_RX\_PDMA and SPI\_TRIGGER\_TX\_RX\_PDMA added.
  - TIMER: TIMER\_SET\_OPMODE added.
  - UART: UART PDMA DISABLE and UART PDMA ENABLE added.
- Sample code
  - I2C\_Wakeup\_Slave: Improved wakeup robustness.
  - UART samples and USCI\_UART\_TxRx\_Function: Fixed '\r' issue.
  - USBD\_VCOM\_DualPort: Fixed only one VCOM port in Linux.

#### **Revision 3.01.004** (Released 2019-10-02)

- Standard driver
  - TIMER: TIMER\_Open fixed return value.
  - USCI SPI: USPI SET SS HIGH fixed implementation.
- Sample code
  - I2C\_PDMA\_TRX, ISP\_DFU, ISP\_I2C, ISP\_RS485, and ISP\_SPI: Added.
  - FMC ExeInSRAM: Added IAR and GCC projects.
  - UART TxRx Function: Fixed data lost.

#### Revision 3.01.003 (Released 2019-06-19)

- Standard driver
  - I2C: Added error handling for new transfer.
  - TIMER: TIMER GetModuleClock used correct PCLK.
  - USBD: Fixed potential issue.
- Sample code
  - ISP samples: Added.
  - TIMER\_Delay: Used correct PCLK.
  - USBD samples: Fixed potential issue.
  - USBD\_Audio\_NAU8822 and USBD\_Audio\_HID\_NAU8822: Cleared FIFO before play / record.



#### Revision 3.01.002 (Released 2018-12-28)

- CMSIS: Upgraded to v5.1.1.
- Standard driver
  - CLK: CLK PowerDown required HIRC / MIRC auto trim disabled.
  - PDMA: PDMA Open supported multiple open.
- Sample code
  - Keil projects used "NULink Debugger" instead of "Nuvoton Nu-Link Debugger".
  - Semihost fixed build failure with GCC toolchain 7-2018-q2.
  - I2S\_PDMA\_PlayRecord: Fixed race condition.
  - TIMER CaptureCounter: Fixed array access overflow.
  - USBD VCOM samples: Fixed UART baud rate calculation.
  - USBD\_VCOM\_And\_HID\_Transfer: Fixed buffer overflow.

#### Revision 3.01.001 (Released 2018-06-27)

- NuMicro.h: Added.
- NUC121.h: Separated register declarations of each IP into distinct header files.
- Standard driver
  - USBD: Fixed USB Control-In packet with the same size as endpoint limit.
- Sample code
  - Added Eclipse projects with GCC toolchain.
  - ADC samples, CLK\_ClockDetector, HIRC\_Trim, and UART\_Wakeup\_LXT: Set analog pins as input mode and disabled digital input function.
  - PDMA\_ScatterGather\_PingPongBuffer: Added PDMA error handling.
  - UART\_PDMA: Added UART error handling.
  - USCI\_I2C\_Monitor: Added.

#### Revision 3.00.003 (Released 2017-12-13)

- NUC121.h: TIMER\_CTL\_TRGDAC\_Pos and TIMER\_CTL\_TRGDAC\_Msk removed.
- Standard driver
  - I2C: I2C\_ReadByteTwoRegs, I2C\_ReadMultiBytesTwoRegs,
     I2C\_WriteByteTwoRegs, and I2C\_WriteMultiBytesTwoRegs fixed high byte lost.
  - SPI: I2S\_Open improved I2S sample rate accuracy.
  - TIMER: TIMER TRG TO EADC replaced by TIMER TRG TO ADC.
  - USBD
    - USBD\_L1RESUME replaced by USBD\_STATE\_L1RESUME.



- USBD L1SUSPEND replaced by USBD STATE L1SUSPEND.
- USCI-I2C: UI2C\_ReadByteTwoRegs, UI2C\_ReadMultiBytesTwoRegs, UI2C\_WriteByteTwoRegs, and UI2C\_WriteMultiBytesTwoRegs fixed high byte lost.
- USCI-UART: UUART\_Open and UUART\_SetLine\_Config fixed wrong baud rate.

## Revision 3.00.002 (Released 2017-03-09)

- NUC121.h: BPWM0\_IRQn and BPWM1\_IRQn added.
- Standard driver
  - BPWM: BPWM ConfigOutputChannel fixed 100% duty issue.
  - CLK
    - CLK\_CLKSEL1\_ADCSEL\_HIRC\_DIV2 replaced by CLK\_CLKSEL1\_ADCSEL\_HIRC.
    - GPIOA\_MODULE, GPIOB\_MODULE, GPIOC\_MODULE, GPIOD MODULE, GPIOE MODULE, and GPIOF MODULE fixed.
  - I2C: I2C CLEAR WAKEUP WR STATUS removed.
  - PWM: PWM ConfigOutputChannel fixed 100% duty issue.
  - SYS
    - SYS\_GPF\_MFPL\_PF0MFP\_XT1\_OUT, SYS\_GPF\_MFPL\_PF0MFP\_X32\_OUT, SYS\_GPF\_MFPL\_PF1MFP\_XT1\_IN, and SYS\_GPF\_MFPL\_PF1MFP\_X32\_IN added.
    - SYS\_GPD\_MFPL\_PD3MFP\_UART0\_CTS replaced by SYS\_GPD\_MFPL\_PD3MFP\_UART0\_nCTS.
    - SYS\_GPD\_MFPL\_PD3MFP\_UART0\_RTS replaced by SYS\_GPD\_MFPL\_PD3MFP\_UART0\_nRTS.
- Sample code
  - BPWM DutySwitch: Fixed 100% duty issue.
  - FMC\_IAP: Fixed Wrong project setting.
  - LED\_Toggle, UART\_Wakeup\_LXT: Added.
  - PWM\_DutySwitch: Fixed100% duty issue.
  - USBD samples: Replied USB 2.1 version only if SUPPORT\_LPM defined.
  - USBD\_Audio\_HID\_NAU8822: Fixed PD11 issue.
  - USCI\_UART\_TxRx\_Function: Fixed compilation error.

#### Revision 3.00.001 (Released 2016-10-07)

Initial release.



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