

## NUC1261 Series CMSIS BSP Guide

Directory Introduction for 32-bit NuMicro™ Family

### Directory Information

Please extract the “NUC1261Series\_BSP\_CMSIS\_V3.00.003.zip” file firstly, and then put the “NUC1261Series\_BSP\_CMSIS\_V3.00.003” folder into the working folder (e.g. .\Nuvoton\BSP Library\).

This BSP folder contents:

|             |   |
|-------------|---|
| Document\   | Device driver reference manual and reversion history. |
| Library\    | Device driver header and source files.                |
| SampleCode\ | Device driver sample code.                            |

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## 1 .\Document\

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|---|---|
| <b>CMSIS.html</b>   | <p>Introduction of CMSIS version 4.5.0. CMSIS components included CMSIS-CORE, CMSIS-Driver, CMSIS-DSP, etc.</p> <ul style="list-style-type: none"> <li>● CMSIS-CORE: API for the Cortex-M0 processor core and peripherals.</li> <li>● CMSIS-Driver: Defines generic peripheral driver interfaces for middleware making it reusable across supported devices.</li> <li>● CMSIS-DSP: DSP Library Collection with over 60 Functions for various data types: fix-point (fractional q7, q15, q31) and single precision floating-point (32-bit).</li> </ul> |
| <b>NuMicro NUC126 Series CMSIS BSP Revision History.pdf</b> | <p>The revision history of NUC1261 Series BSP.</p>  |
| <b>NuMicro NUC126 Series Driver Reference Guide.chm</b>     | <p>The usage of drivers in NUC1261 Series BSP.</p>  |

## 2 .\Library\

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|----------------------|--|
| <b>CMSIS\</b>        | Cortex® Microcontroller Software Interface Standard (CMSIS) V4.5.0 definitions by ARM® Corp. |
| <b>Device\</b>       | CMSIS compliant device header file.  |
| <b>SmartcardLib\</b> | Library for accessing a smartcard.   |
| <b>StdDriver\</b>    | All peripheral driver header and source files.   |

### 3 .\Sample Code\

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| <b>CardReader\</b>        | CCID (Circuit card interface device) sample code for smart card interface.   |
| <b>Hard_Fault_Sample\</b> | <p>Show hard fault information when hard fault happened.</p> <p>The hard fault handler show some information included program counter, which is the address where the processor was executing when the hard fault occur. The listing file (or map file) can show what function and instruction that was.</p> <p>It also shows the Link Register (LR), which contains the return address of the last function call. It can show the status where CPU comes from to get to this point.</p> |
| <b>ISP\</b>               | Sample codes for In-System-Programming.  |
| <b>Semihost\</b>          | Show how to print and get character through IDE console window.  |
| <b>RegBased\</b>          | The sample codes which access control registers directly.  |
| <b>StdDriver\</b>         | Demonstrate the usage of NUC1261 series MCU peripheral driver APIs.  |
| <b>Template\</b>          | A project template for NUC1261 series MCU.   |

## 4 .\SampleCode\ISP

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| <b>ISP_DFU</b>   | In-System-Programming Sample code through USB interface and following Device Firmware Upgrade Class Specification. |
| <b>ISP_HID</b>   | In-System-Programming Sample code through USB HID interface.   |
| <b>ISP_I2C</b>   | In-System-Programming Sample code through I2C interface.   |
| <b>ISP_RS485</b> | In-System-Programming Sample code through RS485 interface.   |
| <b>ISP_SPI</b>   | In-System-Programming Sample code through SPI interface.   |
| <b>ISP_UART</b>  | In-System-Programming Sample code through UART interface.  |

## 5 .\SampleCode\RegBased

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| <b>ACMP</b>                         | Demonstrate how Analog Comparator (ACMP) works with internal band-gap voltage.   |
| <b>ACMP_Wakeup</b>                  | Show how to wake up MCU from Power-down mode by ACMP wake-up function.   |
| <b>ADC_ContinuousScanMode</b>       | Perform A/D Conversion with ADC continuous scan mode.  |
| <b>ADC_PDMA_SingleCycleScanMode</b> | Perform A/D Conversion with ADC single cycle scan mode and transfer result by PDMA.  |
| <b>ADC_PwmTrigger</b>               | Demonstrate how to trigger ADC by PWM.   |
| <b>ADC_ResultMonitor</b>            | Monitor the conversion result of Channel 2 by the digital compare function.  |
| <b>ADC_SingleCycleScanMode</b>      | Perform A/D Conversion with ADC single cycle scan mode.  |
| <b>ADC_SingleMode</b>               | Perform A/D Conversion with ADC single mode.   |
| <b>CLK_ClockDetector</b>            | Show the usage of clock fail detector and clock frequency monitor function.  |
| <b>CRC_CCITT</b>                    | Implement CRC in CRC-CCITT mode and get CRC checksum results.  |
| <b>CRC_CRC32</b>                    | Implement CRC in CRC-32 mode with PDMA transfer.   |
| <b>CRC_CRC8</b>                     | Implement CRC in CRC-8 mode and get CRC checksum results.  |
| <b>EBI_NOR</b>                      | Configure EBI interface to access MX29LV320T (NOR Flash) on EBI interface.   |
| <b>EBI_SRAM</b>                     | Configure EBI interface to access BS616LV4017 (SRAM) on EBI interface.   |
| <b>FMC_ExecInSRAM</b>               | Implement a code and execute the code in SRAM to program embedded Flash (support KEIL MDK only).   |
| <b>FMC_IAP</b>                      | Show how to call LDROM functions from APROM. The code in APROM will look up the table at 0x100E00 to get the address of function of LDROM and call the function. |

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| <b>FMC_MultiBoot</b>        | Implement a multi-boot system to boot from different applications in APROM. A LDROM code and four APROM code are implemented in this sample code. |
| <b>FMC_RW</b>               | Demonstrate how to read/program embedded Flash by ISP function.   |
| <b>GPIO_EINTAndDebounce</b> | Show the usage of GPIO external interrupt function and de-bounce function.  |
| <b>GPIO_INT</b>             | Show the usage of GPIO interrupt function.  |
| <b>GPIO_OutputInput</b>     | Show how to set GPIO pin mode and use pin data input/output control.  |
| <b>GPIO_PowerDown</b>       | Show how to wake up system from Power-down mode by GPIO interrupt.  |
| <b>HDIV</b>                 | Show how to use divider API and how to use hardware divider by control registers.   |
| <b>I2C_EEPROM</b>           | Demonstrate how to access EEPROM through a I2C interface  |
| <b>I2C_GCMode_Master</b>    | Demonstrate how a Master uses I2C address 0x0 to write data to I2C Slave. This sample code needs to work with I2C_GCMode_Slave.                   |
| <b>I2C_GCMode_Slave</b>     | Demonstrate how to receive Master data in GC (General Call) mode. This sample code needs to work with I2C_GCMode_Master.                          |
| <b>I2C_Loopback</b>         | Demonstrate how a Master accesses a Slave.  |
| <b>I2C_Master</b>           | Demonstrate how a Master accesses a Slave. This sample code needs to work with I2C_Slave.   |
| <b>I2C_Master_PDMA</b>      | Demonstrate how a Master accesses Slave using PDMA TX mode and PDMA RX mode.  |
| <b>I2C_Slave</b>            | Demonstrate how to set I2C in slave mode to receive data from a Master. This sample code needs to work with I2C_Master.                           |
| <b>I2C_Slave_PDMA</b>       | Demonstrate how a Slave uses PDMA Rx mode to receive data from a Master.  |

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| <b>I2C_Wakeup_Slave</b>                   | Demonstrate how to set I2C to wake up MCU from Power-down mode. This sample code needs to work with I2C_Master.                |
| <b>I2S_Master</b>                         | Configure SPI1 as I2S Master mode and demonstrate how I2S works in Master mode. This sample code needs to work with I2S_Slave. |
| <b>I2S_PDMA_NAU8822</b>                   | This is an I2S demo with PDMA function connected with NAU8822 codec.   |
| <b>I2S_PDMA_Play</b>                      | This is an I2S demo for playing data and demonstrating how I2S works with PDMA.  |
| <b>I2S_PDMA_PlayRecord</b>                | This is an I2S demo for playing and recording data with PDMA function.   |
| <b>I2S_PDMA_Record</b>                    | This is an I2S demo for recording data and demonstrating how I2S works with PDMA.  |
| <b>I2S_Slave</b>                          | Configure SPI1 as I2S Slave mode and demonstrate how I2S works in Slave mode. This sample code needs to work with I2S_Master.  |
| <b>PDMA</b>                               | Use PDMA Channel 2 to transfer data from memory to memory.   |
| <b>PDMA_ScatterGather_PingPongBuffer</b>  | Use PDMA to implement Ping-Pong buffer by scatter-gather mode (memory to memory).  |
| <b>PDMA_Scatter_Gather</b>                | Use PDMA Channel 4 to transfer data from memory to memory by scatter-gather mode.  |
| <b>PWM_Capture</b>                        | Capture the PWM1 Channel 0 waveform by PWM1 Channel 2.   |
| <b>PWM_DeadZone</b>                       | Demonstrate how to use PWM Dead Zone function.   |
| <b>PWM_DoubleBuffer_PeriodLoadingMode</b> | Change duty cycle and period of output waveform by PWM Double Buffer function (Period loading mode).                           |
| <b>PWM_DutySwitch</b>                     | Change duty cycle of output waveform by configured period.   |
| <b>PWM_OutputWaveform</b>                 | Demonstrate how to use PWM output waveform.  |
| <b>PWM_PDMA_Capture</b>                   | Capture the PWM1 Channel 0 waveform by PWM1 Channel 2, and use PDMA to transfer captured data.                                 |



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| <b>PWM_SyncStart</b>        | Demonstrate how to use PWM counter synchronous start function.   |
| <b>RTC_AlarmWakeup</b>      | Use RTC alarm interrupt event to wake up system.   |
| <b>RTC_TimeAndTick</b>      | Show the current RTC data/time per tick.   |
| <b>SCUART_TxRx</b>          | Show Smartcard UART by connecting PA.0 and PA.1 pins.  |
| <b>SC_ReadATR</b>           | Read Smartcard ATR from the SC0 port.  |
| <b>SPI_Loopback</b>         | Implement SPI Master loop back transfer. This sample code needs to connect SPI0_MISO0 pin and SPI0_MOSI0 pin together. It will compare the received data with transmitted data.        |
| <b>SPI_MasterFifoMode</b>   | Configure SPI0 as Master mode and demonstrate how to communicate with an off-chip SPI Slave device with FIFO mode. This sample code needs to work with SPI_SlaveFifoMode.              |
| <b>SPI_PDMA_LoopTest</b>    | Demonstrate SPI data transfer with PDMA. SPI0 will be configured as Master mode and SPI1 will be configured as Slave mode. Both TX PDMA function and RX PDMA function will be enabled. |
| <b>SPI_SlaveFifoMode</b>    | Configure SPI0 as Slave mode and demonstrate how to communicate with an off-chip SPI Master device with FIFO mode. This sample code needs to work with SPI_MasterFifoMode.             |
| <b>SYS_BODWakeup</b>        | Show how to wake up system form Power-down mode by brown-out detector interrupt.   |
| <b>SYS_PLLClockOutput</b>   | Change system clock to different PLL frequency and output system clock from CLKO pin.  |
| <b>SYS_VoltageDetector</b>  | Show how to use voltage detector to detect pin input voltage.  |
| <b>TIMER_CaptureCounter</b> | Show how to use the timer2 capture function to capture timer2 counter value.   |
| <b>TIMER_EventCounter</b>   | Show how to use the timer2 capture function to capture timer2 counter value.   |
| <b>TIMER_PeriodicINT</b>    | Implement timer counting in periodic mode.   |

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| <b>TIMER_PWM_ChangeDuty</b>     | Change duty cycle and period of output waveform in PWM down count type.  |
| <b>TIMER_PWM_DeadTime</b>       | Demonstrate Timer PWM Complementary mode and Dead-Time function.   |
| <b>TIMER_PWM_OuputWaveform</b>  | Demonstrate output different duty waveform in Timer0~Timer3 PWM.   |
| <b>TIMER_TimeoutWakeup</b>      | Use timer0 periodic time-out interrupt event to wake up system.  |
| <b>UART_AutoBaudRate_Master</b> | Show how to use auto baud rate detection function. This sample code needs to work with UART_AutoBaudRate_Slave.  |
| <b>UART_AutoBaudRate_Slave</b>  | Show how to use auto baud rate detection function. This sample code needs to work with UART_AutoBaudRate_Master. |
| <b>UART_Autoflow_Master</b>     | Transmit and receive data with auto flow control. This sample code needs to work with UART_Autoflow_Slave.       |
| <b>UART_Autoflow_Slave</b>      | Transmit and receive data with auto flow control. This sample code needs to work with UART_Autoflow_Master.      |
| <b>UART_IrDA_Master</b>         | Transmit and receive data in UART IrDA mode. This sample code needs to work with UART_IrDA_Slave.                |
| <b>UART_IrDA_Slave</b>          | Transmit and receive data in UART IrDA mode. This sample code needs to work with UART_IrDA_Master.               |
| <b>UART_LIN</b>                 | Transmit LIN frame including header and response in UART LIN mode.   |
| <b>UART_PDMA</b>                | Transmit and receive UART data with PDMA.  |
| <b>UART_RS485_Master</b>        | Transmit and receive data in UART RS485 mode. This sample code needs to work with UART_RS485_Slave.              |
| <b>UART_RS485_Slave</b>         | Transmit and receive data in UART RS485 mode. This sample code needs to work with UART_RS485_Master.             |
| <b>UART_TxRxFunction</b>        | Transmit and receive data from PC terminal through RS232 interface.  |
| <b>UART_Wakeup</b>              | Show how to wake up system from Power-down mode by   |

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|                                      | UART interrupt.   |
| <b>USCI_I2C_EEPROM</b>               | Demonstrate how to access EEPROM through a USCI_I2C interface   |
| <b>USCI_I2C_Loopback</b>             | Demonstrate how a Master accesses Slave.  |
| <b>USCI_I2C_Loopback_10bit</b>       | Demonstrate how a Master uses 10-bit addressing access Slave.   |
| <b>USCI_I2C_Master</b>               | Demonstrate how a Master accesses Slave. This sample code needs to work with I2C_Slave.   |
| <b>USCI_I2C_Master_10bit</b>         | Demonstrate how a Master uses 10-bit addressing access Slave. This sample code needs to work with I2C_Slave.  |
| <b>USCI_I2C_Slave</b>                | Demonstrate how to set I2C in Slave mode to receive data from a Master. This sample code needs to work with I2C_Master.   |
| <b>USCI_I2C_Slave_10bit</b>          | Demonstrate how to set I2C in 10-bit addressing slave mode to receive data from a Master. This sample code needs to work with I2C_Master.   |
| <b>USCI_I2C_Wakeup_Slave</b>         | Demonstrate how to set I2C to wake up MCU from Power-down mode. This sample code needs to work with I2C_Master.   |
| <b>USCI_SPI_Loopback</b>             | Implement USCI_SPI1 Master loop back transfer. This sample code needs to connect USCI_SPI1_MISO pin and USCI_SPI1_MOSI pin together. It will compare the received data with transmitted data. |
| <b>USCI_SPI_MasterMode</b>           | Configure USCI_SPI1 as Master mode and demonstrate how to communicate with an off-chip SPI Slave device. This sample code needs to work with USCI_SPI_SlaveMode.                              |
| <b>USCI_SPI_SlaveMode</b>            | Configure USCI_SPI1 as Slave mode and demonstrate how to communicate with an off-chip SPI Master device. This sample code needs to work with USCI_SPI_MasterMode.                             |
| <b>USCI_UART_AutoBaudRate_Master</b> | Show how to use auto baud rate detection function. This sample code needs to work with USCI_UART_AutoBaudRate_Slave.  |
| <b>USCI_UART_AutoBaudRate_Slave</b>  | Show how to use auto baud rate detection function. This sample code needs to work with  |

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|                                  | USCI_UART_AutoBaudRate_Master.   |
| <b>USCI_UART_Autoflow_Master</b> | Transmit and receive data with auto flow control. This sample code needs to work with USCI_UART_Autoflow_Slave.                                  |
| <b>USCI_UART_Autoflow_Slave</b>  | Transmit and receive data with auto flow control. This sample code needs to work with USCI_UART_Autoflow_Master.                                 |
| <b>USCI_UART_RS485_Master</b>    | Transmit and receive data in RS485 mode. This sample code needs to work with USCI_UART_RS485_Slave.  |
| <b>USCI_UART_RS485_Slave</b>     | Transmit and receive data in RS485 mode. This sample code needs to work with USCI_UART_RS485_Master.   |
| <b>USCI_UART_TxRxFunction</b>    | Transmit and receive data from PC terminal through RS232 interface.  |
| <b>USCI_UART_Wakeup</b>          | Show how to wake up system from Power-down mode by USCI interrupt in UART mode.  |
| <b>WDT_TimeoutWakeupAndReset</b> | Implement WDT time-out interrupt event to wake up system and generate time-out reset system event while WDT time-out reset delay period expired. |
| <b>WWDT_CompareINT</b>           | Show how to reload the WWDT counter value.   |

## 6 .\SampleCode\StdDriver

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| <b>ACMP</b>                         | Demonstrate how Analog Comparator (ACMP) works with internal band-gap voltage.          |
| <b>ACMP_Wakeup</b>                  | Show how to wake up MCU from Power-down mode by ACMP wake-up function.                  |
| <b>ADC_ContinuousScanMode</b>       | Perform A/D Conversion with ADC continuous scan mode.                                   |
| <b>ADC_PDMA_SingleCycleScanMode</b> | Perform A/D Conversion with ADC single cycle scan mode and transfer result by PDMA.     |
| <b>ADC_PwmTrigger</b>               | Demonstrate how to trigger ADC by PWM.  |
| <b>ADC_ResultMonitor</b>            | Monitor the conversion result of Channel 2 by the digital compare function.             |
| <b>ADC_SingleCycleScanMode</b>      | Perform A/D Conversion with ADC single cycle scan mode.                                 |
| <b>ADC_SingleMode</b>               | Perform A/D Conversion with ADC single mode.  |
| <b>CLK_ClockDetector</b>            | Show the usage of clock fail detector and clock frequency monitor function.             |
| <b>CRC_CCITT</b>                    | Implement CRC in CRC-CCITT mode and get the CRC checksum result.                        |
| <b>CRC_CRC32</b>                    | Implement CRC in CRC-32 mode with PDMA transfer.  |
| <b>CRC_CRC8</b>                     | Implement CRC in CRC-8 mode and get the CRC checksum result.                            |
| <b>EBI_NOR</b>                      | Configure EBI interface to access MX29LV320T (NOR Flash) on EBI interface.              |
| <b>EBI_SRAM</b>                     | Configure EBI interface to access BS616LV4017 (SRAM) on EBI interface.                  |
| <b>FMC_ExecInSRAM</b>               | Implement a code and execute in SRAM to program embedded Flash (support KEIL MDK only). |
| <b>FMC_IAP</b>                      | Show how to set VECMAP to LDROM and reboot to LDROM from APROM.                         |
| <b>FMC_RW</b>                       | Demonstrate how to read/program embedded Flash by ISP                                   |

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|                             | function.   |
| <b>GPIO_EINTAndDebounce</b> | Show the usage of GPIO external interrupt function and de-bounce function.  |
| <b>GPIO_INT</b>             | Show the usage of GPIO interrupt function.  |
| <b>GPIO_OutputInput</b>     | Show how to set GPIO pin mode and use pin data input/output control.  |
| <b>GPIO_PowerDown</b>       | Show how to wake up system from Power-down mode by GPIO interrupt.  |
| <b>HDIV</b>                 | Show how to use divider API and how to use hardware divider by control registers.   |
| <b>I2C_EEPROM</b>           | Demonstrate how to access EEPROM through a I2C interface  |
| <b>I2C_GCMode_Master</b>    | Demonstrate how a Master uses I2C address 0x0 to write data to I2C Slave. This sample code needs to work with I2C_GCMode_Slave. |
| <b>I2C_GCMode_Slave</b>     | Demonstrate how to receive Master data in GC (General Call) mode. This sample code needs to work with I2C_GCMode_Master.        |
| <b>I2C_Loopback</b>         | Demonstrate how a Master accesses Slave.  |
| <b>I2C_Master</b>           | Demonstrate how a Master accesses Slave. This sample code needs to work with I2C_Slave.   |
| <b>I2C_Master_PDMA</b>      | Demonstrate how a Master accesses Slave using PDMA TX mode and PDMA RX mode.  |
| <b>I2C_Slave</b>            | Demonstrate how to set I2C in slave mode to receive the data from a Master. This sample code needs to work with I2C_Master.     |
| <b>I2C_Slave_PDMA</b>       | Demonstrate how a Slave uses PDMA Rx mode receive data from a Master.   |
| <b>I2C_Wakeup_Slave</b>     | Demonstrate how to set I2C to wake up MCU from Power-down mode. This sample code needs to work with I2C_Master.                 |
| <b>I2S_Master</b>           | Configure SPI1 as I2S Master mode and demonstrate how I2S works in Master mode. This sample code needs to work                  |

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|   | with I2S_Slave.   |
| <b>I2S_PDMA_NAU8822</b>                   | This is an I2S demo with PDMA function connected with NAU8822 codec.  |
| <b>I2S_PDMA_Play</b>                      | This is an I2S demo for playing data and demonstrating how I2S works with PDMA.   |
| <b>I2S_PDMA_PlayRecord</b>                | This is an I2S demo for playing and recording data with PDMA function.  |
| <b>I2S_PDMA_Record</b>                    | This is an I2S demo for recording data and demonstrating how I2S works with PDMA.   |
| <b>I2S_Slave</b>                          | Configure SPI1 as I2S Slave mode and demonstrate how I2S works in Slave mode. This sample code needs to work with I2S_Master. |
| <b>PDMA</b>                               | Use PDMA Channel 2 to transfer data from memory to memory.  |
| <b>PDMA_ScatterGather_PingPongBuffer</b>  | Use PDMA to implement Ping-Pong buffer by scatter-gather mode (memory to memory).   |
| <b>PDMA_Scatter_Gather</b>                | Use PDMA Channel 4 to transfer data from memory to memory by scatter-gather mode.   |
| <b>PWM_Capture</b>                        | Capture the PWM1 Channel 0 waveform by PWM1 Channel 2.  |
| <b>PWM_DeadZone</b>                       | Demonstrate how to use PWM Dead Zone function.  |
| <b>PWM_DoubleBuffer_PeriodLoadingMode</b> | Change duty cycle and period of output waveform by PWM Double Buffer function (Period loading mode).                          |
| <b>PWM_DutySwitch</b>                     | Change duty cycle of output waveform by configured period.  |
| <b>PWM_OutputWaveform</b>                 | Demonstrate how to use PWM output waveform.   |
| <b>PWM_PDMA_Capture</b>                   | Capture the PWM1 Channel 0 waveform by PWM1 Channel 2, and use PDMA to transfer captured data.                                |
| <b>PWM_SyncStart</b>                      | Demonstrate how to use PWM counter synchronous start function.  |
| <b>RTC_AlarmWakeup</b>                    | Use RTC alarm interrupt event to wake up system.  |



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| <b>RTC_TimeAndTick</b>             | Show the current RTC data/time per tick.   |
| <b>SCUART_TxRx</b>                 | Show Smartcard UART by connecting PA.0 and PA.1 pins.  |
| <b>SC_ReadATR</b>                  | Read Smartcard ATR from the SC0 port.  |
| <b>SC_ReadSimPhoneBook</b>         | Read SIM phone book from the SC0 port.   |
| <b>SPI_Loopback</b>                | Implement SPI Master loop back transfer. This sample code needs to connect MISO_0 pin and MOSI_0 pin together. It will compare the received data with transmitted data.                |
| <b>SPI_MasterFifoMode</b>          | Configure SPI0 as Master mode and demonstrate how to communicate with an off-chip SPI Slave device with FIFO mode. This sample code needs to work with SPI_SlaveFifoMode.              |
| <b>SPI_PDMA_LoopTest</b>           | Demonstrate SPI data transfer with PDMA. SPI0 will be configured as Master mode and SPI1 will be configured as Slave mode. Both TX PDMA function and RX PDMA function will be enabled. |
| <b>SPI_SlaveFifoMode</b>           | Configure SPI0 as Slave mode and demonstrate how to communicate with an off-chip SPI Master device with FIFO mode. This sample code needs to work with SPI_MasterFifoMode.             |
| <b>SYS_BODWakeup</b>               | Show how to wake up system form Power-down mode by brown-out detector interrupt.   |
| <b>SYS_PLLClockOutput</b>          | Change system clock to different PLL frequency and output system clock from CLK0 pin.  |
| <b>SYS_VoltageDetector</b>         | Show how to use voltage detector to detect pin input voltage.  |
| <b>TIMER_ACMPTrigger</b>           | Show how to use ACMP0 to trigger Timer capture event.  |
| <b>TIMER_CaptureCounter</b>        | Show how to use the timer2 capture function to capture timer2 counter value.   |
| <b>TIMER_Delay</b>                 | Show how to use timer0 to create various delay time.   |
| <b>TIMER_EventCounter</b>          | Implement timer1 event counter function to count the external input event.   |
| <b>TIMER_InterTimerTriggerMode</b> | Demonstrate how to use Inter-Timer trigger function.   |



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| <b>TIMER_PeriodicINT</b>        | Implement timer counting in periodic mode.   |
| <b>TIMER_PWM_Brake</b>          | Generate Timer brake event by Timer brake pin.   |
| <b>TIMER_PWM_ChangeDuty</b>     | Change duty cycle and period of output waveform in PWM down count type.  |
| <b>TIMER_PWM_DeadTime</b>       | Demonstrate Timer PWM Complementary mode and Dead-Time function.   |
| <b>TIMER_PWM_OuputWaveform</b>  | Demonstrate output different duty waveform in Timer0~Timer3 PWM.   |
| <b>TIMER_TimeoutWakeup</b>      | Use timer0 periodic time-out interrupt event to wake up system.  |
| <b>TIMER_ToggleOut</b>          | Implement timer counting in toggle-output mode.  |
| <b>UART_AutoBaudRate_Master</b> | Show how to use auto baud rate detection function. This sample code needs to work with UART_AutoBaudRate_Slave.  |
| <b>UART_AutoBaudRate_Slave</b>  | Show how to use auto baud rate detection function. This sample code needs to work with UART_AutoBaudRate_Master. |
| <b>UART_Autoflow_Master</b>     | Transmit and receive data with auto flow control. This sample code needs to work with UART_Autoflow_Slave.       |
| <b>UART_Autoflow_Slave</b>      | Transmit and receive data with auto flow control. This sample code needs to work with UART_Autoflow_Master.      |
| <b>UART_IrDA_Master</b>         | Transmit and receive data in UART IrDA mode. This sample code needs to work with UART_IrDA_Slave.                |
| <b>UART_IrDA_Slave</b>          | Transmit and receive data in UART IrDA mode. This sample code needs to work with UART_IrDA_Master.               |
| <b>UART_LIN</b>                 | Transmit LIN frame including header and response in UART LIN mode.   |
| <b>UART_PDMA</b>                | Transmit and receive UART data with PDMA.  |
| <b>UART_RS485_Master</b>        | Transmit and receive data in UART RS485 mode. This sample code needs to work with UART_RS485_Slave.              |
| <b>UART_RS485_Slave</b>         | Transmit and receive data in UART RS485 mode. This   |

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|                                       | sample code needs to work with UART_RS485_Master.   |
| <b>UART_TxRxFunction</b>              | Transmit and receive data from PC terminal through RS232 interface.   |
| <b>UART_Wakeup</b>                    | Show how to wake up system from Power-down mode by UART interrupt.  |
| <b>USBD_HID_Keyboard</b>              | Show how to implement a USB keyboard device. This sample code supports to use GPIO to simulate key input.   |
| <b>USBD_HID_Mouse</b>                 | Show how to implement a USB mouse device. The mouse cursor will move automatically when this mouse device connecting to PC by USB.  |
| <b>USBD_HID_Mouse2</b>                | Demonstrate how to implement a USB mouse device. It uses PC0 ~ PC5 to control mouse directions and mouse keys. It also supports USB suspend and remote wake-up.   |
| <b>USBD_HID_MouseKeyboard</b>         | Demonstrate how to implement a USB mouse function and a USB keyboard on the same USB device. The mouse cursor will move automatically when this mouse device connecting to PC. This sample code uses a GPIO to simulate key input.    |
| <b>USBD_HID_Transfer</b>              | Transfer data between USB device and PC through USB HID interface. A windows tool is also included in this sample code to connect with USB device.  |
| <b>USBD_HID_Transfer_and_Keyboard</b> | Demonstrate how to implement a composite device. (HID Transfer and keyboard) Transfer data between USB device and PC through USB HID interface. A windows tool is also included in this sample code to connect with USB device.       |
| <b>USBD_HID_Transfer_and_MSC</b>      | Demonstrate how to implement a composite device. (HID Transfer and Mass storage) Transfer data between USB device and PC through USB HID interface. A windows tool is also included in this sample code to connect with a USB device. |
| <b>USBD_MassStorage_CDROM</b>         | Demonstrate how to simulate a USB CD-ROM device.  |
| <b>USBD_MassStorage_DataFlash</b>     | Use embedded Data Flash as storage to implement a USB Mass-Storage device.  |
| <b>USBD_Micro_Printer</b>             | Show how to implement a USB micro printer device.   |

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| <b>USBD_Printer_and_HID_Transfer</b> | Demonstrate how to implement a composite device (USB micro printer device and HID Transfer). Transfer data between a USB device and PC through a USB HID interface. A windows tool is also included in this sample code to connect with a USB device.                   |
| <b>USBD_VCOM_and_HID_Keyboard</b>    | Implement a USB composite device with virtual COM port and keyboard functions.  |
| <b>USBD_VCOM_and_HID_Transfer</b>    | Demonstrate how to implement a composite device (VCOM and HID Transfer). It supports one virtual COM port and transfers data between a USB device and PC through a USB HID interface. A windows tool is also included in this sample code to connect with a USB device. |
| <b>USBD_VCOM_and_MassStorage</b>     | Implement a USB composite device. It supports one virtual COM port and one USB Mass-Storage device.   |
| <b>USBD_VCOM_DualPort</b>            | Demonstrate how to implement a USB dual virtual COM port device.  |
| <b>USBD_VCOM_SinglePort</b>          | Implement a USB virtual COM port device. It supports one virtual COM port.  |
| <b>USCI_I2C_EEPROM</b>               | Demonstrate how to access EEPROM through a USCI_I2C interface.  |
| <b>USCI_I2C_Loopback</b>             | Demonstrate how a Master accesses Slave.  |
| <b>USCI_I2C_Loopback_10bit</b>       | Demonstrate how a Master uses 10-bit addressing access Slave.   |
| <b>USCI_I2C_Master</b>               | Demonstrate how a Master access Slave. This sample code needs to work with I2C_Slave.   |
| <b>USCI_I2C_Master_10bit</b>         | Demonstrate how a Master use 10-bit addressing access Slave. This sample code needs to work with I2C_Slave.   |
| <b>USCI_I2C_Slave</b>                | Demonstrate how to set I2C in slave mode to receive the data from a Master. This sample code needs to work with I2C_Master.   |
| <b>USCI_I2C_Slave_10bit</b>          | Demonstrate how to set I2C in 10-bit addressing slave mode to receive the data from a Master. This sample code needs to work with I2C_Master.   |
| <b>USCI_I2C_Wakeup_Slave</b>         | Demonstrate how to set I2C to wake up MCU from Power-   |

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|                                      | down mode. This sample code needs to work with I2C_Master.  |
| <b>USCI_SPI_Loopback</b>             | Implement USCI_SPI1 Master loop back transfer. This sample code needs to connect USCI_SPI1_MISO pin and USCI_SPI1_MOSI pin together. It will compare the received data with transmitted data. |
| <b>USCI_SPI_MasterMode</b>           | Configure USCI_SPI1 as Master mode and demonstrate how to communicate with an off-chip SPI Slave device. This sample code needs to work with USCI_SPI_SlaveMode.                              |
| <b>USCI_SPI_SlaveMode</b>            | Configure USCI_SPI1 as Slave mode and demonstrate how to communicate with an off-chip SPI Master device. This sample code needs to work with USCI_SPI_MasterMode.                             |
| <b>USCI_UART_AutoBaudRate_Master</b> | Show how to use auto baud rate detection function. This sample code needs to work with USCI_UART_AutoBaudRate_Slave.  |
| <b>USCI_UART_AutoBaudRate_Slave</b>  | Show how to use auto baud rate detection function. This sample code needs to work with USCI_UART_AutoBaudRate_Master.   |
| <b>USCI_UART_Autoflow_Master</b>     | Transmit and receive data with auto flow control. This sample code needs to work with USCI_UART_Autoflow_Slave.   |
| <b>USCI_UART_Autoflow_Slave</b>      | Transmit and receive data with auto flow control. This sample code needs to work with USCI_UART_Autoflow_Master.  |
| <b>USCI_UART_RS485_Master</b>        | Transmit and receive data in RS485 mode. This sample code needs to work with USCI_UART_RS485_Slave.   |
| <b>USCI_UART_RS485_Slave</b>         | Transmit and receive data in RS485 mode. This sample code needs to work with USCI_UART_RS485_Master.  |
| <b>USCI_UART_TxRxFunction</b>        | Transmit and receive data from PC terminal through a RS232 interface.   |
| <b>USCI_UART_Wakeup</b>              | Show how to wake up system from Power-down mode by USCI interrupt in UART mode.   |
| <b>WDT_TimeoutWakeupAndReset</b>     | Implement WDT time-out interrupt event to wake up system and generate time-out reset system event while WDT time-out reset delay period expired.  |
| <b>WWDT_CompareINT</b>               | Show how to reload the WWDT counter value.  |



## 7 REVISION HISTORY

| Date       | Revision | Description  |
|------------|----------|--|
| 2021.01.21 | 3.00.003 | <ol style="list-style-type: none"> <li>1. Modified to pass USB-IF CV-Chapter 9 &amp; Class test of all USBD sample code.</li> <li>2. Fixed Semihost sample code.</li> <li>3. Added SPI_TRIGGER_TX_RX_PDMA and SPI_DISABLE_TX_RX_PDMA API.</li> <li>4. Added Apache-2.0 license declaration in driver source.</li> <li>5. Added README.md file.</li> <li>6. Fixed USCI_SPI driver issue in Library/StdDriver/src/usci_spi.c</li> <li>7. Fixed USPI_SET_SS_HIGH macro in Library/StdDriver/inc/usci_spi.h</li> <li>8. Corrected adc definitions for conversion sampling time selection in Library/StdDriver/inc/adc.h</li> </ol> |
| 2019.11.12 | 3.00.002 | <ol style="list-style-type: none"> <li>1. Add ISP Sample code</li> <li>2. Supports GNU GCC.</li> <li>3. Added Mass Storage sample code to support SD Card.</li> <li>4. Added FMCIDLE_MODULE definition in CLK driver.</li> </ol>   |
| 2018.09.21 | 3.00.001 | <ol style="list-style-type: none"> <li>1. Initially issued.</li> </ol>   |

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