

**ARM® Cortex®-M0**  
**32-bit Microcontroller**

**NuMicro® Family**  
**NUC1261 Series BSP**  
**Revision History**

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**Revision 3.00.003** (Released 2021-0-21)

1. Modified to pass USB-IF CV-Chapter 9 & Class test of all USB D sample code.
2. Fixed Semihost sample code.
3. Added SPI\_TRIGGER\_TX\_RX\_PDMA and SPI\_DISABLE\_TX\_RX\_PDMA API.
4. Added Apache-2.0 license declaration in driver source.
5. Added README.md file.
6. Fixed USCI\_SPI driver issue in Library/StdDriver/src/usci\_spi.c
7. Fixed USPI\_SET\_SS\_HIGH macro in Library/StdDriver/inc/usci\_spi.h
8. Corrected adc definitions for conversion sampling time selection in Library/StdDriver/inc/adc.h

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**Revision 3.00.002** (Released 2019-11-12)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Added Mass Storage sample code to support SD Card.
4. Added FMCIDLE\_MODULE definition in CLK driver.

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**Revision 3.00.001** (Released 2018-09-21)

1. Initial Release

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