

ARM® Cortex®-M0
32-bit Microcontroller

NuMicro® Family
NUC1261 Series BSP
Revision History

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Revision 3.00.004 (Released 2022-08-17)

1. Added PE.0 in Library/Device/Nuvoton/NUC1261/Include/NUC1261.h.
2. Fixed return in main() cause hardfault issue.
3. Added timeout handler for infinite loop.
4. Fixed USB_D_MassStorage_CDROM crash on Linux.
5. Changed the pin selection in USB sample code.
6. Fixed build errors of FMC and I2S sample code.
7. Changed the pin selection in the relevant EBI and TIMER sample code.
8. ACMP output is changed from PD6 to PD7 in ACMP sample code.
9. Rebuilt IAR smartcard lib by IAR 8.xx.
10. Added I2C hang up and recover mechanism for I2C Master and Slave sample code.
11. Modified GPIO number and MFP in
Library/Device/Nuvoton/NUC1261/Include/NUC1261.h.
12. Enabled schmitt trigger of I2C sample code.
13. Fixed UART TX FIFO control issue in USB_D_VCOM sample code.

Revision 3.00.003 (Released 2021-01-21)

1. Modified to pass USB-IF CV-Chapter 9 & Class test of all USB_D sample code.
2. Fixed Semihost sample code.
3. Added SPI_TRIGGER_TX_RX_PDMA and SPI_DISABLE_TX_RX_PDMA API.
4. Added Apache-2.0 license declaration in driver source.
5. Added README.md file.
6. Fixed USCI_SPI driver issue in Library/StdDriver/src/usci_spi.c
7. Fixed USPI_SET_SS_HIGH macro in Library/StdDriver/inc/usci_spi.h
8. Corrected adc definitions for conversion sampling time selection in
Library/StdDriver/inc/adc.h

Revision 3.00.002 (Released 2019-11-12)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Added Mass Storage sample code to support SD Card.
4. Added FMC_IDLE_MODULE definition in CLK driver.

Revision 3.00.001 (Released 2018-09-21)

1. Initial Release

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