

ARM® Cortex®-M0 32-bit Microcontroller

NuMicro[®] Family NUC1261 Series BSP Revision History

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Revision 3.00.005 (Released 2024-05-28)

- 1. Revised some SC sample codes and update SC library.
- 2. Fixed the issue causing execution failure in the USCI-I2C sample code by correcting the erroneous status return.
- 3. Added FMC_SPROM sample code.
- 4. Added slew rate control for SPI sample codes.
- 5. Used "volatile" with the function pointer to disable compiler optimizations in I2C/UI2C Sample code.
- 6. Fixed UI2C Multibytes Master and Singlebyte Master bug issue.
- 7. Modified SYS_PowerDown_MinCurrent and USCI_UART_TxRxFunction sample code.
- 8. Updated USBD HID keyboard sample code to support LED status.
- 9. Added driver and sample code related to USCI SPI PDMA function.
- 10. Added SYS_PowerDown_MinCurrent sample code.
- 11. Modified UART_Read() to return received data count when time-out.
- 12. Modified SYS_UnLockReg() time-out handler.
- 13. Called UART FIFO size from uart.h in USBD VCOM sample code.
- 14. Fixed TX hardware FIFO size to 16 in USBD VCOM sample code.
- 15. Added SYS driver PE0MFP(SYS GPE MFPL[3:0]) bit field definition for NUC1261.

Revision 3.00.004 (Released 2022-08-17)

- 1. Added PE.0 in Library/Device/Nuvoton/NUC1261/Include/NUC1261.h.
- 2. Fixed return in main() cause hardfault issue.
- 3. Added timeout handler for infinite loop.
- 4. Fixed USBD MassStorage CDROM crash on Linux.
- 5. Changed the pin selection in USBD sample code.
- 6. Fixed build errors of FMC and I2S sample code.
- 7. Changed the pin selection in the relevant EBI and TIMER sample code.
- 8. ACMP output is changed from PD6 to PD7 in ACMP sample code.
- 9. Rebuilt IAR smartcard lib by IAR 8.xx.
- 10. Added I2C hang up and recover mechanism for I2C Master and Slave sample code.
- 11. Modified GPIO number and MFP in Library/Device/Nuvoton/NUC1261/Include/NUC1261.h.
- 12. Enabled schmitt trigger of I2C sample code.
- 13. Fixed UART TX FIFO control issue in USBD VCOM sample code.

Revision 3.00.003 (Released 2021-01-21)

- 1. Modified to pass USB-IF CV-Chapter 9 & Class test of all USBD sample code.
- 2. Fixed Semihost sample code.
- 3. Added SPI TRIGGER TX RX PDMA and SPI DISABLE TX RX PDMA API.
- 4. Added Apache-2.0 license declaration in driver source.
- 5. Added README.md file.
- 6. Fixed USCI SPI driver issue in Library/StdDriver/src/usci spi.c
- 7. Fixed USPI SET SS HIGH macro in Library/StdDriver/inc/usci spi.h
- 8. Corrected adc definitions for conversion sampling time selection in Library/StdDriver/inc/adc.h

Revision 3.00.002 (Released 2019-11-12)

- 1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
- 2. Supports GNU GCC.



- 3. Added Mass Storage sample code to support SD Card.4. Added FMCIDLE_MODULE definition in CLK driver.

Revision 3.00.001 (Released 2018-09-21)

1. Initial Release



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