

ARM® Cortex®-M0
32-bit Microcontroller

NuMicro® Family
NUC126 Series BSP
Revision History

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Revision 3.00.006 (Released 2022-08-17)

1. Revised printing error message for I2C sample code.
2. Fixed return in main() cause hardfault issue.
3. Revised infinite loop timeout handler.
4. Fixed USBD_MassStorage_CDROM crash on Linux.
5. Added I2C hang up and recover mechanism for I2C Master and Slave sample code.
6. Opened CESTOPEN in trim function of USBD sample code.
7. Updated auto trim to use SW boundary function of USBD sample code.
8. Enabled schmitt trigger of I2C sample code.
9. Fixed UART TX FIFO control issue in USBD_VCOM sample code.

Revision 3.00.005 (Released 2021-01-21)

1. Fixed USCI_SPI driver issue in Library/StdDriver/src/usci_spi.c
2. Modified to pass USB-IF CV-Chapter 9 & Class test of all USBD Sample code.
3. Fixed USPI_SET_SS_HIGH macro in Library/StdDriver/inc/usci_spi.h
4. Corrected adc definitions for conversion sampling time selection in Library/StdDriver/inc/adc.h
5. Added Apache-2.0 license declaration in driver source.
6. Added README.md file.
7. Added SPI_TRIGGER_TX_RX_PDMA and SPI_DISABLE_TX_RX_PDMA API.

Revision 3.00.004 (Released 2019-10-25)

1. To support GNU GCC.
2. Add emWin supporting.
3. Improve USB driver reliability.
4. Add ISP firmware sample code.

Revision 3.00.003 (Released 2018-01-26)

1. Added CLK_SysTickLongDelay() function for long delay.
2. Improved baud rate calculation accuracy of UART driver.
3. Improved robust of USB sample code.

Revision 3.00.002 (Released 2017-05-05)

1. Update multi-function pin name definitions.
2. Add single byte and multi byte write/read APIs for I2C.

Revision 3.00.001 (Released 2016-10-17)

1. Initial Release

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