

ARM® Cortex®-M 32-bit Microcontroller

NuMicro® Family NANO100AN Series CMSIS BSP Revision History

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Revision 3.03.001 (Released 2024-04-17)

1. Update USBD keyboard samples to support LED status

Revision 3.03.000 (Released 2023-01-10)

- 2. Fixed USBD VCOM UART TX control issue
- 3. Modify USBD HID bInterval to 10
- 4. Fix USBD_CDROM Linux/MAC compatible issue
- 5. Add timeout to infinite loops in drivers
- 6. Minor bug fix.

Revision 3.02.002 (Released 2020-10-8)

- 7. Added Apache-2.0 license declaration into driver source code.
- 8. Minor bug fix.

Revision 3.02.001 (Released 2019-11-7)

- 1. Added ISP related samples.
- 2. Added sample USBD_MassStorage_SDCard.
- 3. Minor bug fix.

Revision 3.02.000 (Released 2018-09-17)

- 1. Added Eclipse project support.
- 2. Minor bug fix.

Revision 3.01.002 (Released 2017-03-10)

- 1. Fixed smartcard driver and library behaviors that do not comply with EMV2000 spec.
- 2. Fixed CCID sample bug that incorrect error code is returned.
- Updated SC_ReadSimPhoneBook sample code to support SIM card with CHV1 disabled.

Revision 3.01.001 (Released 2016-07-28)

- 1. Updated CMSIS to v4.5.0.
- 2. Added sample code USBD HID Transfer CTRL.
- 3. Fixed the HIDTransferTest.exe bug to use correct sector size to compare data.
- 4. Updated HIDTransferTest.exe to support the connection of the composite device with HID interface number other than 0.
- 5. Fixed the USBD_VCOM_SerialEmulator and USBD_VCOM_And_HID_Keyboard samples bug to ensure SET_LINE_CODE command is properly handled.
- 6. Minor bug fixes.

Revision 3.01.000 (Released 2015-07-03)

- Removed FMC driver's FMC_SetBootSource(),FMC_DisableAPUpdate(),FMC_DisableConfigUpdate(),FMC_ DisableLDUpdate(),FMC_EnableAPUpdate(),FMC_EnableConfigUpdate(),FMC_EnableLDUpdate() in fmc.h, because there exist functionally identical macros.
- 2. Removed DAC driver dac.c and dac.h. Removed samples DAC_PDMATrigger, DAC_SoftwareTrigger, and DAC_TimerTrigger. Removed register and interrupt definitions in Nano100Series.h.
- 3. Renamed sample GPIO as GPIO_IOTest.
- 4. Renamed sample PDMA as PDMA_Memory.



- 5. Renamed sample SYS as SYS Control.
- 6. Fixed IAR project device type selection mistakes of all samples.
- 7. Fixed SC_SET_STOP_BIT_LEN implementation error in sc.h.
- 8. Fixed the bug that timer_delay() set prescale in wrong register in timer.c.
- 9. Fixed clock driver hard fault problem by dividing HCLK prior to apply new HCLK clock source in clk.c.
- 10. Fixed CLK_PLLCTL_FB_DV_Msk implementation error in Nano100Series.h.
- 11. Fixed FMC APROM END definition in fmc.h.
- 12. Fixed USB device compliant issues, including samples USBD_HID_Mouse, USBD_HID_Transfer, USBD_Mass_Storage_Flash, and USBD_VCOM_SerialEmulator, USBD driver and header files are also updated.
- 13. Fixed UART baudrate setting errors in samples USBD_VCOM_And_HID_Keyboard and USBD_VCOM_SerialEmulator.
- 14. Modified Timer_Open() to not start timer in it, in timer.c.
- 15. Moved SYS_Unlockreg() from sys.c to sys.h.
- 16. Update GPIO driver, insert delay to wait I/O stable in gpio.c.
- 17. Update sample GPIO_PowerDown, unlock control registers when enter power mode.
- 18. Added sample SYS_TrimIRC.
- 19. Added sample USBD_HID_Keyboard.
- 20. Added sample USBD HID MouseKeyboard.
- 21. Added sample USBD_HID_Touch.
- 22. Added sample USBD_HID_Transfer_And_Keyboard.
- 23. Added sample USBD_HID_Transfer_And_MSC.
- 24. Added sample USBD_Mass_Storage_CDROM.
- 25. Added sample USBD VCOM And HID Keyboard.
- 26. Added sample PWRDWN DEMO.

Revision 3.00.000 (Released 2014-10-31)

1. Primary release version.



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