ARM® Cortex®-M23

32-bit Microcontroller

NuMicro® Family
NUC1263 Series BSP
Revision History

For additional information or questions, please contact: Nuvoton Technology Corporation.

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Revision 3.00.003 (Released 2023-07-17)

1. Revise ADC driver for sampling HSA.
2. Add spdh GCC Lib.
3. Remove SPDH sample code.

Revision 3.00.002 (Released 2023-06-15)

1. Support block write mode in SPDH_LLSIDevice.
2. Add spdh_device library for HUB Device application.
3. Modify I2C_Master sample code to support 10 bit mode by compiler option.
4. Change the verification conditions for read and write operations in I2C_Master to check each address group.
5. Add MR register settings file for LLSI device.
6. Modify VendorName of Nu_DFU.inf.
7. Sync code with internal Hub with lighting device sample code.
8. Add Double Flashing Mode on SPDH_LLSIDevice sample.
9. Add MR55 register to adjust LED speed on SPDH_LLSIDevice sample.
10. Revise I3CS driver.
11. Modify UART_Read() to return received data count when time-out.
12. Update SPI_Loopback sample code.
13. Update SYS_PowerDown_MinCurrent sample code.
14. Remove the PHY type setting of USBD driver in Library/StdDriver/src/usbd.c
15. Modify SYS_UnLockReg() time-out handler in Library/StdDriver/inc/sys.h

Revision 3.00.001 (Released 2022-09-26)

1. Initial Release

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