

PY32T020 series 32-bit ARM® Cortex®-M0+ microcontroller HAL Library Sample Manual

1 ADC

1.1 ADC_AnalogWatchdog

此样例演示了 ADC 的模拟看门狗功能,当开启看门狗的通道的电压值不在设定的上下限中,会进入看门狗中断。

This example demonstrates the analog watchdog function of ADC. When the voltage value of the channel that opens the watchdog is not within the set upper or lower limits, Will enter watchdog interrupt.

1.2 ADC_SingleConversion_TriggerSW_Polling

此样例演示了 ADC 的软件触发和轮询功能。

This example demonstrates the software triggering and polling functions of ADC.

1.3 ADC_SingleConversion_TriggerTimer_IT

此样例演示了 ADC 的 TIM 触发和中断的功能。

This example demonstrates the TIM trigger and interrupt functions of ADC.

1.4 ADC_TempSensor

此样例演示了 ADC 的 Tempsensor 的采样功能。

This sample demonstrates the sampling function of ADC's Tempsensor.

1.5 ADC_VrefbufAndVrefint

此样例演示了 ADC 的 VREFINT 采样功能和 VREFBUF 的功能,通过 VREFINT 推算出 VREFBUF 的电压。

This example demonstrates the VREFINT sampling function and VREFBUF function of ADC, and calculates the voltage of VREFBUF through VREFINT.

2 COMP

2.1 COMP_CompareGpioVs1_2VCC_IT

此样例演示了 COMP 比较器中断功能, PA02 作为比较器负端输入, 1/2VCCA 作为正端输入, 通过调整 PA02 上的输入电压, 当检测到比较器输出状态为高时, LED 灯亮, 比较器输出状态为低时, LED 灯灭。

This example demonstrates the COMP comparator interrupt function, with PA02 as the negative input of the comparator and 1/2VCCA as the positive input. By adjusting the input voltage on PA02, when the comparator output state is detected to be high, the LED light will turn on, and when the comparator output state is low, the LED light will turn off.

2.2 COMP_CompareGpioVs1_2VCC_Polling

此样例演示了 COMP 比较器轮询功能, PA02 作为比较器负端输入, 1/2VCCA 作为正端输入, 通过调整 PA02 上的输入电压, 当检测到比较器输出状态为高时, LED 灯亮, 比较器输出状态为低时, LED 灯灭。

This example demonstrates the COMP comparator polling function, with PA02 as the negative input of the comparator and 1/2VCCA as the positive input. By adjusting the input voltage on PA02, the LED lights up when the comparator output state is detected to be high, and turns off when the comparator output state is low.

2.3 COMP CompareGpioVs1 2VCC WakeUpFromStop

此样例演示了 COMP 比较器唤醒功能, PA0 作为比较器负端输入, 32/64VCC 作为比较器正端输入, 进入 stop 模式后,通过调整 PA0 上的输入电压,产生中断唤醒 stop 模式。

This example demonstrates the COMP comparator wake-up function, with PA0 as the negative input and 32/64VCC as the positive input of the comparator. After entering stop mode, the interrupt wake-up stop mode is generated by adjusting the input voltage on PA0.

2.4 COMP_CompareGpioVs1_2VCC_Window

此样例演示了 COMP 比较器的 window 功能,比较器 1 的 Plus 端用比较器 2 的 IO4(1/2VCCA)作为输入,PB1 作为比较器负端输入,当 PB1 的电压值大于 1.65V 时,LED 灯灭,小于 1.65V 时,LED 灯亮。

This example demonstrates the window function of the COMP comparator. The Plus end of comparator 1 uses the IO4 (1/2VCCA) of comparator 2 as the input, and PB1 as the negative end input. When the voltage value of PB1 is greater than 1.65V, the LED light turns off, and when it is less than 1.65V, the LED light turns on.

3 CRC

3.1 CRC_CalculateCheckValue

此样例演示了 CRC 校验功能,通过对一个数组里的数据进行校验,得到的校验值与理论校验值进行比较,相等则 LED 灯亮,否则 LED 灯熄灭。

This example demonstrates the CRC verification function. By verifying the data in an array, the obtained verification value is compared with the theoretical verification value. If it is equal, the LED light will be on, otherwise the LED light will be off.

4 EXTI

4.1 EXTI_ToggleLed_IT

此样例演示了 GPIO 外部中断功能,PA15 引脚上的每一个上升沿都会产生中断,中断函数中 LED 灯会翻转一次。

This example demonstrates the GPIO external interrupt function, where each rising edge on the PA15 pin generates an interrupt, and the LED light in the interrupt function flips once.

4.2 EXTI_WakeUp_Event

此样例演示了通过 PA6 引脚唤醒 MCU 的功能。下载程序并运行后,LED 灯处于常亮状态;按下用户按键后,LED 灯处于常暗状态,且 MCU 进入 STOP 模式;拉低 PA6 引脚后,MCU 唤醒,LED 灯处于闪烁状态。

This example demonstrates the function of waking up an MCU through the PA6 pin. After downloading the program and running it, the LED light is constantly on; After pressing the user button, the LED light is in a constant dark state and the MCU enters STOP mode; After pulling down the PA6 pin, the MCU wakes up and the LED light is in a flashing state.

5 FLASH

5.1 FLASH_OptionByteWrite_RST

此样例演示了通过软件方式将 RESET 引脚改为普通 GPIO。

This example demonstrates changing the RESET pin to regular GPIO through software.

5.2 FLASH_PageEraseAndWrite

此样例演示了 flash page 擦除和 page 写功能。

This example demonstrates the flash page erase and page write functions.

5.3 FLASH_SectorEraseAndWrite

此样例演示了 flash sector 擦除和 page 写功能。

This example demonstrates the flash sector erase and page write functions.

6 GPIO

6.1 GPIO_FastIO

本样例主要展示 GPIO 的 FAST IO 输出功能,FAST IO 速度可以达到单周期翻转速度。

This example mainly demonstrates the FAST IO output function of GPIO, which can achieve a single cycle flip speed.

6.2 GPIO_Toggle

此样例演示了 GPIO 输出模式,配置 LED 引脚(PA1)为数字输出模式,并且每隔 250ms 翻转一次 LED 引脚电平,运行程序,可以看到 LED 灯以 2Hz 的频率闪烁。

This example demonstrates the GPIO output mode, configuring the LED pin (PA1) to be in digital output mode, and flipping the LED pin level every 250ms. Running the program, you can see that the LED light flashes at a frequency of 2Hz.

7 I2C

7.1 I2C_TwoBoard_CommunicationMaster_IT

此样例演示了 I2C 通过中断方式进行通讯,主机先向从机发送 15byte 数据,然后再接收从机发送的 15byte 数据,主机、从机接收数据成功后,主机和从机板上的小灯处于"常亮"状态。

This sample demonstrates communication between I2C devices using interrupts. The master device sends 15 bytes of data to the slave device and then receives 15 bytes of data from the slave. After successful data transmission and reception between the master and slave, the LEDs on both boards remain constantly lit.

7.2 I2C_TwoBoard_CommunicationMaster_Polling

此样例演示了 I2C 通过轮询方式进行通讯,主机先向从机发送 15byte 数据,然后再接收从机发送的 15byte 数据,主机、从机接收数据成功后,主机和从机板上的小灯处于"常亮"状态。

This sample demonstrates communication between I2C devices using polling. The master device sends 15 bytes of data to the slave device and then receives 15 bytes of data from the slave. After successful data transmission and reception between the master and slave, the LEDs on both boards remain constantly lit.

7.3 I2C_TwoBoard_CommunicationSlave_IT

此样例演示了 I2C 通过中断方式进行通讯,主机先向从机发送 15byte 数据,然后再接收从机发送的 15byte 数据,主机、从机接收数据成功后,主机和从机板上的小灯处于"常亮"状态。

This sample demonstrates communication between I2C devices using interrupts. The master device sends 15 bytes of data to the slave device and then receives 15 bytes of data from the slave. After successful data transmission and reception between the master and slave, the LEDs on both boards remain constantly lit.

7.4 I2C_TwoBoard_MasterTxIndefiniteLengthData_IT

此样例演示了通过中断方式,主机发送不定长数据,从机接收不定长数据。主机向从机发送 10 字节的数据 (0~9),然后从机接收数据 (0~9)并通过串口打印;主机向从机发送 100 字节数据 (1~100),然后从机接收数据 (1~100)并通过串口打印;主机向从机发送 10 字节的数据 (0~9),然后从机接收数据 (0~9)并通过串口打印。

This example demonstrates how the host sends variable length data and the slave receives variable length data through interrupt mode. The host sends 10 bytes of data (0-9) to the slave, and then the slave receives the data (0-9) and prints it through the serial port; The host sends 100 bytes of data (1-100) to the slave, and then the slave receives the data (1-100) and prints it through the serial port; The host sends 10 bytes of data (0-9) to the slave, and then the slave receives the data (0-9) and prints it through the serial port.

7.5 I2C_TwoBoard_SlaveRxIndefiniteLengthData_IT

此样例演示了通过中断方式,主机发送不定长数据,从机接收不定长数据。主机向从机发送 10 字节的数据 (0~9),然后从机接收数据 (0~9)并通过串口打印;主机向从机发送 100 字节数据 (1~100),然后从机接收数据 (1~100)并通过串口打印;主机向从机发送 10 字节的数据 (0~9),然后从机接收数据 (0~9)并通过串口打印。

This example demonstrates how the host sends variable length data and the slave receives variable length data through interrupt mode. The host sends 10 bytes of data (0-9) to the slave, and then the slave receives the data (0-9) and prints it through the serial port; The host sends 100 bytes of data (1-100) to the slave, and then the slave receives the data (1-100) and prints it through the serial port; The host sends 10 bytes of data (0-9) to the slave, and then the slave receives the data (0-9) and prints it through the serial port.

8 IWDG

8.1 IWDG_Reset

此样例演示了 IWDG 看门狗功能,配置看门狗重载计数值,计数 1s 后复位,然后通过调整每次喂狗的时间 (main 函数 while 循环中代码),可以观察到,如果每次喂狗时间小于 1s 钟,程序能一直正常运行 (LED 灯闪烁),如果喂狗时间超过 1s 钟,程序会一直复位 (LED 灯熄灭)。

This example demonstrates the IWDG watchdog function, configuring the watchdog overload count value, resetting after counting for 1 second, and then adjusting each time The feeding time of the dog (code in the main function while loop) can be observed that if the feeding time is less than 1 second each time, the program Can continue to operate normally (LED flashing), if the dog feeding time exceeds 1 second, the program will continue to reset (LED light off).

9 PWR

9.1 PWR_DEEPSTOP_WFE

此样例演示了在 Deepstop 模式下,使用 GPIO 事件唤醒。

This example demonstrates using GPIO event wake-up in Deepstop mode.

9.2 PWR_DEEPSTOP_WFI

此样例演示了在 Deepstop 模式下,使用 GPIO 中断唤醒。

This example demonstrates using GPIO interrupt wake-up in Deepstop mode.

9.3 PWR_HIBERNATE_WFE

此样例演示了在 Hibernate 模式下,使用 GPIO 事件唤醒。

This example demonstrates using GPIO event wake-up in Hibernate mode.

9.4 PWR_HIBERNATE_WFI

此样例演示了在 Hibernate 模式下,使用 GPIO 中断唤醒。

This example demonstrates using GPIO interrupt wake-up in Hibernate mode.

9.5 PWR_SLEEP_WFE

此样例演示了在 sleep 模式下,使用 GPIO 事件唤醒。

This example demonstrates using GPIO event wake-up in sleep mode.

9.6 PWR_SLEEP_WFI

此样例演示了在 sleep 模式下,使用 GPIO 中断唤醒。

This example demonstrates using GPIO interrupt wake-up in sleep mode.

9.7 PWR_STOP_WFE

此样例演示了在 stop 模式下,使用 GPIO 事件唤醒。

This example demonstrates using GPIO event wake-up in stop mode.

9.8 PWR_STOP_WFI

此样例演示了在 stop 模式下,使用 GPIO 中断唤醒。

This example demonstrates using GPIO interrupt wake-up in stop mode.

10 RCC

10.1 RCC_HSEOutput

此样例配置系统时钟为 HSE, 并通过 MCO (PA08) 引脚输出。

This sample configures the system clock as HSE and outputs it through the MCO (PA08) pin.

10.2 RCC_HSIOutput

此样例配置系统时钟为 HSI, 并通过 MCO (PA08) 引脚输出。

This sample configures the system clock as HSI and outputs it through the MCO (PA08) pin.

10.3 RCC_LSEOutput

此样例配置系统时钟为 LSE,并通过 MCO (PA08) 引脚输出。注意系统时钟切换为 LSE 之前,要求把 SysTick 中断关闭掉,因为 SysTick 中断默认是 1ms 一次中断,由于 LSE 时钟频率过低,SysTick 中断 会导致程序无法正常运行。

This sample configures the system clock as LSE and outputs it through the MCO (PA08) pin. Note that before switching the system clock to LSE, it is required to disable the SysTick interrupt. This is because the SysTick interrupt defaults to occur every 1ms, and due to the low frequency of LSE, the SysTick interrupt can disrupt the normal operation of the program.

10.4 RCC_LSIOutput

此样例配置系统时钟为 LSI,并通过 MCO (PA08) 引脚输出。注意系统时钟切换为 LSI 之前,要求把 SysTick 中断关闭掉,因为 SysTick 中断默认是 1ms 一次中断,由于 LSI 时钟频率过低,SysTick 中断 会导致程序无法正常运行。

This sample configures the system clock as LSI and outputs it through the MCO (PA08) pin. Note that before switching the system clock to LSI, it is required to disable the SysTick interrupt. This is because the SysTick interrupt defaults to occur every 1ms, and due to the low frequency of LSI, the SysTick interrupt can disrupt the normal operation of the program.

10.5 RCC SysclockSwitch

此样例演示系统时钟切换功能。样例中配置系统时钟从 HSI 切换到 HSE, 并通过 MCO (PA08) 引脚输出系统时钟。

This sample demonstrates the system clock switching functionality. The sample configures the system clock to switch from HSI to HSE and outputs the system clock through the MCO (PA08) pin.

Puya Semiconductor 13 / 22

11 RTC

11.1 RTC_AlarmSecond_IT

此样例演示 RTC 的秒中断和闹钟中断功能。每次秒中断,在中断函数中会打印字符"RTC_IT_SEC"并输出实时时间。

This sample demonstrates the RTC's second interrupt and alarm interrupt functionality. Each time the second interrupt occurs, the interrupt function prints the string "RTC_IT_SEC" and outputs the current RTC count time.

11.2 RTC_WakeUpAlarm

此样例演示通过 RTC 闹钟中断每隔 1 秒将 MCU 从 STOP 模式下唤醒,并且每次唤醒会翻转 LED, LED 翻转间隔为 1 秒。

This sample demonstrates waking up the MCU from STOP mode every 1 second using RTC alarm interrupt. Each time the MCU wakes up, the LED will toggle its state. The LED toggling interval is 1 second.

11.3 RTC_WakeUpSecond

此样例演示了通过 RTC 的秒中断唤醒 MCU 的功能。下载程序并运行后,LED 灯处于常亮状态;按下用户按键后,LED 灯处于常暗状态,且 MCU 进入 STOP 模式;RTC 秒中断唤醒 MCU 后,LED 灯处于闪烁状态。

This sample demonstrates waking up the MCU using RTC second interrupt. After downloading and running the program, the LED is continuously on. Pressing the user button turns off the LED and puts the MCU into STOP mode. When the RTC second interrupt wakes up the MCU, the LED starts blinking.

12 SPI

12.1 SPI_TwoBoards_FullDuplexMaster_IT

此样例是利用中断对串口外设接口(SPI)与外部设备以全双工串行方式进行通信的演示,此接口设置为主模式,为外部从设备提供通信时钟 SCK。主机通过 MOSI 引脚发送数据,从 MISO 引脚接收从机的数据,数据以主机提供的 SCK 沿同步被移位,完成全双工通信。

This example is a demonstration of using interrupts to communicate with the serial peripheral interface (SPI) and external devices in a full duplex serial mode. This interface is set as the main mode and provides communication clock SCK for external slave devices. The master sends data through the MOSI pin and receives data from the slave through the MISO pin. The data is synchronously shifted along the SCK provided by the master, completing full duplex communication.

12.2 SPI_TwoBoards_FullDuplexMaster_Polling

此样例是通过轮询方式对串口外设接口(SPI)与外部设备以全双工串行方式进行通信的演示,此接口设置为主模式,为外部从设备提供通信时钟 SCK。主机通过 MOSI 引脚发送数据,从 MISO 引脚接收从机的数据,数据以主机提供的 SCK 沿同步被移位,完成全双工通信。

This example is a demonstration of communication between the serial peripheral interface (SPI) and external devices in full duplex serial mode through polling. This interface is set as the main mode and provides communication clock SCK for external slave devices. The host sends data through the MOSI pin and receives data from the slave through the MISO pin. The data is synchronously shifted along the SCK provided by the host, completing full duplex communication.

12.3 SPI_TwoBoards_FullDuplexSlave_IT

此样例是利用中断对串口外设接口(SPI)与外部设备以全双工串行方式进行通信的演示,此接口设置为主模式,为外部从设备提供通信时钟 SCK。主机通过 MOSI 引脚发送数据,从 MISO 引脚接收从机的数据,数据以主机提供的 SCK 沿同步被移位,完成全双工通信。

This example is a demonstration of using interrupts to communicate with the serial peripheral interface (SPI) and external devices in a full duplex serial mode. This interface is set as the main mode and provides communication clock SCK for external slave devices. The master sends data through the MOSI pin and receives data from the slave through the MISO pin. The data is synchronously shifted along the SCK provided by the master, completing full duplex communication.

12.4 SPI_TwoBoards_FullDuplexSlave_Polling

此样例是通过轮询方式对串口外设接口(SPI)与外部设备以全双工串行方式进行通信的演示,此接口设置为主模式,为外部从设备提供通信时钟 SCK。主机通过 MOSI 引脚发送数据,从 MISO 引脚接收从机的数据,数据以主机提供的 SCK 沿同步被移位,完成全双工通信。

This example is a demonstration of communication between the serial peripheral interface (SPI) and external devices in full duplex serial mode through polling. This interface is set as the main mode and provides communication clock SCK for external slave devices. The host sends data through the MOSI pin and receives data from the slave through the MISO pin. The data is synchronously shifted along the SCK provided by the host, completing full duplex communication.

13 TIM

13.1 TIM14_LSICalibrate

此样例配置 LSI 时钟从 MCO(PA8)输出并将 TIM14 的通道 1 连接到 MCO。将系统时钟配置为 24MHz, TIM14 时钟为 240KHz,设置重载值为 10001,使能 TIM14 的输入捕获功能,调整 LSI trimming 值, LED 亭起表示校准完成。

This example configures the LSI clock to output from MCO (PA8) and connects channel 1 of TIM14 to MCO. Set the system clock to 24MHz, set the clock of TIM14 to 240KHz, set the overload value to 10001, enable the input capture function of TIM14, adjust the value of LSI trimming, and the LED ON indicates that calibration is complete.

13.2 TIM1 6Step

此样例演示了使用 TIM1 产生"六步 PWM 信号",每间隔 1ms 在 SysTick 中断中触发换向,实现无刷电机的换向。

This example demonstrates the use of TIM1 to generate a "six step PWM signal", which triggers commutation in the SysTick interrupt every 1ms to achieve commutation of a brushless motor.

13.3 TIM1_AutoReloadPreload

此样例实现了定时器的基本计数功能,以及演示了 ARR 自动重载功能,样例在定时器重载中断中翻转 LED 灯 修 改 main.c 中 的 第 56 行 配 置 TimHandle.Init.AutoReloadPreload = TIM_AUTORELOAD_PRELOAD_ENABLE;使能自动重载功能,新的 ARR 值在第四次进中断时生效,配置 TimHandle.Init.AutoReloadPreload = TIM_AUTORELOAD_PRELOAD_DISABLE;禁止自动重载功能,新的 ARR 值在第三次进中断时生效,上ED 灯以 2.5HZ 的频率翻转

This sample demonstrates base count function of the timer, and show ARR register autoreload function. Example toggle LED in timer update interrupt. Modify line 56 in main.c. Set TimHandle.Init. AutoReloadPreload = TIM_AUTORELOAD_PRELOAD_ENABLE to enable autoreload, and new ARR value will takes effect on the fourth interrupt generate. Set TimHandle. Init. AutoReloadPreload = TIM_AUTORELOAD_PRELOAD_DISABLE to disable autoreload, and new ARR value will takes effect on the third interrupt generate. After taking effect, the LED lights blinked at a frequency of 2.5 HZ.

13.4 TIM1_ComplementarySignals

此样例实现了定时器的互补输出功能,三组互补共六路 pwm 输出。

This sample demonstrates complementary output function of the timer, Three sets of complementary outputs total six pwm outputs.

13.5 TIM1_ComplementarySignals_DeadTime

此样例实现了定时器的刹车功能,CH1 和 CH1N 互补 pwm 输出,接收到外部 IO 口的刹车信号(低电平)后,PWM 信号关闭,由于 BDTR.AOE 置位,所以刹车信号取消(高电平)后,继续 pwm 输出,此样例实现了死区功能。通过调整 OCxE,CCxP,OISx,CCxNE,CCxNP,OISxN 的配置,可实现刹车功能的各种应用

This sample demonstrates brake function of the timer, the CH1 and CH1N complementary pwm outputs. After receiving the brake signal (low level) from the external IO port, the PWM signal is turned off. Because BDTR. AOE is set, the pwm output continues after the brake signal is cancelled (high level). This example realizes the dead zone function. By adjusting the OCxE, CCxP, OISx, CCxNE, CCxNP, OISxN configuration, which can realize the brake function of a variety of applications

13.6 TIM1_EncoderTI2AndTI1

此样例实现了 TIM1 中的编码器计数功能,TI1(PA3)和 TI2(PA5)作为编码器输入引脚,通过 CNT 寄存器可观察到计数器变化,通过 uwDirection 变量可观察到计数器的计数方向,通过打印数据也可观察计数方向和 CNT 寄存器计数值,打印数据 Direction = 0 为向上计数,Direction = 1 为向下计数。

This sample demonstrates encoder count function of the TIM1,TI1(PA3) and TI2(PA5) configured as encoder input pins. The change of the counter can be observed through the CNT register, and the counting direction of the counter can be observed through the uwDirection variable. The counting Direction and CNT register can also be observed by printing data. The printed data Direction = 0 indicates CounterMode: Up, and direction = 1 indicates CounterMode: down.

13.7 TIM1 ExternalClockMode1

此样例演示了 TIM1 的外部时钟模式 1 功能,选择 ETR(PA12)引脚作为外部时钟输入源,并使能更新中断,在中断中翻转 LED 灯

This sample demonstrates external clock mode 1 function of the TIM1. Select the ETR(PA12) pin as the external clock input source and enable the update interrupt to flip the LED light in the interrupt.

13.8 TIM1_ExternalClockMode1_TI1F

此样例演示了 TIM1 的外部时钟模式 1 功能,选择 TI1FP(PA3)引脚作为外部时钟输入源,并使能更新中断,在中断中翻转 LED 灯

This sample demonstrates the external clock mode 1 function of TIM1, selects the TI1FP(PA3) pin as the external clock input source, and enables the update interrupt and toggle the LED light in the interrupt

13.9 TIM1_ExternalClockMode2

此样例演示了 TIM1 的外部时钟模式 2 功能,选择 ETR(PA12)引脚作为外部时钟输入源,并使能更新中断,在中断中翻转 LED 灯。

This sample demonstrates the external clock mode 2 function of TIM1, selects the ETR(PA12) pin as the external clock input source, and enables the update interrupt and toggle the LED light in the interrupt

13.10 TIM1_InputCapture_TI1FP1

此样例演示了在 TIM1(PA3)输入捕获功能,PA3 输入时钟信号,TIM1 捕获成功后,会进入捕获中断,每进一次中断,翻转一次 LED

This sample demonstrates the input capture function of TIM1(PA3), PA3 input clock signal, when TIM1 capture success, will enter the capture interrupt, and toggle the LED in the interrupt

13.11 TIM1_InputCapture_XORCh1Ch2Ch3

此样例演示了 TIM1 的三通道异或输入捕获功能。配置 PA3、PA5、PA4 为通道 1、通道 2、通道 3 的输入引脚。每当有一个引脚电平变化时会触发捕获中断,并在中断处理中翻转 LED。

This example demonstrates the three channel XOR input capture function of TIM1. Configure PA3, PA5, and PA4 as input pins for channels 1, 2, and 3. Whenever a pin level changes, a capture interrupt is triggered and the LED is flipped during interrupt processing.

13.12 TIM1_OC_Toggle

此样例演示了 TIM1 的输出比较模式。将捕获/比较通道 1 (CH1) 的输出映射到 PA3, 开启捕获/比较通道 1 (CH1) 并设置为比较输出翻转模式。

This example demonstrates the output comparison mode of TIM1. Map the output of capture/compare channel 1 (CH1) to PA3, turn on capture/compare channel 1 (CH1), and set it to compare output flipping mode.

13.13 TIM1_OnePulseOutput

此样例演示了 TIM1 的单脉冲模式, CH2(PA05)引脚上的上升沿, 触发计数器开始计数, 当计数值与 CCR1 匹配时, CH1(PA03)输出高电平, 直到计数器溢出, CH1 再次输出低电平, 计数器溢出后, 定时器停止工作, 本例程脉冲宽度计算 (TIM1 ARR-TI1 CCR1)/CLK=(65535-16383)/24000000=2.048ms

This sample demonstrates the one pulse mode of TIM1. The rising edge on the CH2(PA05) pin triggers the counter to start counting, when the count value matches CCR1, CH1(PA03) outputs a high level. When the counter overflows, CH1 outputs the low level again. After the counter overflows,

Puya Semiconductor 20 / 22

the timer stops working. This example pulse width calculation (TIM1_ARR-TI1_CCR1)/CLK= (65,535-16383)/ 24,000,000 =2.048ms

13.14 TIM1_PWM

本例程输出 4 路 PWM,通道 1 的占空比为 20%,通道 2 为 40%,通道 3 为 60%,通道 4 为 80%本例程周期为 24000000/2000/1200=10Hz。

This routine outputs 4 PWM channels, with a duty cycle of 20% for channel 1, 40% for channel 2, 60% for channel 3, and 80% for channel 4 The cycle of this routine is 24000000/2000/1200=10Hz.

13.15 TIM1_Update_IT

此样例演示了 TIM1 的更新中断功能,在更新中断中翻转 LED。

This example demonstrates the update interrupt function of TIM1, flipping the LED during the update interrupt.

14 UART

14.1 UART_HyperTerminal_IT

此样例演示了 UART 的中断方式发送和接收数据, UART 配置为 115200, 数据位 8, 停止位 1, 校验位 None,下载并运行程序后,上位机通过 UART 会接收到 0x1-0xC,然后通过上位机下发 12 个数据,例如 0x1~0xC,则,MCU 会把接收到的数据再次发送到上位机。

This example demonstrates how to use UART to send an amount of data in interrupt mode UART configuration is 115200 baud rate, data bit 8, stop bit 1, check bit None. After download and run the program, the upper computer will receive 0x1-0xC Then send 12 data(such as 0x1~0xC)by the host computer, and the MCU will send the received data again.

14.2 UART_HyperTerminal_Polling

此样例演示了 UART 的 POLLING 方式发送和接收数据,UART 配置为 115200,数据位 8,停止位 1,校验位 None,下载并运行程序后,通过 UART 会接收到 0x1-0xC,然后通过上位机下发 12 个数据,例如 0x1~0xC,MCU 会把接收到的数据再次发送。

This example demonstrates how to use UART to send an amount of data in polling mode UART configuration is 115200 baud rate, data bit 8, stop bit 1, check bit None. After download and run the program, the upper computer will receive 0x1-0xC Then send 12 data(such as 0x1~0xC)by the host computer, and the MCU will send the received data again.