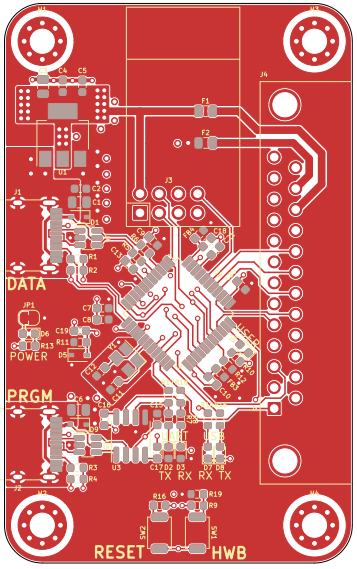
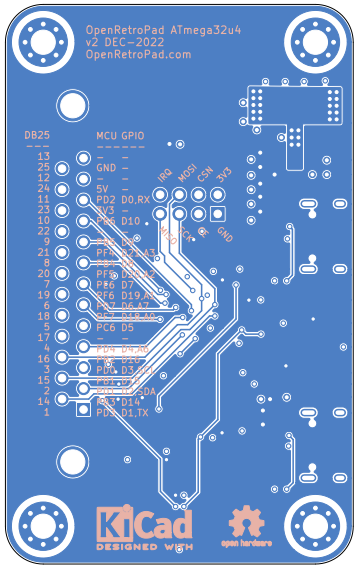
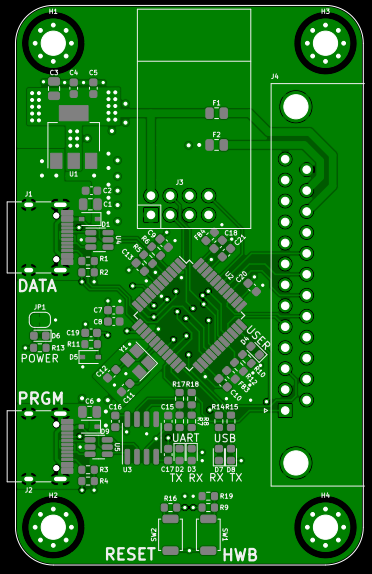

DATA
POWER
PRGM
RESET
HWB
TX RX RX TX


OpenRetroPad ATmega32u4
v2 DEC-2022
OpenRetroPad.com
DB25
MCU GPIO
13 -
25 GND -
12 -
24 5V -
11 PD2 D0,RX
23 3V3 -
10 PB6 D10
22
9
21 PF4 D18,A3
8
20 PF5 D19,A2
7 PB6 D7
19 PF6 D19,A1
6 PF7 D6,A7
18 PC7 D13,A5
5 PC6 D5
17 -
4 PD4 D4,A8
16 PD6 D12
3 PD3 D3,SCL
15 PD1 D2,SDA
2 PB5 D14
14 -
1 PD5 D1,TX
IRQ
MOSI
CSN
3V3
SCK
MISO
GND
KiCad
DESIGNED WITH
open hardware


H1
H3
C3
C4
C5
J4
F1
F2
U1
J3
J1
C2
C1
D1
R1
R2
DATA
JP1
C7
C8
D6
C19
R13
R11
D5
POWER
C13
C12
C11
C10
C9
C18
C21
C20
C22
C23
C24
C25
C26
C27
C28
C29
C30
C31
C32
C33
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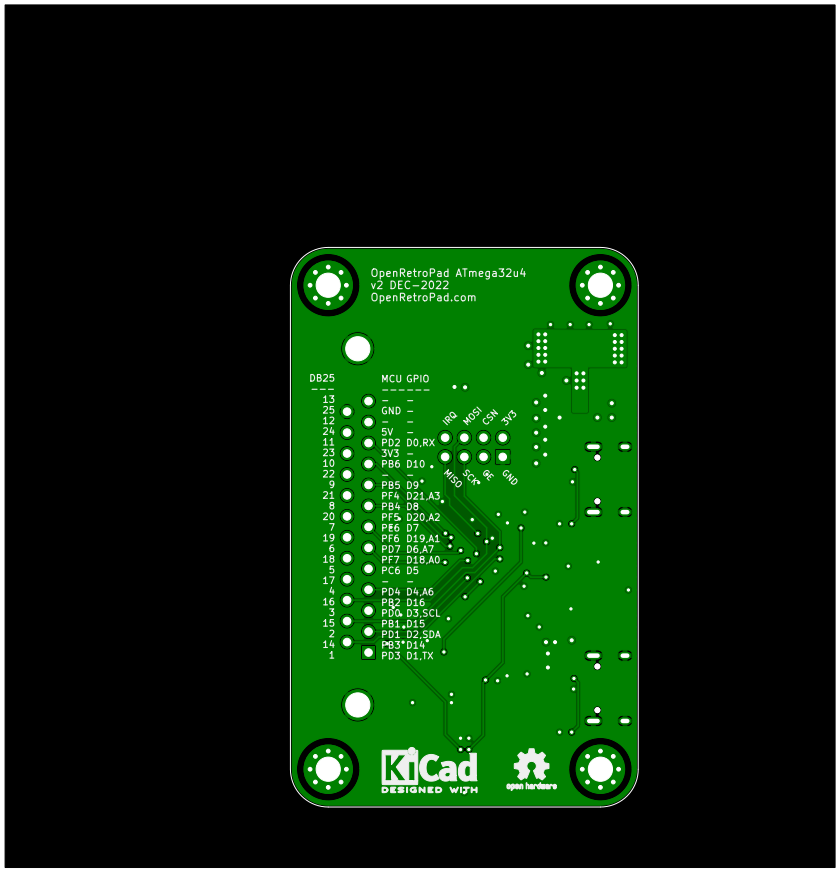
C38
C39
C40
C41
C42
C43
C44
C45
C46
C47
C48
USER
PRGM
C6
C16
R17R18
C15
R14R15
UART
USB
TX RX RX TX
U3
C17D2 D3
D7 D8
R3
R4
J2
H2
H4
R16
R19
R9
SW2
SW1
RESET
HWB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9 PB5 D9
21 PF4 D21,A3
8 PB4 D8
20 PF5 D20,A2
7 PE6 D7
19 PF6 D19,A1
6 PD7 D6,A7
18 PF7 D18,A0
5 PC6 D5
17 -
4 PD4 D4,A6
16 PB2 D16
3 PD0 D3,SCL
15 PB1 D15
2 PD1 D2,SDA
14 PB3 D14
1 PD3 D1,TX
IRQ
MOSI
CSN
3V3
MISO
SCK
CE
GND
KiCad
DESIGNED WITH
open hardware