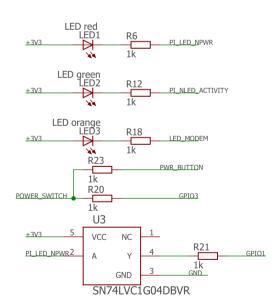
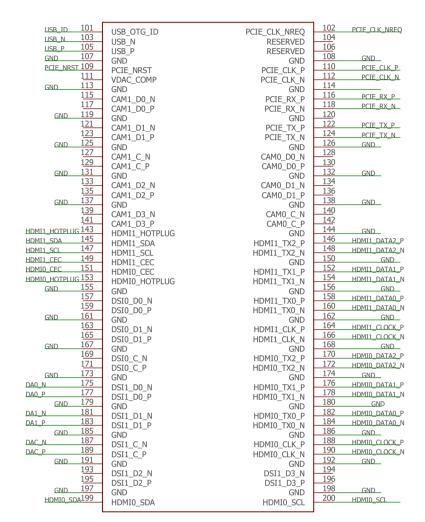
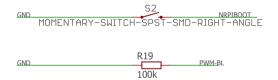


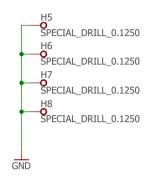
RPI-CM4_NO_HOLES

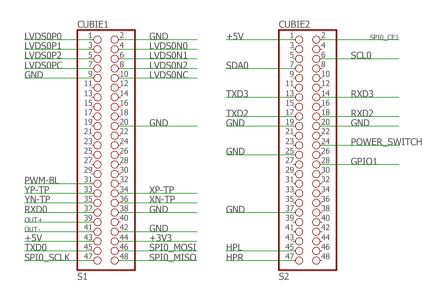
U\$1

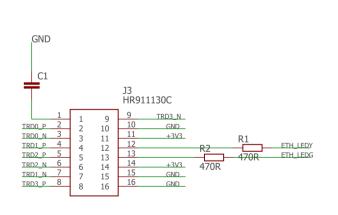


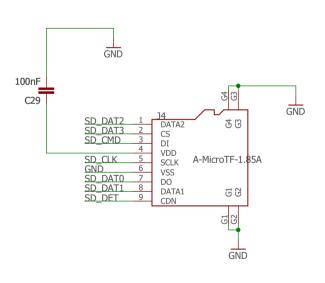


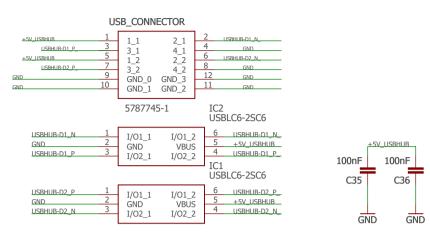


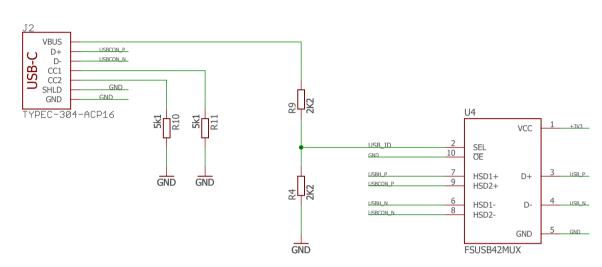


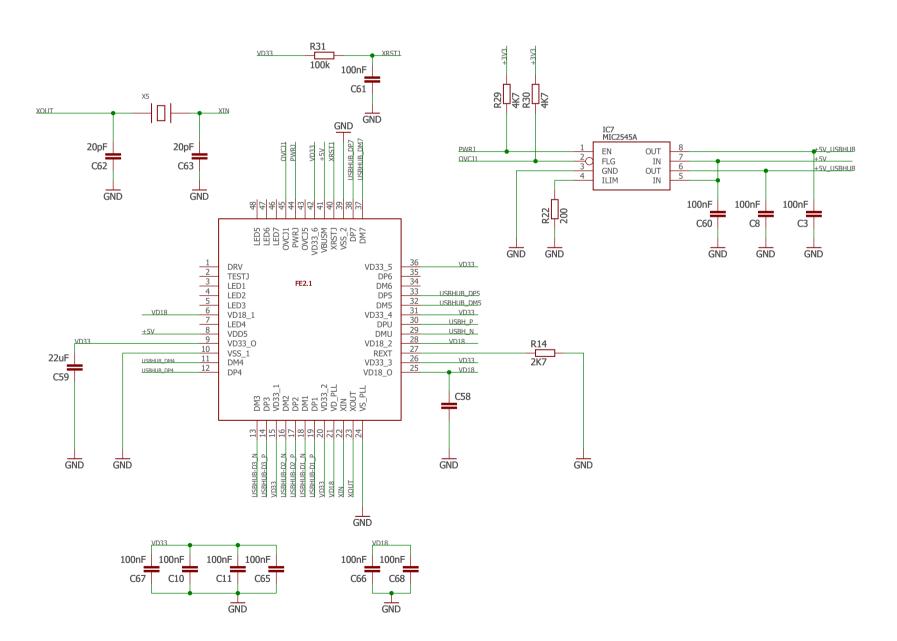


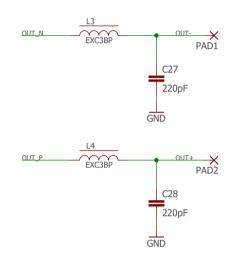


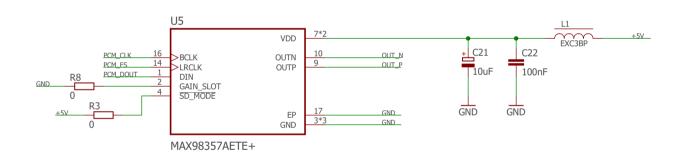


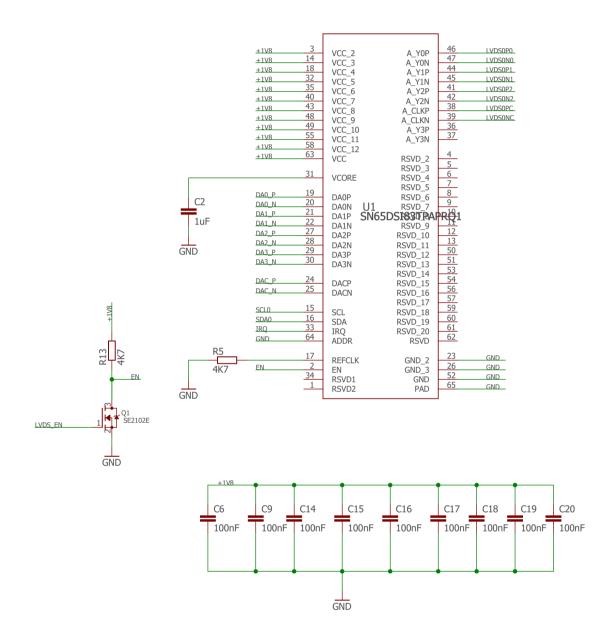


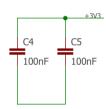




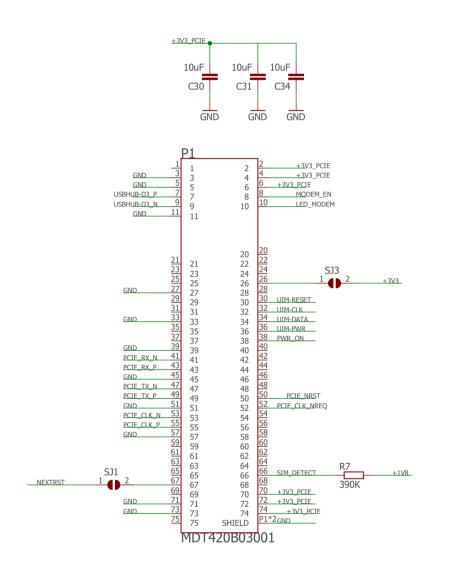


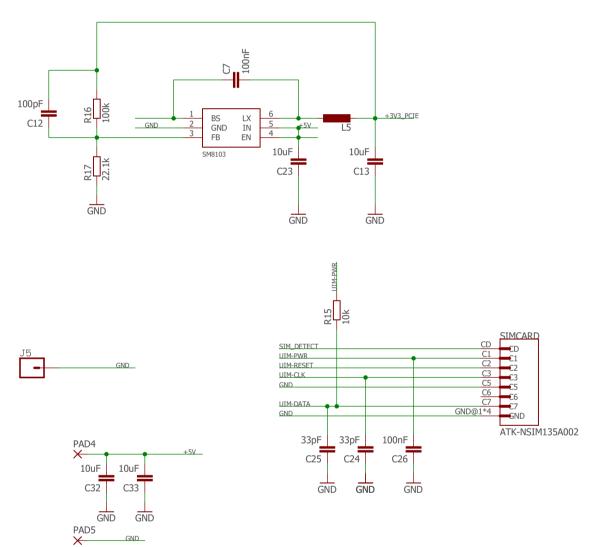






±3V3 1 ±3V3 7 ±3V3 9 ±3V3 10	VCC VBAT VREF IOVDD		DCLK < /CS DIN DOUT	16 0 15 14 12	SPIO_SCLK SPIO_CEO SPIO_MOSI SPIO_MISO
XP-TP 2 YP-TP 3	XP YP	/P	BUSY ENIRQ AUX	<u>13</u> <u>§</u> 11	PENIRQ
<u>XN-TP</u> 4 <u>YN-TP</u> 5	XN	PT2046	GND	6	GND





GND

J1G\$1

________J1SHIELD