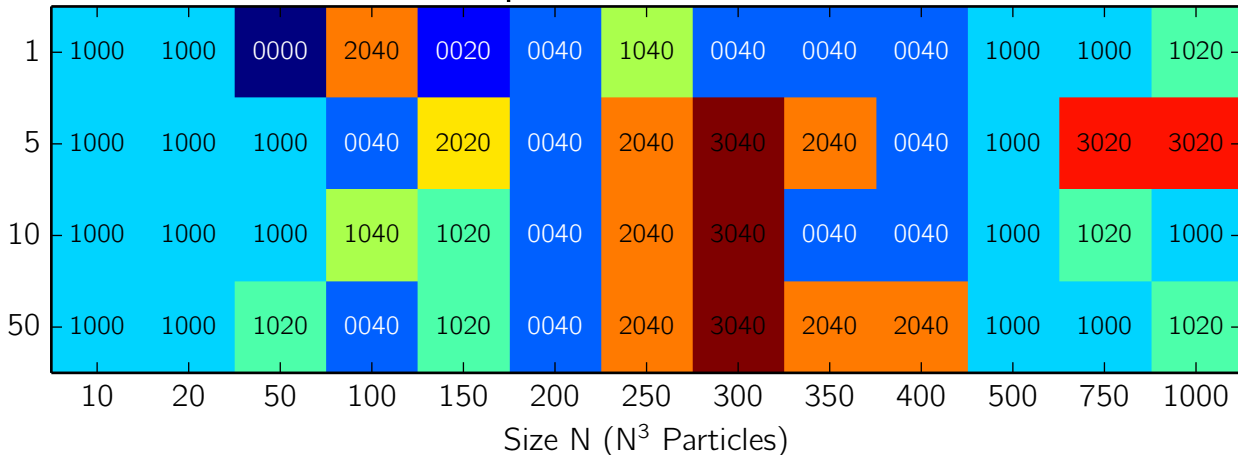


# Optimal Modules

Temperature T



0: greschd v6 sim / 1: greschd v3 sim / 2: greschd v4 sim / 3: greschd v2 sim  
 0: msk v1 pbc  
 0: msk v0 c array dynamic / 2: msk v0 std vec / 4: msk v2 dynamic zip  
 0: mkl mt rng