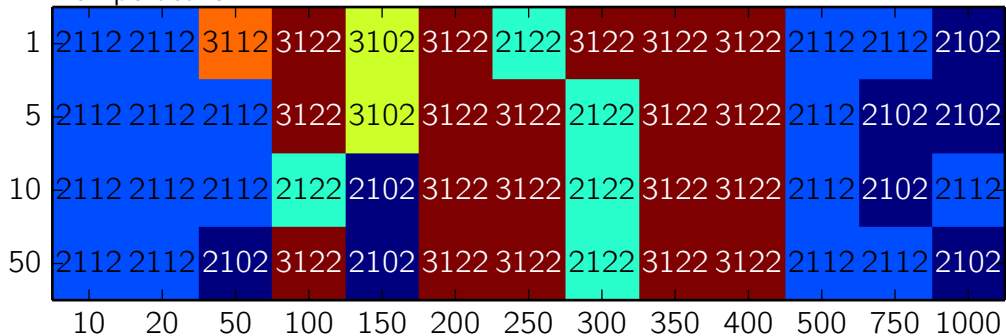


Optimal Modules

Temperature T



2: greschd v2 sim / 3: greschd v6 sim / 3: greschd v4 sim

1: msk v1 pbc

0: msk v0 std vec / 1: msk v0 c array dynamic / 2: msk v2 dynamic zip / 4: msk v1 zorder

1: custom mt / 2: mkl mt rng