Connector. Pin	Net Name	Length(mil)	Delay(ns)
HDMI RX			
CON4. 48	HDMI_RX_CLKN	948. 332	0. 1532
CON4. 46	HDMI RX CLKP	947. 603	
CON4. 42 CON4. 40	HDMI_RX_DON HDMI_RX_DOP	887. 055 887. 221	0. 1434 0. 1435
CON4. 40	HDMI RX D1N	875. 092	0. 1435
CON4. 34	HDMI RX D1P	873. 983	0. 1415
CON4.30	HDMI_RX_D2N	887. 957	0. 1435
CON4. 28	HDMI_RX_D2P	887. 469	0. 1434
HDMI TXO			
CON4. 59	HDMIO_TX_SBDN	1116. 736	0. 1794
CON4. 57	HDMIO_TX_SBDP	1117. 4	0.1795
CON4. 47 CON4. 45	HDMIO_TXON_PORT HDMIO TXOP PORT	1167. 663 1166. 975	
CON4. 41	HDMIO TX1N PORT	1145. 99	0. 1837
CON4. 39	HDMIO TX1P PORT	1146. 968	0. 1838
CON4. 35	HDMIO_TX2N_PORT	1173. 109	0. 1877
CON4.33	HDMIO_TX2P_PORT	1171. 382	0. 1875
CON4. 53	HDMIO_TX3N_PORT	1146. 384	0. 184
CON4. 51	HDMIO_TX3P_PORT	1146. 041	0. 1839
HDMI TX1	JIDMI 1 TV CDDN	1001 407	0 1705
CON4. 29 CON4. 27	HDMI1 TX SBDN HDMI1 TX SBDP	1081. 427 1079. 969	0. 1735 0. 1733
CON4. 27	HDMI1 TXON PORT	1156. 537	0. 1733
CON4. 17	HDMI1 TXOP PORT	1157. 311	0. 1853
CON4. 11	HDMI1_TX1N_PORT	1152. 569	0. 1848
CON4. 9	HDMI1_TX1P_PORT	1154. 517	0. 185
CON4.5	HDMI1_TX2N_PORT	1093. 124	0. 1756
CON4. 3	HDMI1_TX2P_PORT	1090. 303	0. 1752
CON4. 23	HDMI1_TX3N_PORT	1071.503	
CON4. 21	HDMI1_TX3P_PORT	1071. 306	0. 1721
PCIE30 CON3. 60	PCIE3O PORT1 REFCLKN IN	1240. 101	0. 1983
CON3. 58	PCIE30 PORT1 REFCLKP IN	1239. 758	
CON3. 54	PCIE30 PORT1 TX3N	1152. 256	
CON3. 52	PCIE30_PORT1_TX3P	1151. 282	0. 1849
CON3.48	PCIE30_PORT1_TX2N	1139. 95	
CON3. 46	PCIE30_PORT1_TX2P	1140. 322	0. 1829
CON3. 24	PCIE30_PORTO_TXON	1094. 045	
CON3. 22 CON3. 30	PCIE30 PORTO TXOP PCIE30 PORTO TX1N	1095. 81 1190. 96	0. 1761 0. 1906
CON3. 28	PCIESO PORTO TX1P	1190. 90	0. 1907
CON3. 36	PCIE30 PORT1 RX2N	1118. 162	0. 1791
CON3. 34	PCIE30 PORT1 RX2P	1118. 929	0. 1792
CON3. 42	PCIE30_PORT1_RX3N	1091. 798	
CON3. 40	PCIE30_PORT1_RX3P	1092. 683	0.1754
CON3. 18	PCIE30_PORTO_REFCLKN_IN	1085. 73	
CON3. 16	PCIE30_PORTO_REFCLKP_IN	1087. 051	0. 1744
CON3. 6 CON3. 4	PCIE30_PORTO_RXON PCIE30_PORTO_RXOP	1176. 362 1177. 782	0. 1887 0. 1889
CON3. 4 CON3. 12	PCIE30 PORTO RX1N	1127. 329	0. 1811
CON3. 10	PCIE30 PORTO RX1P	1127. 882	0. 1812
PCIE20	TOLEGO TORTO MILI	1121.002	0.1012
CON3.5	PCIE20_1_TXN/SATA30_1_TXN	1113. 859	0. 1786
CON3. 3	PCIE20_1_TXP/SATA30_1_TXP	1115. 396	
CON3. 11	PCIE20 1 RXN/SATA30 1 RXN	980. 561	0. 158
CON3. 9	PCIE20 1 RXP/SATA30 1 RXP	978. 863	
CON3. 23 CON3. 21	PCIE20_2_RXN/SATA30_2_RXN PCIE20_2_RXP/SATA30_2_RXP	1023. 612 1024. 011	0. 1649 0. 1649
CON3. 21	PCIE20 2 RAP/SATA30 2 RAP PCIE20 1 REFCLKN	985. 497	0. 1586
CON3. 17	PCIE20 1 REFCLKP	984. 777	0. 1585
CON3. 35	PCIE20 2 TXN/SATA30_2 TXN	1025. 547	0. 1653
CON3. 33	PCIE20 2 TXP/SATA30 2 TXP	1026. 135	
CON3. 29	PCIE20_2_REFCLKN	1026. 102	
CON3. 27	PCIE20 2 REFCLKP	1025. 573	0. 165
2. 5G ETH	DOEG METUS	242.25	0.0=10=
CON1. 98	P25G_MDIN3	319. 364	
CON1. 96 CON1. 92	P25G_MDIP3 P25G MDIN2	317. 464 314. 585	
CON1. 92	P25G MDIP2	315. 301	0.05423
CON1. 86	P25G MDIN1	324. 723	
CON1. 84	P25G_MDIP1	323. 706	
CON1. 80	P25G_MDINO	313.067	0.05387
CON1.78	P25G_MDIP0	313. 831	0.05399
MIPI_CSIO_RX	MIDI GOTO DU CUITO		
CON1. 54	MIPI_CSIO_RX_CLKON	1604.332	
CON1. 54	MIPI CSIO RX CLKOP	1604. 609	
CON1.38	MIPI_CSIO_RX_CLK1N	1666. 681	0. 2656

CON1. 36	MIPI CSIO RX CLK1P	1666. 737	0. 2657
CON1. 68	MIPI CSIO RX DON	1458. 442	0. 2037
CON1. 66	MIPI CSIO RX DOP	1457. 691	0. 2328
CON1. 62	MIPI CSIO RX D1N	1497. 911	0. 2392
CON1.60	MIPI_CSIO_RX_D1P	1497. 538	0. 2391
CON1. 50	MIPI_CSIO_RX_D2N	1599. 9	0. 255
CON1. 48	MIPI_CSIO_RX_D2P	1600. 116	0. 2551
CON1. 44	MIPI_CSIO_RX_D3N	1613. 825	0. 2573
CON1. 42	MIPI_CSIO_RX_D3P	1614. 259	0.2574
MIPI_CSI1_RX			
CON1. 55	MIPI_CSI1_RX_CLKON	1468. 065	0. 2345
CON1. 53	MIPI_CSI1_RX_CLKOP	1469. 01	0. 2347
CON1. 37	MIPI_CSI1_RX_CLK1N	1581. 984	0. 2523
CON1. 35	MIPI_CSI1_RX_CLK1P	1582. 772	0. 2525
CON1. 67	MIPI_CSI1_RX_DON	1464. 086	0. 234
CON1. 65	MIPI_CSI1_RX_DOP	1465. 008	0. 2342
CON1. 61	MIPI_CSI1_RX_D1N	1492. 847	0. 2383
CON1. 59	MIPI_CSI1_RX_D1P	1494. 653	0. 2385
CON1. 49	MIPI_CSI1_RX_D2N	1473. 352	0. 2354
CON1. 47	MIPI_CSI1_RX_D2P	1473. 984	0. 2355
CON1. 43	MIPI_CSI1_RX_D3N	1489. 589	0. 238
CON1. 41	MIPI_CSI1_RX_D3P	1490. 611	0. 2381
MIPI_DPHYO_RX	MIDI DDHVO DV CLVN	1602 207	0.000
CON2. 18	MIPI DPHYO RX CLKN	1683. 207 1683. 565	0. 2685
CON2. 18 CON2. 26	MIPI_DPHYO_RX_CLKP MIPI_DPHYO_RX_DON	1683. 565 1683. 303	0. 2685 0. 2687
CON2. 26 CON2. 24	MIPI DPHYO RX DON MIPI DPHYO RX DOP	1683. 303 1684. 117	0. 2687
CON2. 24 CON2. 19	MIPI DPHYO RX DOP	1686. 857	0. 2688
CON2. 17	MIPI_DPHYO_RX_D1P MIPI_DPHYO_RX_D2N	1687. 617 1683. 214	0. 2693
CON2. 11			0. 2685
CON2. 11 CON2. 14	MIPI_DPHYO_RX_D2P MIPI_DPHYO_RX_D3N	1684. 191 1681. 901	0. 2686 0. 268
MIPI DPHYO TX	MIPI_DPHYO_RX_D3P	1682. 672	0. 2683
CON2. 37	MIPI DPHYO TX CLKN	1463. 696	0. 2342
CON2. 35	MIPI DPHYO TX CLKP	1465. 76	0. 2345
CON2. 49	MIPI DPHYO TX DON	1327. 51	0. 2128
CON2. 49	MIPI DPHYO TX DOP	1328. 471	0. 2128
CON2. 47	MIPI DPHYO TX D1N	1383. 57	0. 2131
CON2. 43	MIPI DPHYO TX D1P	1382, 824	0. 2216
CON2. 41	MIPI DPHYO TX D2N	1474. 438	0. 2359
CON2. 31	MIPI DPHYO TX D2P	1474. 782	0. 2339
CON2. 25	MIPI DPHYO TX D3N	1522. 427	0. 2433
CON2. 23	MIPI DPHYO TX D3P	1522. 494	0. 2434
MIPI DPHY1 RX	MITI_DITITO_IN_DOI	1022. 101	0.2101
CON2. 44	MIPI DPHY1 RX CLKN	1285. 083	0. 2065
CON2. 42	MIPI DPHY1 RX CLKP	1284. 321	0. 2064
CON2. 56	MIPI DPHY1 RX DON	1282. 433	0. 2061
CON2. 54	MIPI DPHY1 RX DOP	1282. 845	0. 2062
CON2. 50	MIPI DPHY1 RX D1N	1282. 793	0. 2061
CON2. 48	MIPI DPHY1 RX D1P	1283. 645	0. 2064
CON2. 38	MIPI DPHY1 RX D2N	1283. 996	0. 2064
CON2. 36	MIPI DPHY1 RX D2P	1284. 252	0. 2064
CON2. 32	MIPI DPHY1 RX D3N	1285. 219	0. 2065
CON2. 32	MIPI DPHY1 RX D3P	1285. 433	0. 2066
MIPI DPHY1 TX	STATE OF THE POST	1200, 100	0.2000
CON2. 74	MIPI DPHY1 TX CLKN	1133. 976	0. 1823
CON2. 72	MIPI DPHY1 TX CLKP	1132. 621	0. 1821
CON2. 86	MIPI DPHY1 TX DON	1069. 635	0. 1722
CON2. 84	MIPI DPHY1 TX DOP	1070. 458	0. 1725
CON2. 80	MIPI DPHY1 TX D1N	1097. 809	0. 1766
CON2. 78	MIPI DPHY1 TX D1P	1098. 483	0. 1768
CON2. 68	MIPI DPHY1 TX D2N	1153. 146	0. 1853
CON2. 66	MIPI DPHY1 TX D2P	1152. 224	0. 1852
CON2. 62	MIPI DPHY1 TX D3N	1179. 658	0. 1895
CON2. 60	MIPI DPHY1 TX D3P	1179. 941	0. 1895
TYPE-C0		11.0.011	0.1000
CON2. 92	TYPECO OTG DM	974. 871	0. 1575
CON2. 90	TYPECO OTG DP	975. 577	0. 1576
CON2. 67	TYPECO SSTX1N	1153. 417	0. 1854
CON2. 65	TYPECO SSTX1P	1153. 348	0. 1855
CON2. 73	TYPECO SSRX1N	1153. 481	0. 1854
CON2. 71	TYPECO SSRX1P	1154. 204	0. 1856
CON2. 55	TYPECO SSTX2N	1202. 159	0. 1932
CON2. 53	TYPECO SSTX2P	1201. 872	0. 1931
CON2. 61	TYPECO SSRX2N	1209. 696	0. 1943
CON2. 59	TYPECO SSRX2P	1210. 523	0. 1945
TYPE-C1		1210.020	J. 1010
,		000 505	0 1400
CON2.98	TYPEC1 OTG DM	926. 585	0. 1499

CON2. 96	TYPEC1 OTG DP	925. 291	0. 1497
CON2.91	TYPEC1_SSTX1N	1076. 199	0. 1729
CON2.89	TYPEC1_SSTX1P	1076.045	0. 1729
CON2. 97	TYPEC1_SSRX1N	1138. 369	0. 1827
CON2. 95	TYPEC1_SSRX1P	1139. 41	0. 1829
CON2. 79 CON2. 77	TYPEC1_SSTX2N TYPEC1_SSTX2P	1121. 841 1121. 637	0. 1803 0. 1803
CON2. 77	TYPEC1 SSRX2N	1103. 944	0. 1774
CON2. 83	TYPEC1 SSRX2P	1104.032	0. 1775
USB20			
CON4. 24	USB20_HOSTO_DM	930. 839	0. 1503
CON4. 22	USB20_HOST0_DP	931. 356	0. 1503
CON4. 18	USB20_HOST1_DM	926. 785	0. 1498
CON4. 16	USB20_HOST1_DP	925. 055	0. 1496
SDMMC CON4. 60	SDMMCO CLK	998. 294	0 1606
CON4. 60 CON4. 56	SDMMCO_CLK SDMMCO_CMD	1001.116	0. 1606 0. 1612
CON4. 64	SDMMCO DO	1046. 144	0. 1612
CON4. 66	SDMMCO D1	1025. 076	0. 165
CON4. 52	SDMMCO D2	1003. 329	0. 1614
CON4. 54	SDMMCO_D3	1003.064	0. 1613
GPI0			
CON4. 70	GPIOO_A4/SDMMC_DET_L	4631. 527	0. 7793
CON3. 47	GPIOO_B2	1242. 003	0. 2075
CON3. 70	GPIOO_C4/UARTO_RX_MO	1558. 969	0. 2657
CON3. 72 CON3. 74	GPIOO_C5/UARTO_TX_MO/PWM4_MO GPIOO C6/PWM5 M1	1589. 323 1582. 038	0. 2699 0. 2692
CON3. 74 CON3. 64	GPIOO D3/CC INT L	1797. 023	0. 2692
CON3. 66	GPIOO D4/PWM3 IR MO	1855. 213	0. 3042
CON3. 68	GPIOO D5	1857. 249	0. 3159
CON3. 73	GPIO1_AO/UART6_RX_M1	1127.6	0. 1807
CON3. 75	GPIO1_A1/UART6_TX_M1	1082.709	0. 1739
CON3. 77	GPIO1_A2/I2C4_SDA_M3_TP	1094. 67	0. 1757
CON3. 79	GPIO1_A3/I2C4_SCL_M3_TP	1076. 061	0. 1729
CON3. 81	GPIO1 A4	1087. 317	0. 1747
CON3. 83 CON3. 76	GPIO1_A7 GPIO1_B0	1103. 362 1252. 485	0. 1774 0. 2008
CON3. 78	GPIO1 B1/SPIO MISO M2	1408. 37	0. 2389
CON3. 80	GPI01_B2/SPI0_MOSI_M2/UART4_RX_M2	1416. 808	0. 2397
CON3. 82	GPI01 B3/SPI0 CLK M2/UART4 TX M2	1600.649	0. 2698
CON3. 90	GPI01_B4/SPI0_CS0_M2/UART7_RX_M2	1430.056	0. 243
CON3.92	GPIO1 B5/SPIO CS1 MO/UART7 TX M2	1557. 594	0. 2638
CON1.71	GPIO1_B6/MIPI_CAM1_CLKOUT	1534. 747	0. 2576
CON1. 75	GPIO1_B7/MIPI_CAM2_CLKOUT	1594. 593	0. 2692
CON3. 45	GPIO1_CO/I2C3_SDA_MO	1087. 654	0. 1749
CON3. 43 CON2. 8	GPIO1_C1/I2C3_SCL_MO	993. 176	0. 1602
CON2. 8 CON3. 39	GPIO1_C4/HP_DET_L GPIO1 D2/TYPEC5V PWREN H	3466. 75 1057. 967	0. 5877 0. 1701
CON3. 41	GPIO1 D3/PWM1 M1	1031. 255	0. 1659
CON3. 99	GPIO1 D5/HDMIIRX DET L	1550. 919	0. 2635
CON3. 86	GPI01 D6/I2C8 SCL M2	1422.693	0. 2417
CON3.88	GPI01_D7/I2C8_SDA_M2	1569.412	0. 2657
CON1.79	GPI02_A6	1536. 86	0. 2456
CON1.81	GPI02 A7	1385. 794	0. 2221
CON1. 83	GPIO2_BO	1384. 083	0. 2215
CON1. 85	GPI02_B1	1338. 382	0. 2143
CON1. 87 CON1. 89	GPI02_B2 GPI02_B3	1351. 057 1820. 468	0. 2161 0. 3031
CON1. 89	GP102_B3 GP102_B4	1676. 703	0. 2807
CON1. 95	GPI02 B5	1705. 958	0. 2858
CON1. 97	GPI02 B6	1640. 687	0. 2735
CON1. 99	GPI02_B7	1555. 648	0. 2599
CON1. 21	GPI02_C0	1887. 672	0. 3008
CON1.23	GPIO2_C1/MIPI_CAM2_RESET_L	2387. 585	0.4007
CON1. 25	GPIO2_C2/MIPI_CAM2_PDN_L	2318. 503	0. 3928
CON1. 27	GPIO2_C3	2237. 297	0.38
CON4. 27	GPIO2_C4	2382.718	0.4043
CON4. 87 CON4. 89	GPI03_A0/SPI4_MISO_M1/PWM10_M0 GPI03_A1/SPI4_MOSI_M1	3014. 861 2991. 849	0. 5128 0. 5093
CON4. 89 CON4. 91	GPIO3 A2/SPI4 CLK M1/UART8 TX M1	2406. 25	0. 4082
CON4. 91	GPIO3 A3/SPI4 CSO M1/UART8 RX M1	2350. 071	0. 4082
CON4. 95	GPIO3 A4/SPI4 CS1 M1	2315. 19	0. 3926
CON4. 97	GPI03_A5	2964. 313	0. 5021
CON4.99	GPIO3_A6/LCD_RST	2882. 229	0. 487
CON4. 100	GPI03_A7	2756. 8	0. 4682
CON4. 82	GPIO3_BO/PWM9_MO	2510. 94	0. 4253
CON4. 84	GPIO3_B1/PWM2_M1/LCD_BL	2662. 416	0. 4494
CON4. 86 CON4. 88	GPIO3 B2	2631. 871	0. 4466
	GPI03 B3	2729. 208	0.4639

CON4.90	GPI03 B4	2811. 975	0. 4775
CON4. 92	GPIO3 B5/UART3 TX M1/PWM12 MO	2721.624	0. 4628
CON4. 94	GPIO3 B6/UART3 RX M1/PWM13 MO	2850. 49	0. 4844
CON4. 98	GPI03 B7	3100.026	0. 5296
CON3. 87	GPIO3 CO/TP INT L	2155. 468	0. 3703
CON3. 89	GPIO3 C1/TP RST L	2249. 669	0. 387
CON3. 91	GPIO3 C2/PWM14 MO	2190. 871	0. 3748
CON3. 93	GPIO3 C3/PWM15 IR MO	2188. 102	0. 3745
CON4. 81	GPI03 C7/I2C5 SCL MO TP	2002. 642	0. 3385
CON4. 79	GPIO3 DO/I2C5 SDA MO TP	1988. 683	0. 3342
CON4. 85	GPIO3 D5/PWM11 M3/LCD 2 BL	2835. 49	0. 4846
CON3. 51	GPIO4 AO/TP 2 INT L	2149. 97	0. 3647
CON3. 53	GPIO4 A1/TP 2 RST L	2288. 592	0. 3878
CON3. 55	GPIO4 A2/PCIE WIFI PERSTN	2383. 358	0. 4046
CON3. 57	GPIO4 A3/LCD 2 RST	2380.011	0.4035
CON3. 61	GPIO4_BO/USB_HOST_PWREN_H	2320. 867	0.3948
CON3. 63	GPIO4 B3	2739. 129	0.4652
CON3. 65	GPIO4 B4/PCIE3OX4 CLKREQN M1 L	2628. 702	0. 4472
CON3. 67	GPIO4 B5	2090. 56	0. 3553
CON3. 69	GPIO4 B6/PCIE30X4 PERSTN M1 L	2172.071	0. 3681
CON1. 26	GPIO4 C2	2348. 524	0. 3978
CON1. 28	GPIO4 C3	1842. 867	0. 2935
CON1.30	GPIO4 C4/MIPI CAM1 RESET L	1803.666	0. 2874
CON1. 32	GPIO4 C5/MIPI CAM1 PDN L	1844. 507	0. 2938
CON1. 31	GPIO4 C6	1760. 751	0. 281
CON4. 78	HDMIIRX HPDOUT H	2693. 588	0.4608
CON4. 67	HDMITXO CEC MO	1740. 589	0. 296
CON3. 95	HDMITXO HPDIN MO	1261. 545	0. 2023
CON4. 63	HDMITXO SCL MO	2004. 284	0. 3353
CON4. 65	HDMITXO SDA MO	1705. 734	0. 2913
CON4. 75	HDMITX1 CEC M2	2199.449	0. 3672
CON3. 97	HDMITX1_HPDIN_MO	1196. 854	0. 1922
CON4. 71	HDMITX1_SCL_M1	2064. 159	0. 3465
CON4. 73	HDMITX1 SDA M1	2044. 152	0.3438
CON4. 76	HDMI RX CEC	1899. 2	0.322
CON4. 72	HDMI RX SCL M1	1975. 347	0. 3329
CON4. 74	HDMI RX SDA M1	1906. 153	0. 3228
CON4. 69	HDMIO TX ON H	1859. 284	0. 3153
CON4. 77	HDMI1_TX_ON_H	1854. 855	0.3126
CON3. 96	I2C6_SCL_MO	2248.073	0. 3841
CON3. 98	I2C6 SDA MO	2120. 918	0.3634
CON2. 7	I2C7 SCL MO	3438. 806	0. 5842
CON2.5	I2C7_SDA_MO	3262. 61	0.555
CON4. 4	TYPECO_SBU1	1069.719	0. 1761
CON4.6	TYPECO_SBU2	994. 453	0. 1643
CON4. 10	TYPEC1_SBU1	970. 598	0. 1596
CON4. 12	TYPEC1 SBU2	934. 679	0. 1541
CON1. 22	UART2 RX MO DEBUG	2604.625	0.444
CON1. 20	UART2 TX MO DEBUG	2767. 633	0.4714