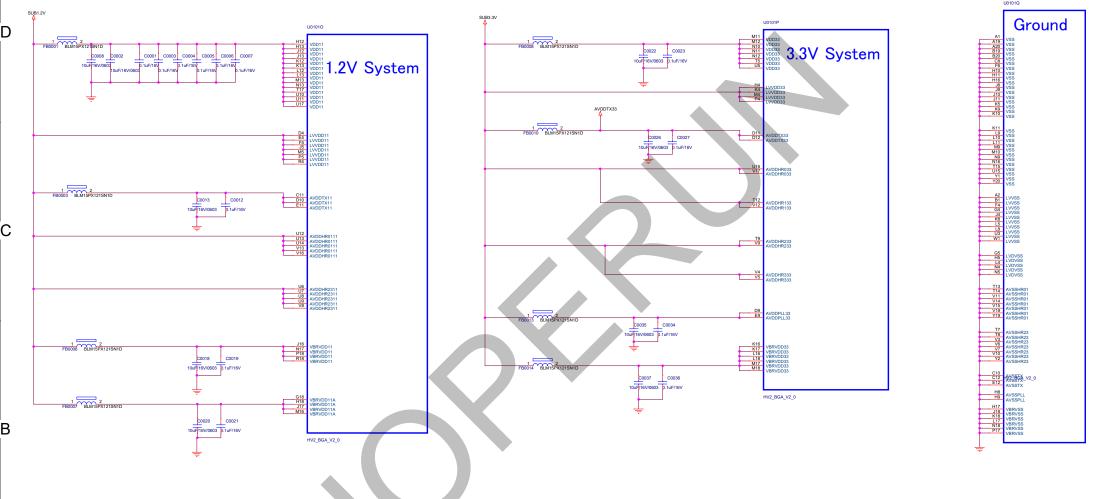
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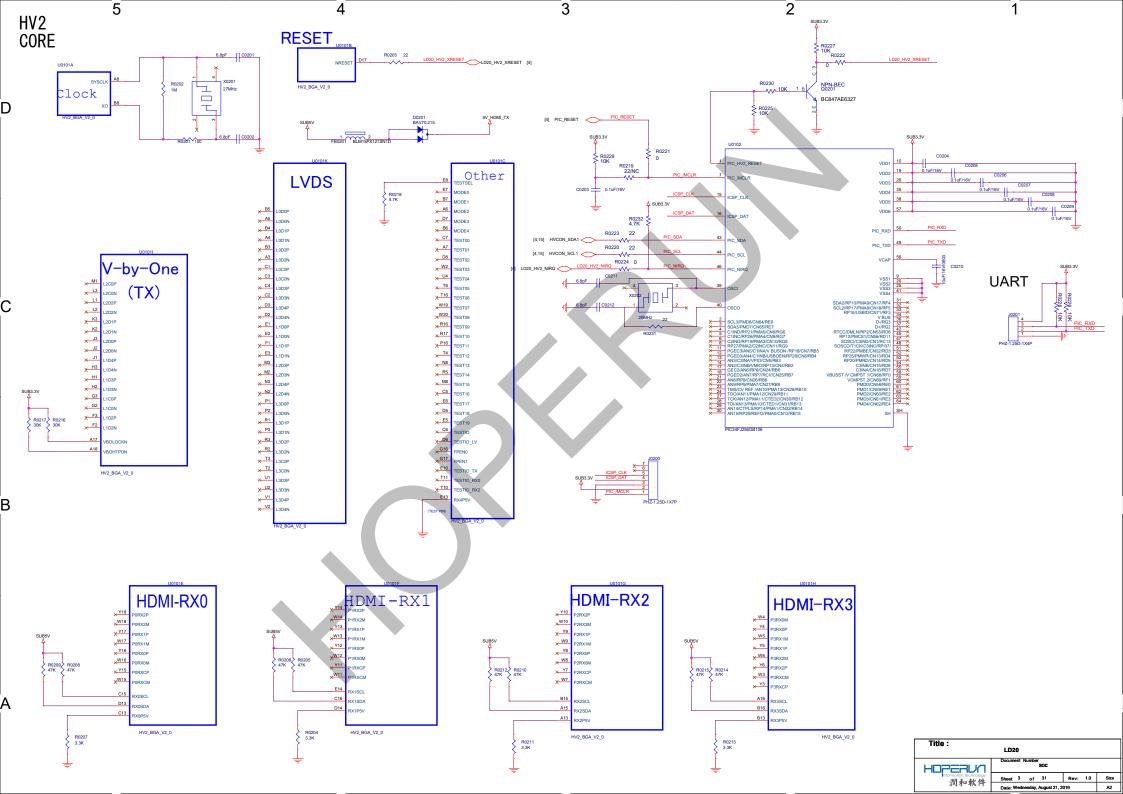
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1	001_TOP				28	028_DCDC_STM				
2	002_HV2_POWER				29	029_DCDC_BE1				
3	003_HV2_CORE				30	030_DCDC_BE2				
4	004_HV2_AUDIO				33	. 031Block Diagram				
5	005_HV2_VIDEO				32		0	Х	Х	0
6	006_LD20_BGA1				33		Х	Х	Х	Х
7	007_LD20_BGA2				34		Х	X	0	0
8	008_LD20_PWR				3!	i l	0	Х	Х	Х
9	009_LD20_DDR#0				30		0	X	0	0
10	010_LD20_DDR#1				3'		Х	Х	Х	Х
11	011_LD20_DDR#2				38		Х	0	0	0
12	012_LD20_DEST				39		0	X	Х	Х
13	013_INITIAL				40					
14	014_PORT				4:		0	Х	Х	X
15	015_I2CI_DBG_SRL				42		Х	Х	Х	Х
16	016_User LEDs				43	3				
17	017_PWR_CTRL_RST				44					
18	018_USB TO UART			K	45					
19	019_eMMC				4 (					
20	020_PERI_USB_WIFI				4					
21	021_00_DCDC_CTRL	0	X	Х	x 48					
22	022_PERI_ETHER(RTL8211E-VB)				49					
23	023_PERI_ETHER_COMMON				50					
24	024_PERI_PCI				53					
25	025_AVIN_HDMI0-1				52					
26	026_SPDIF									
27	027_EXCON_TV_Tuner									
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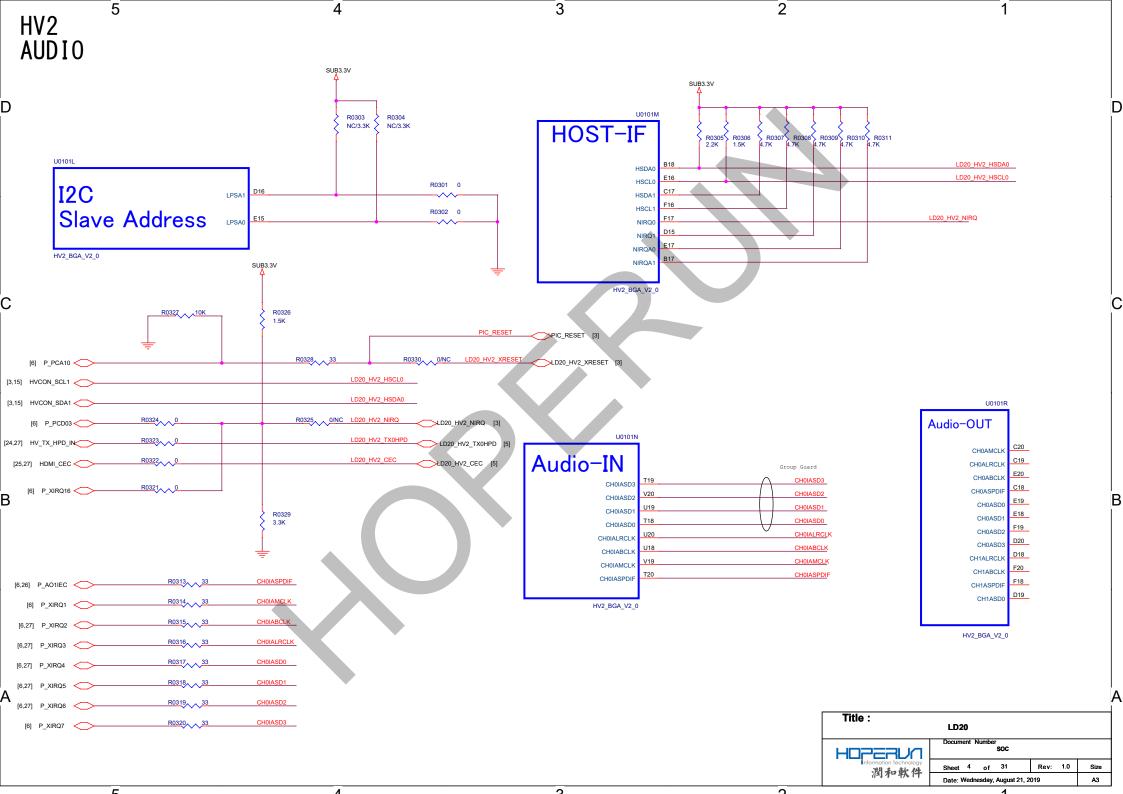
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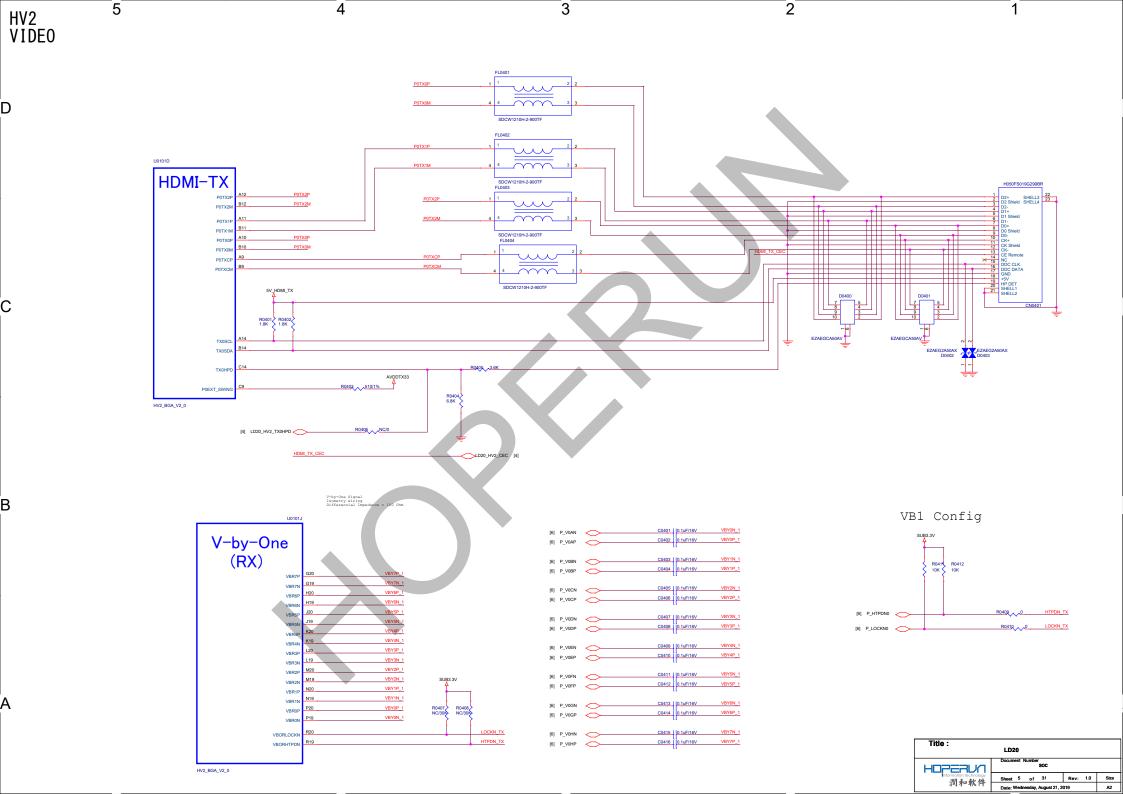


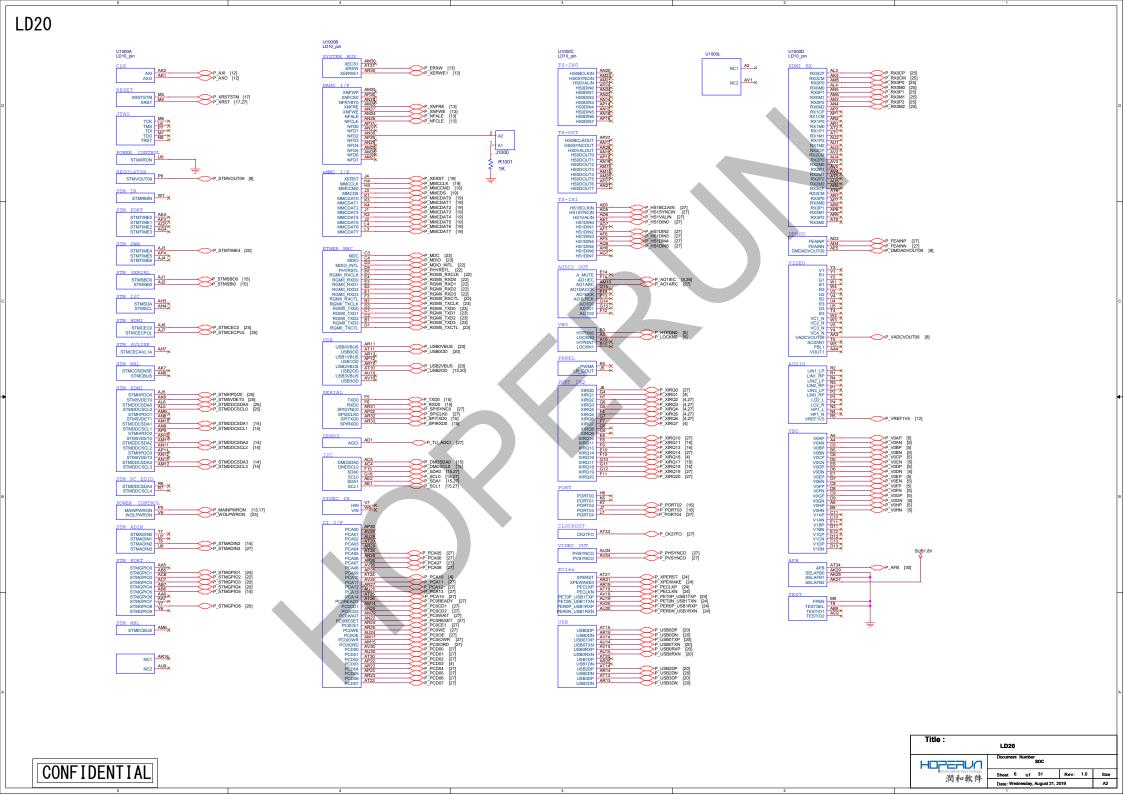


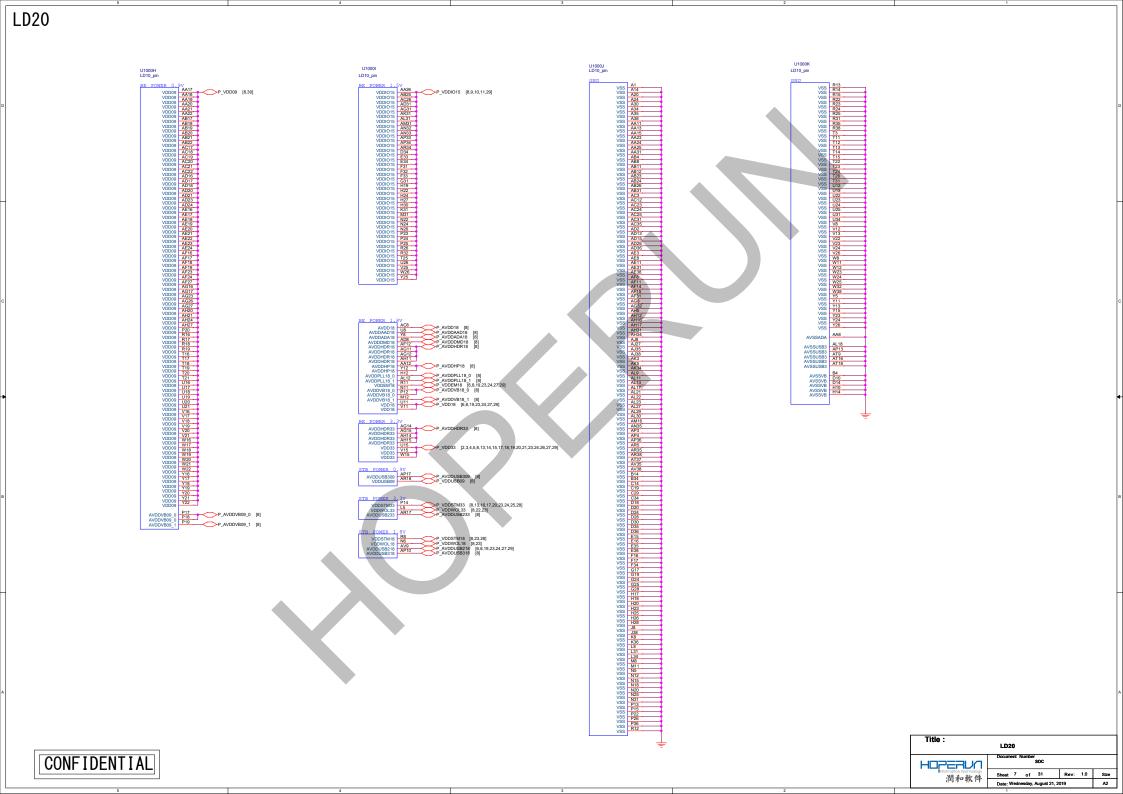


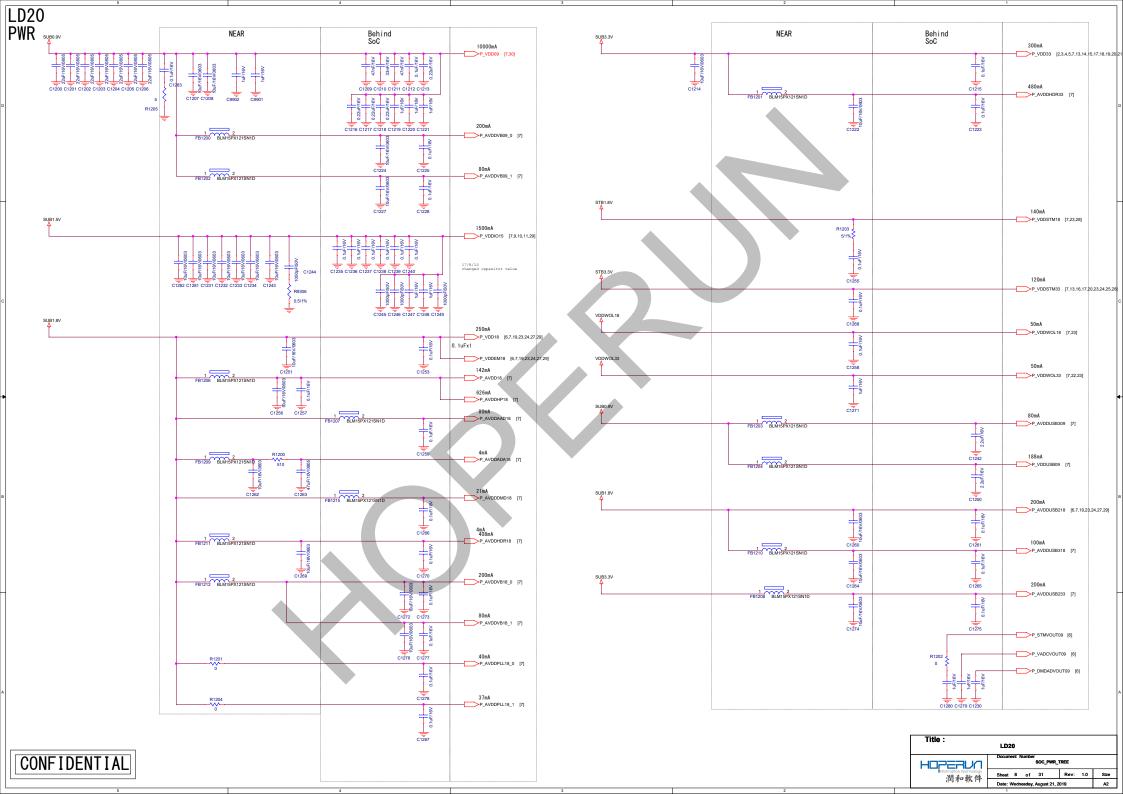


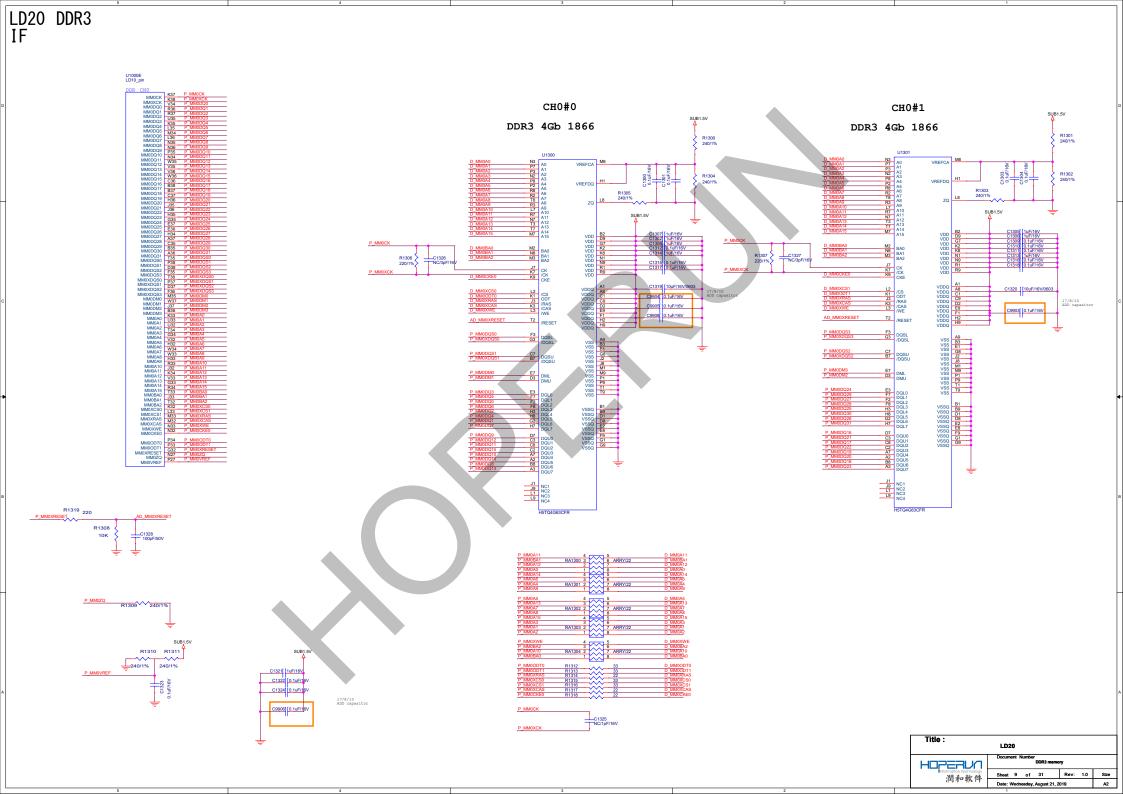


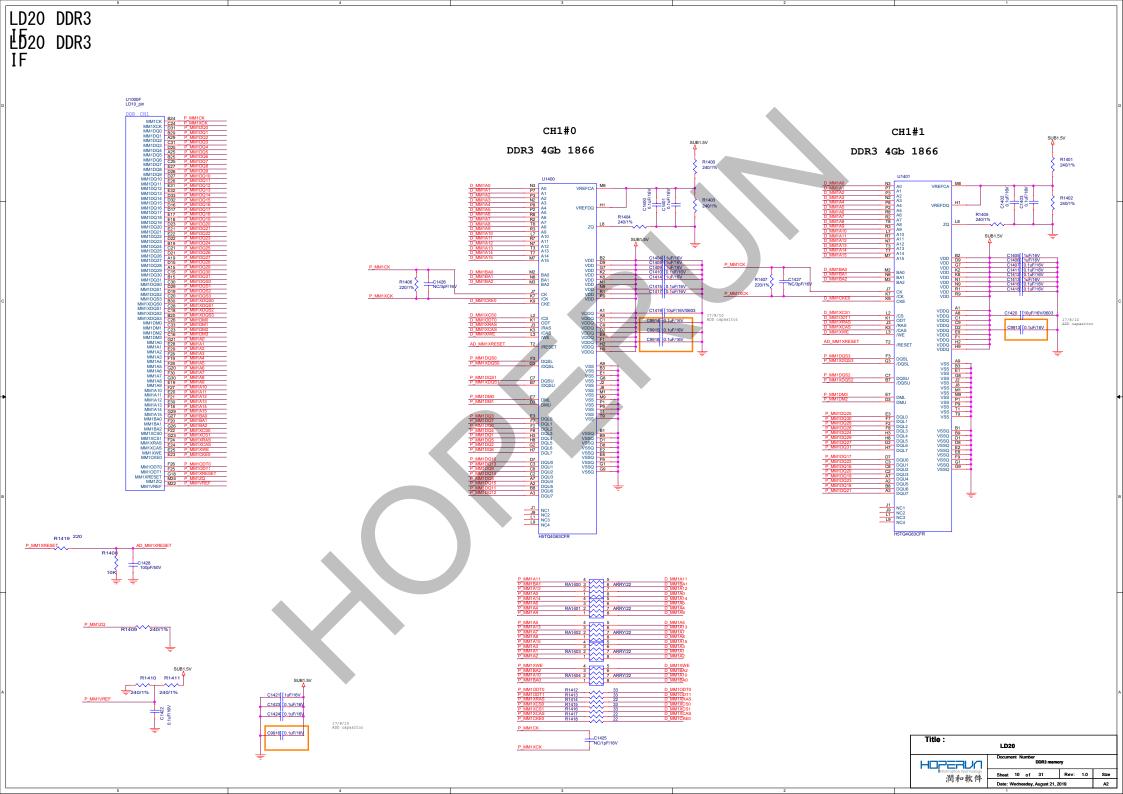


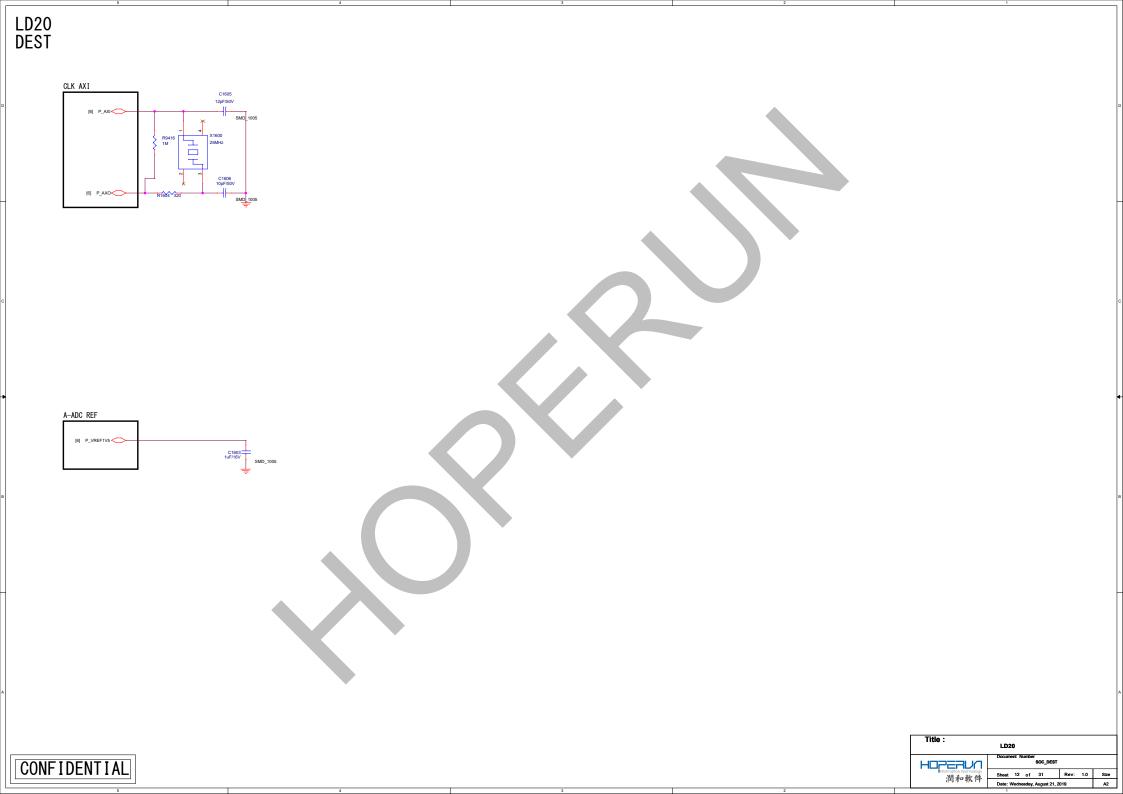


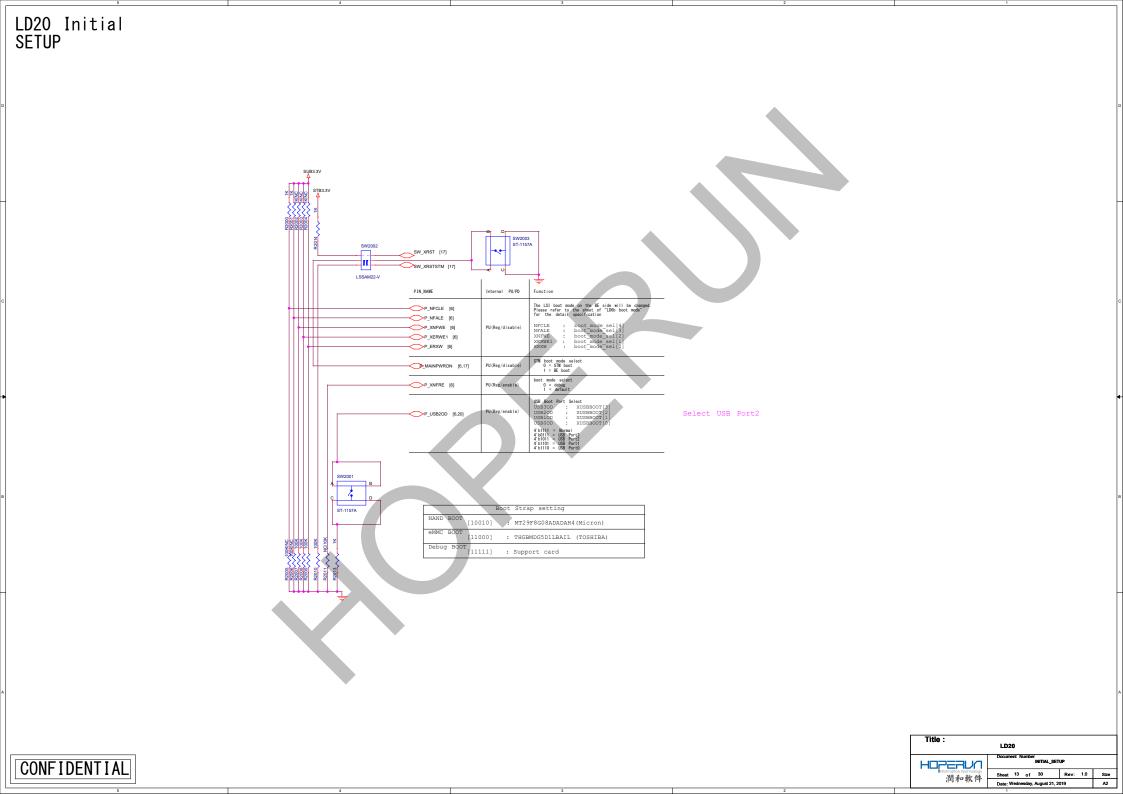


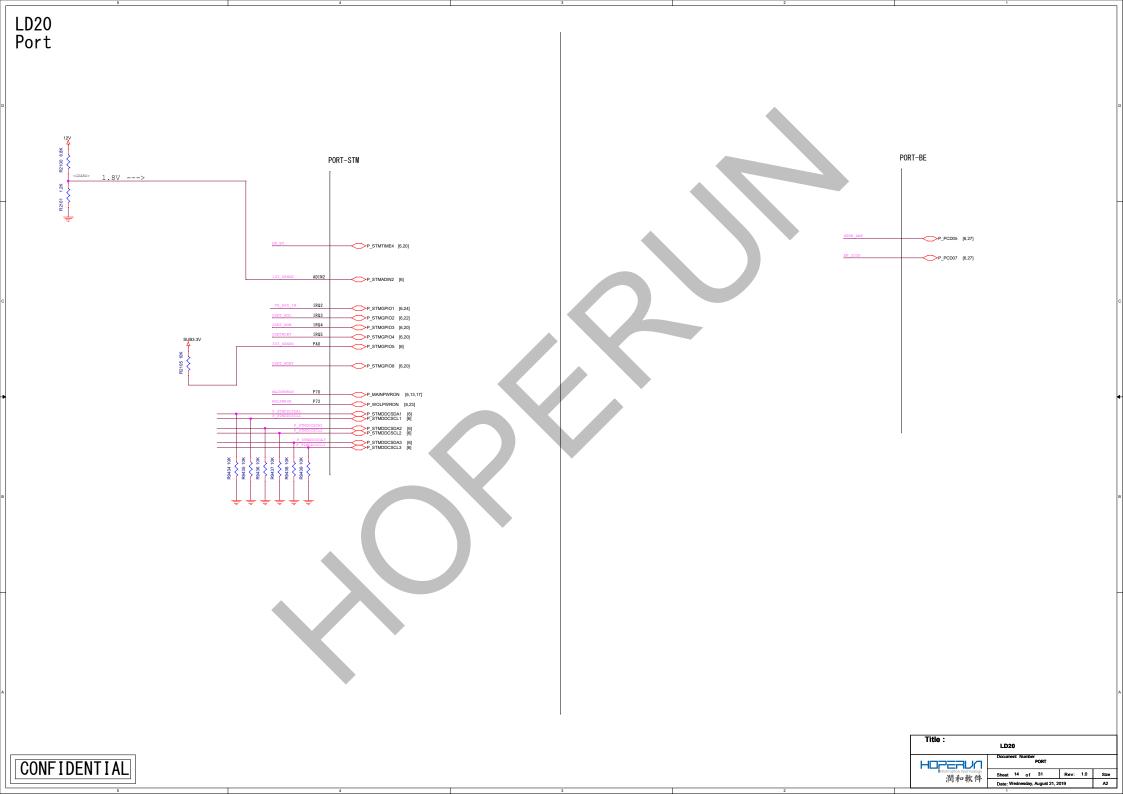


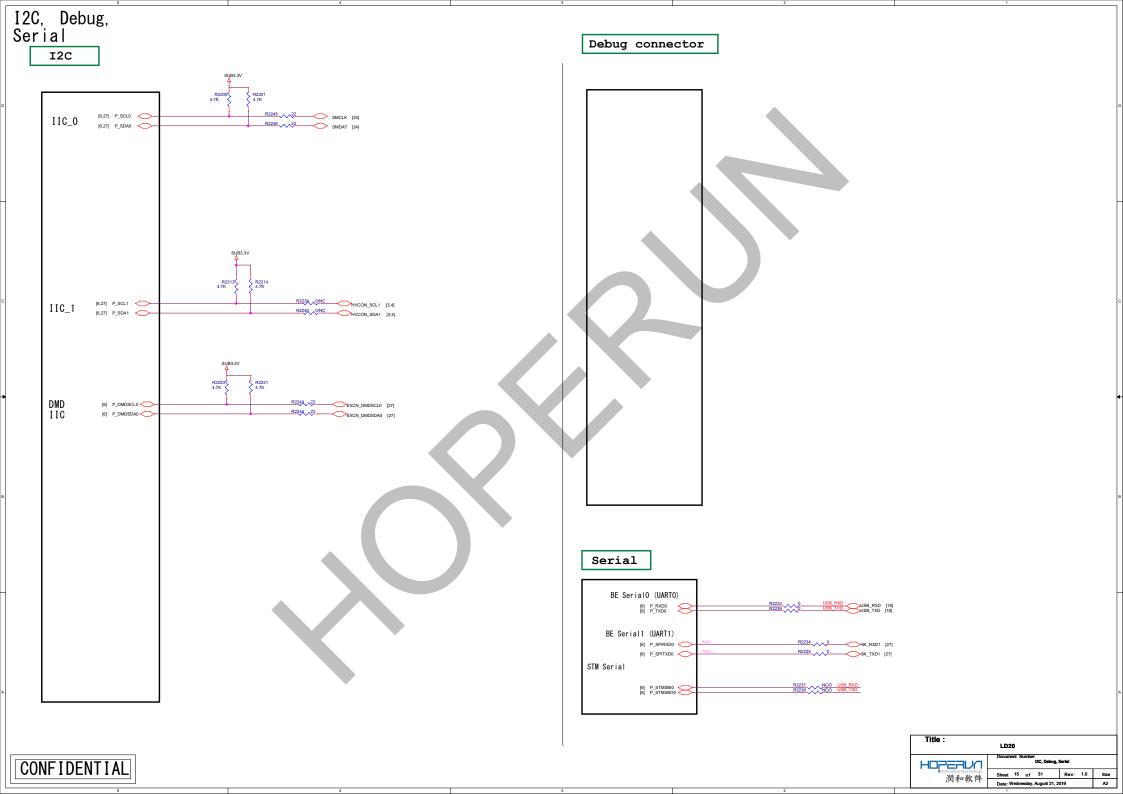


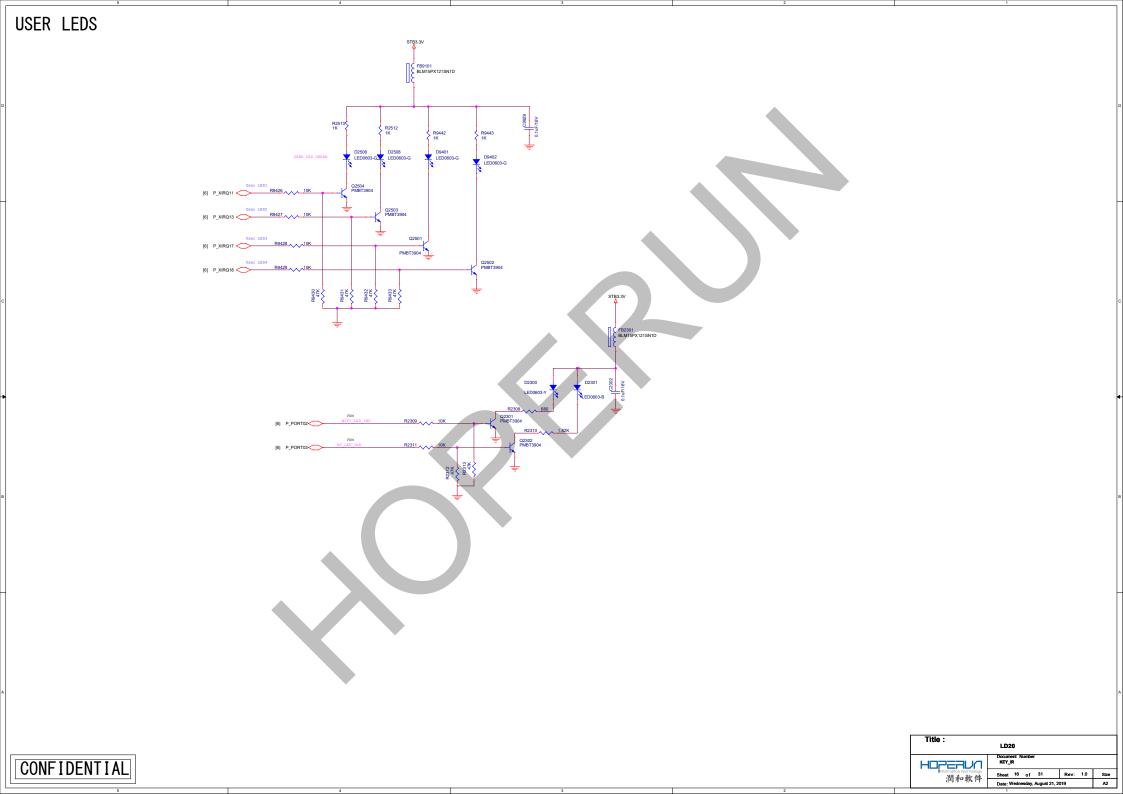


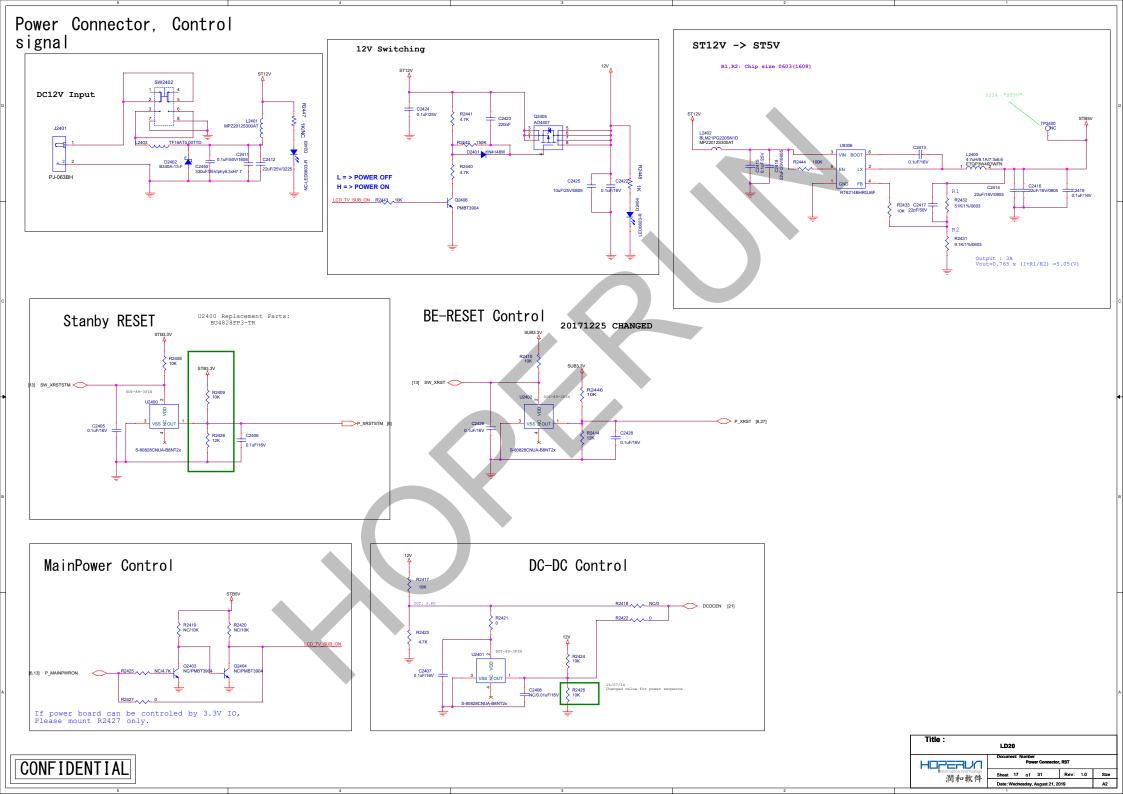


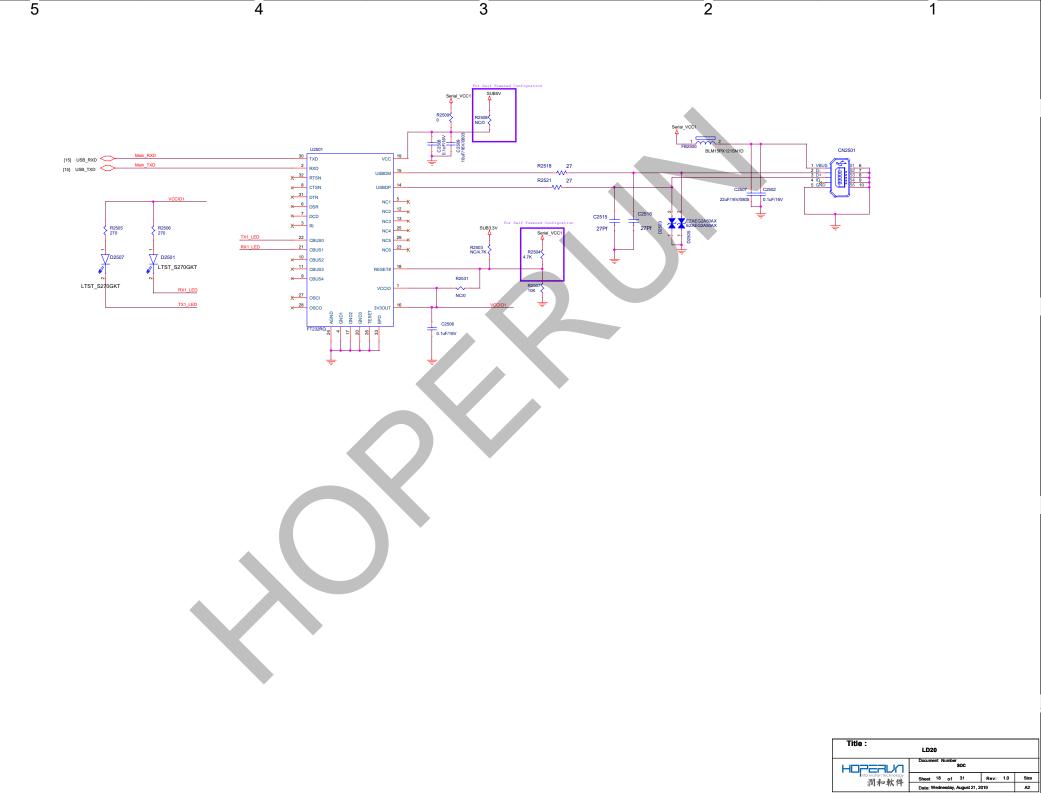




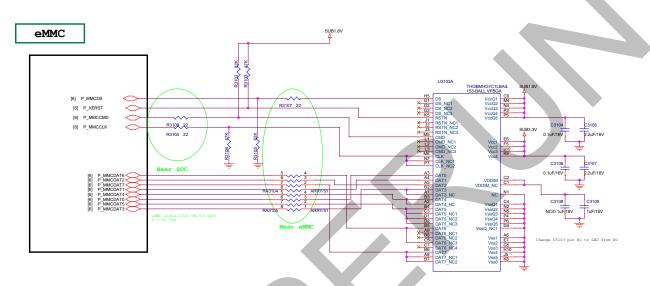








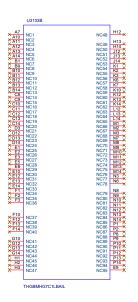
eMMC



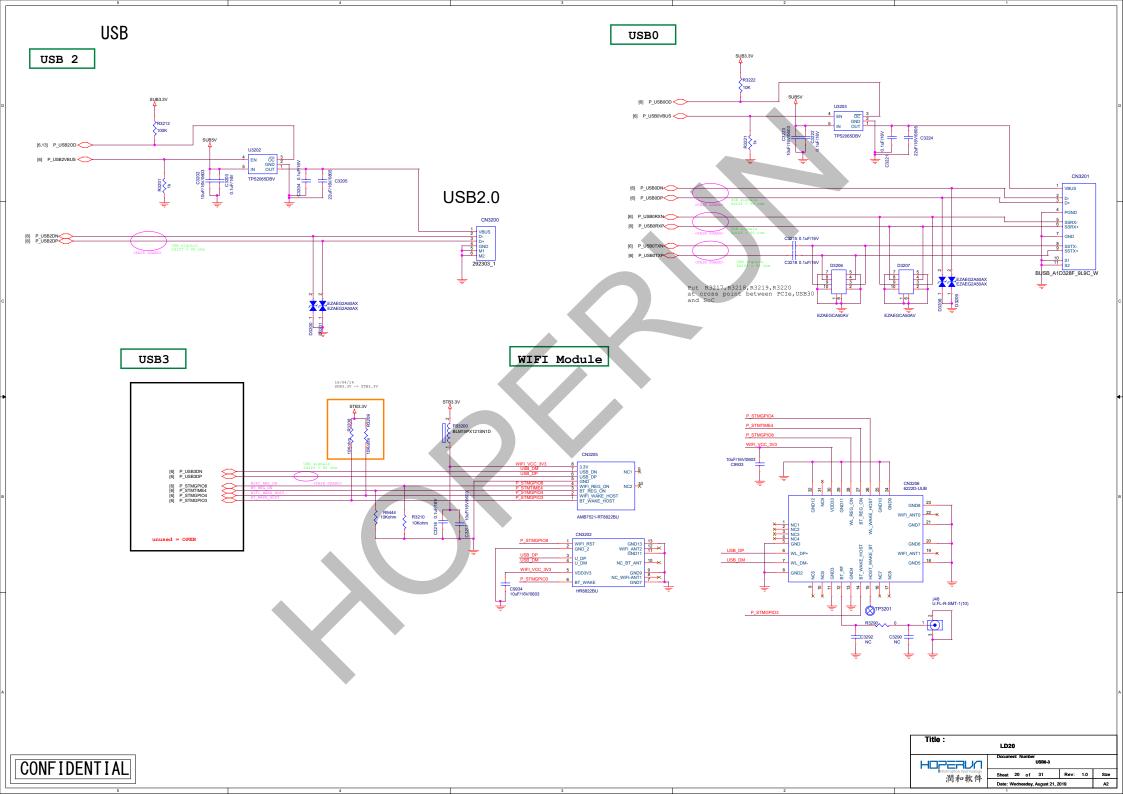
The reference layout is traced according to MICRON eMMC layout guide.

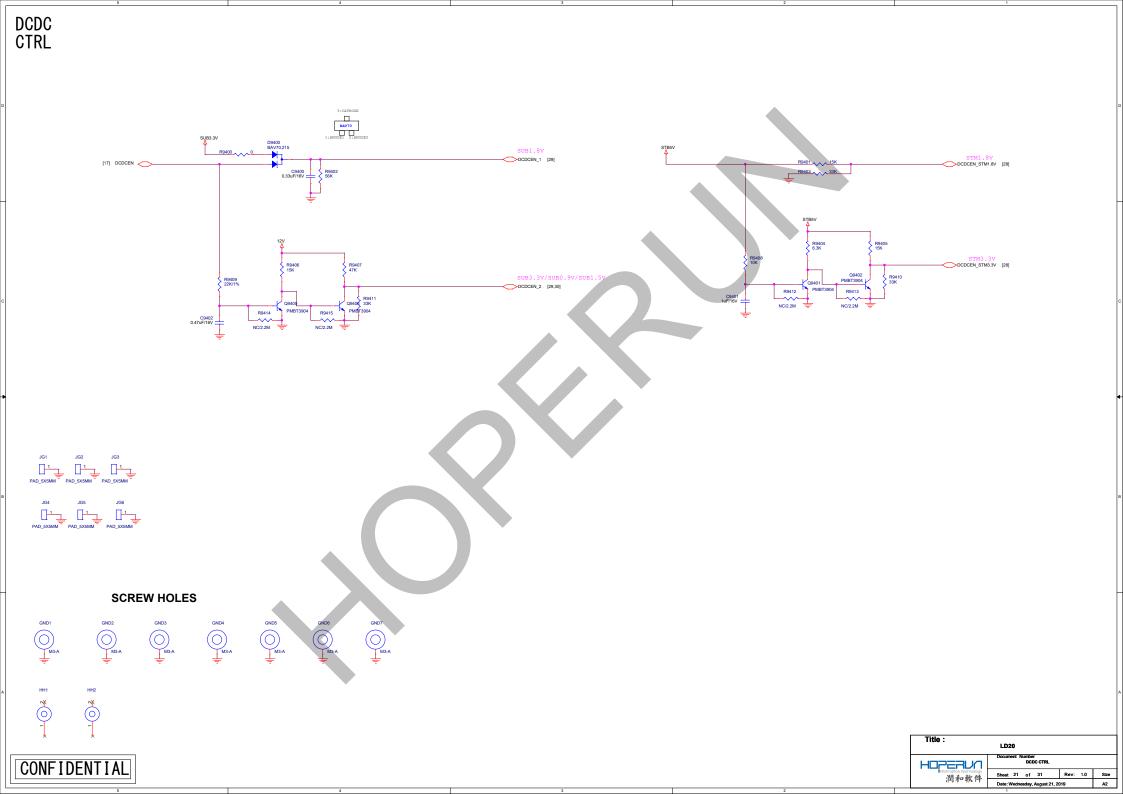
So when we mount TOSHIBA or others Flash, some of the pattern are traced on RFU ball.

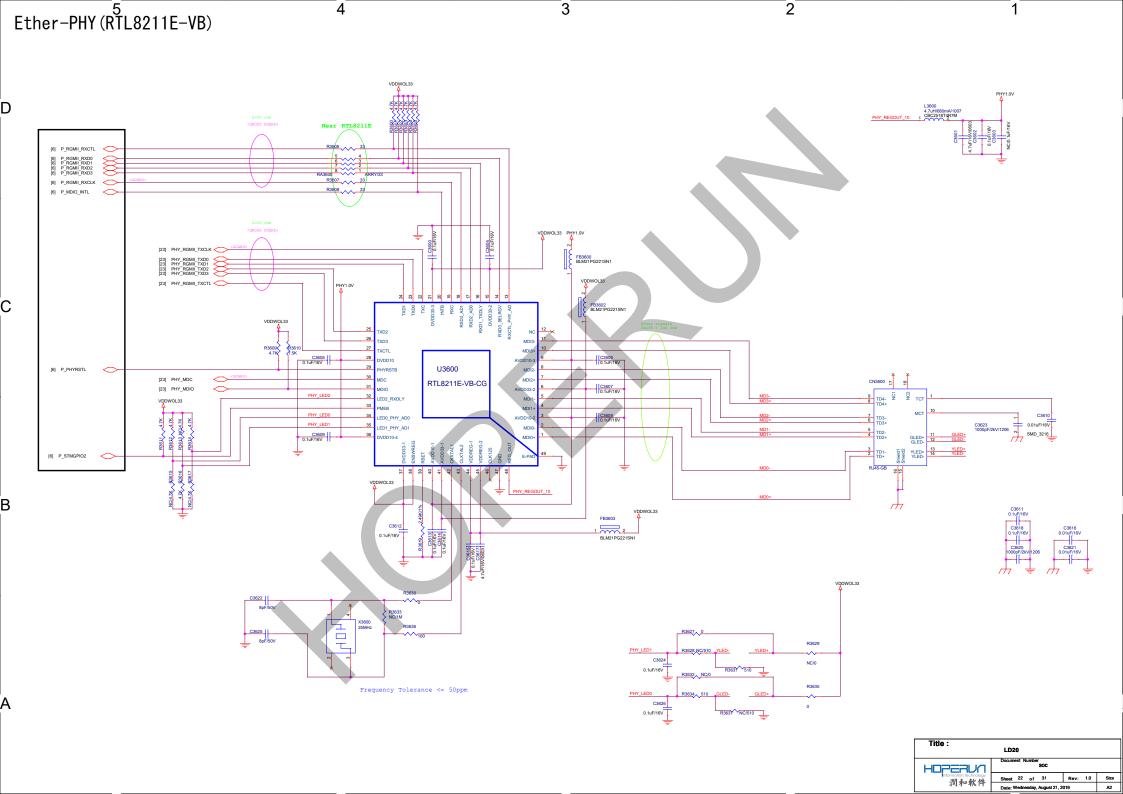
Please change layout pattern if use without MICRON eMMC.

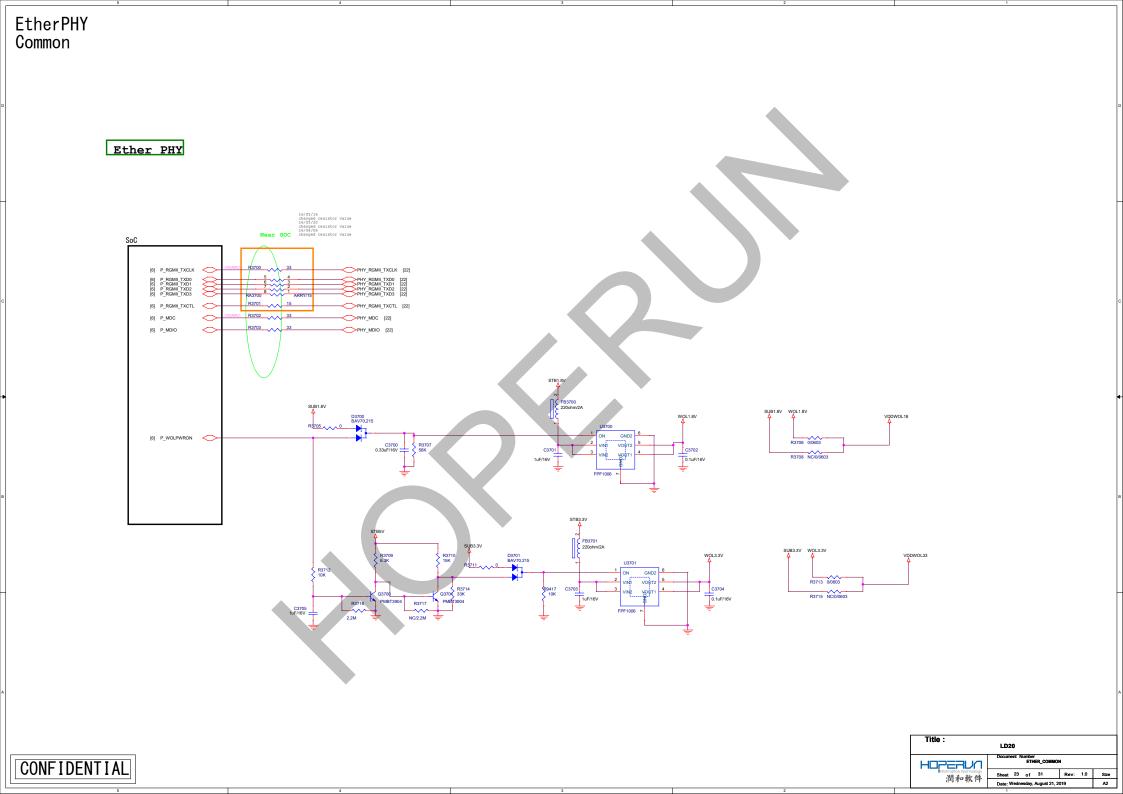


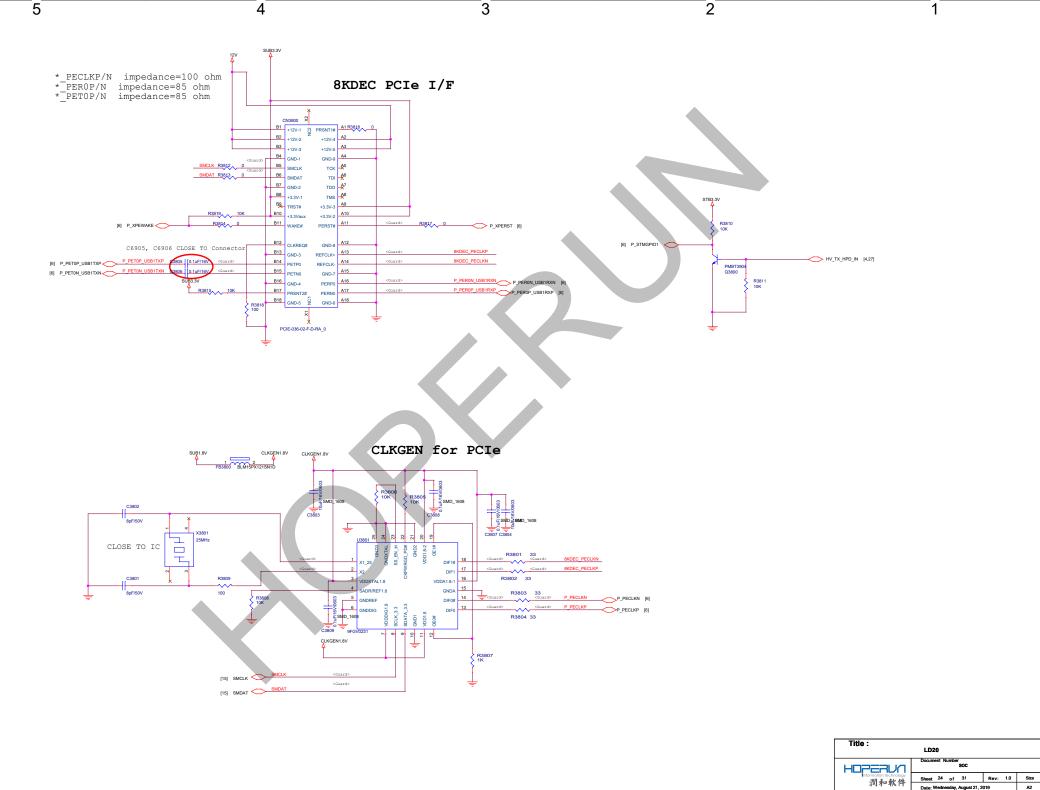
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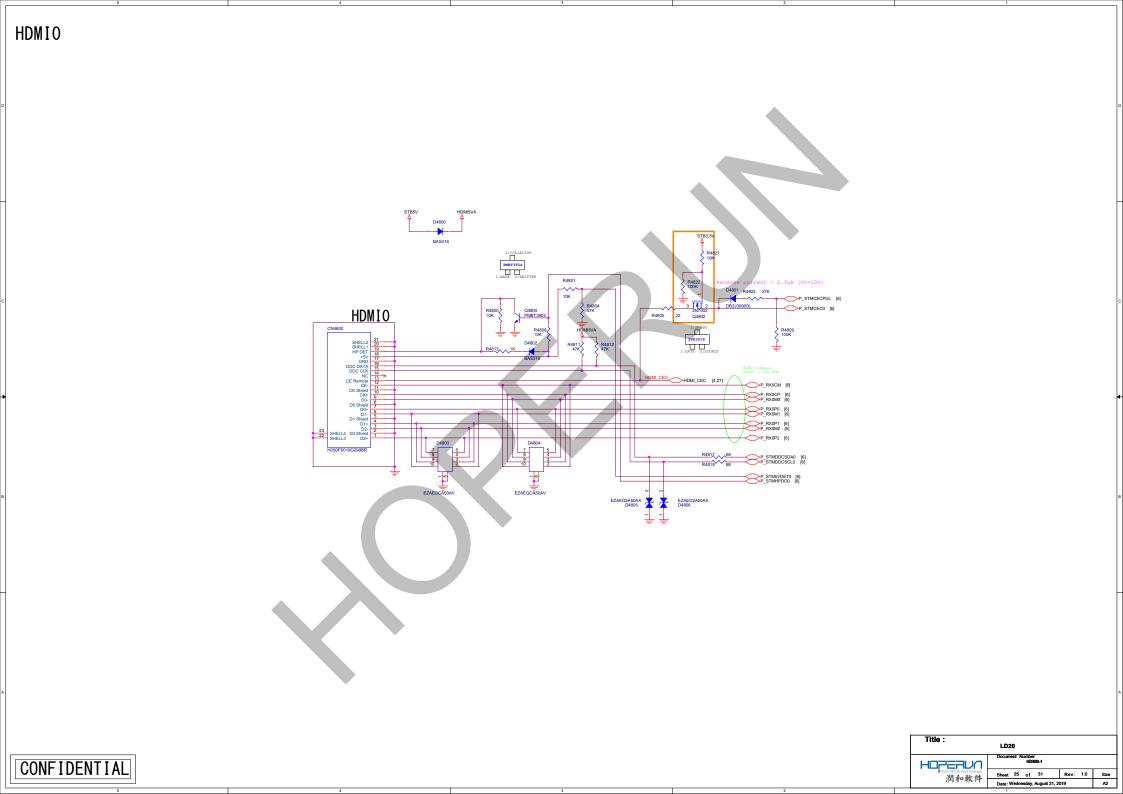


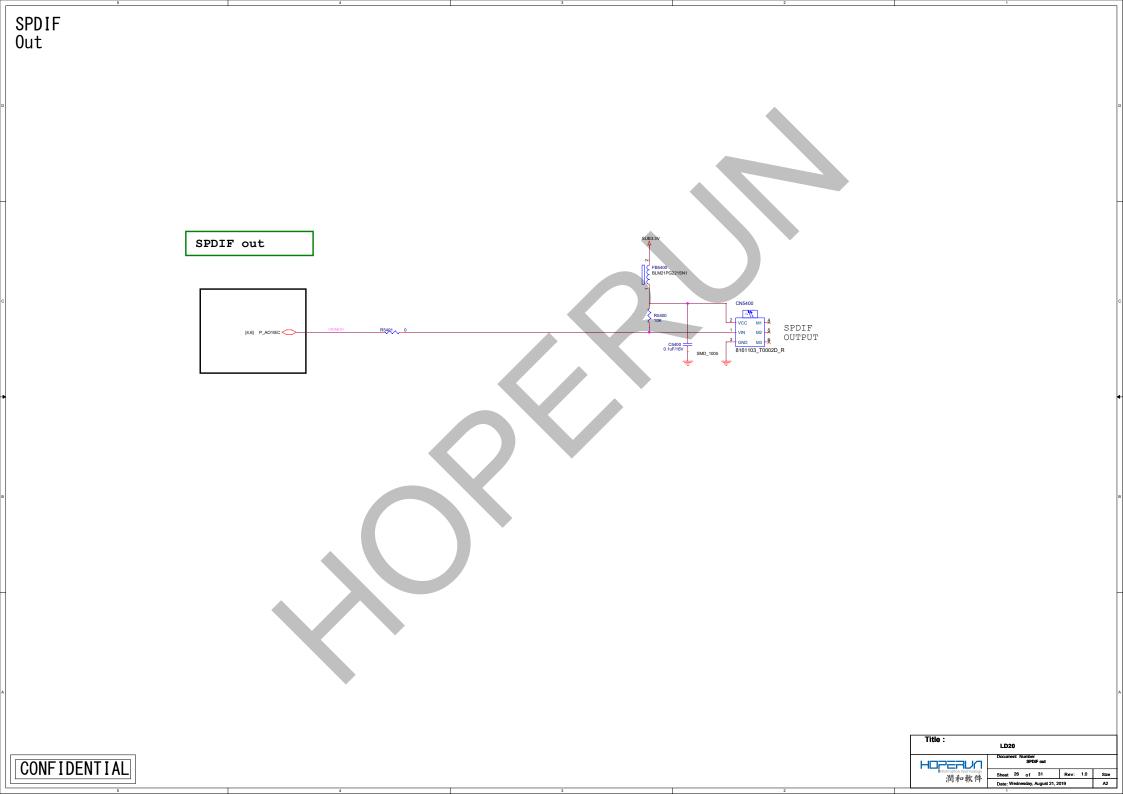


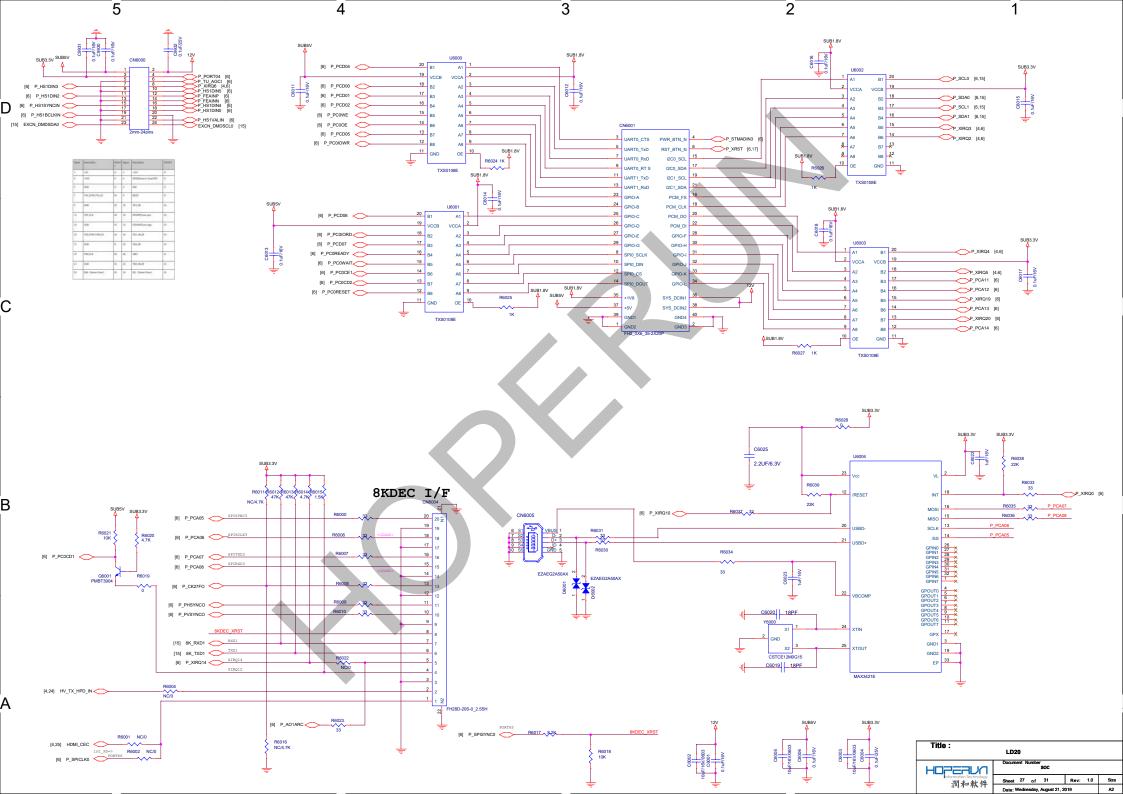




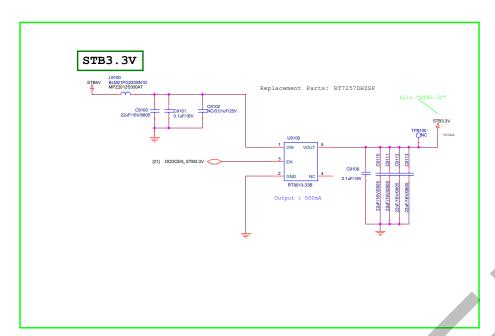
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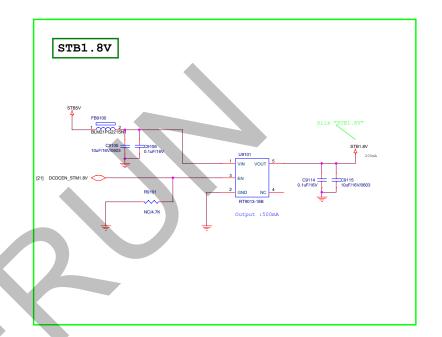








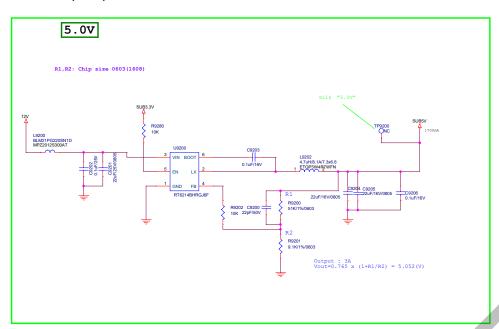


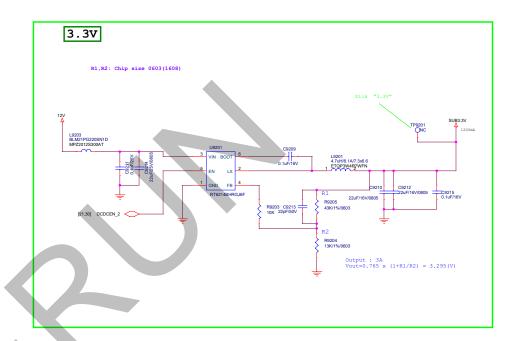


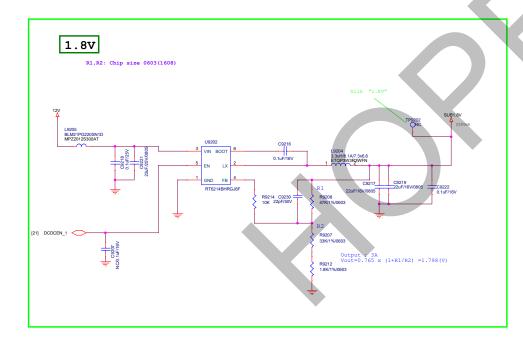
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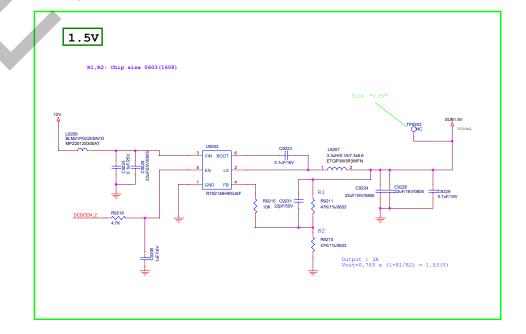
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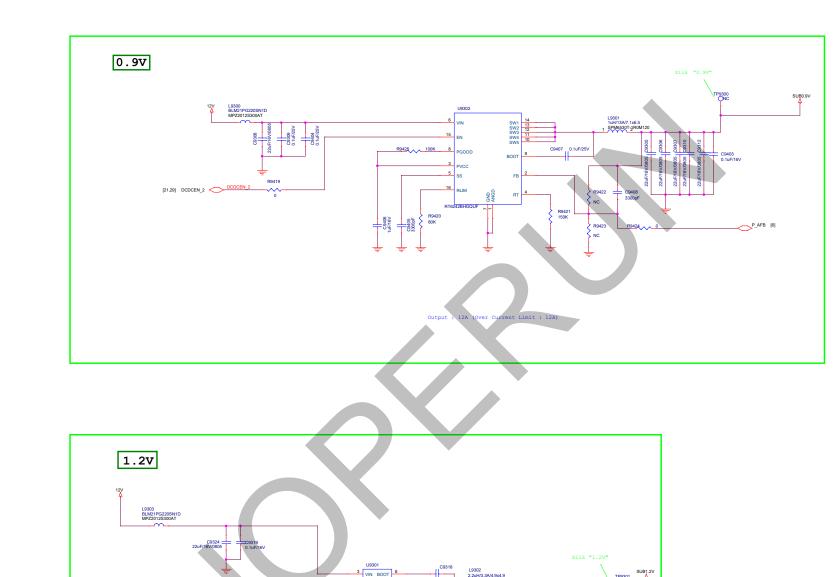


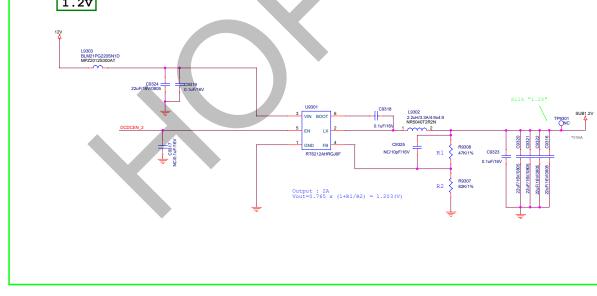




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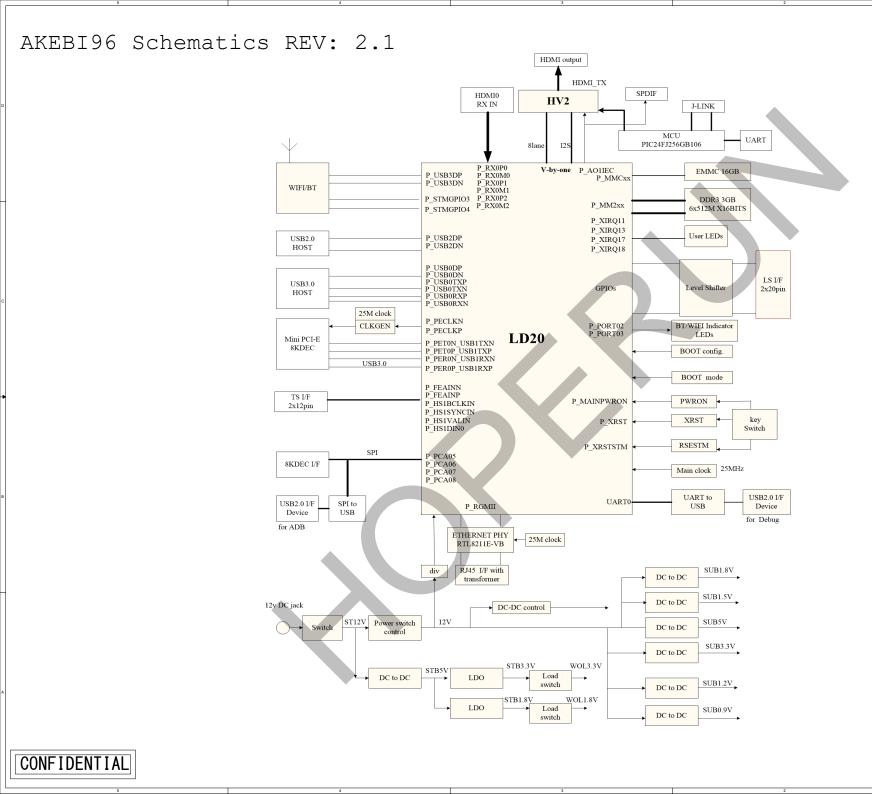




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