



CMOS

COMPLEMENTARY MOS

prepared by:

Gyro A. Madrona
Electronics Engineer

TOPIC OUTLINE

Complementary Metal-Oxide Semiconductor

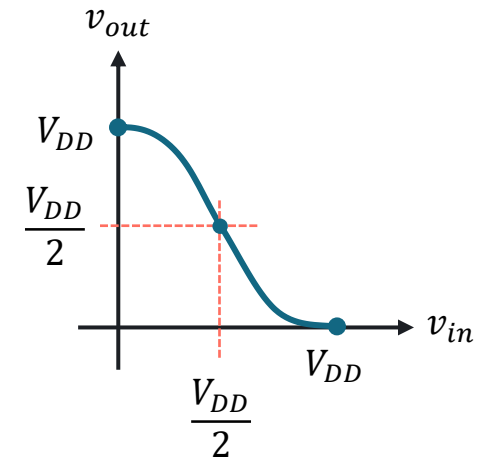
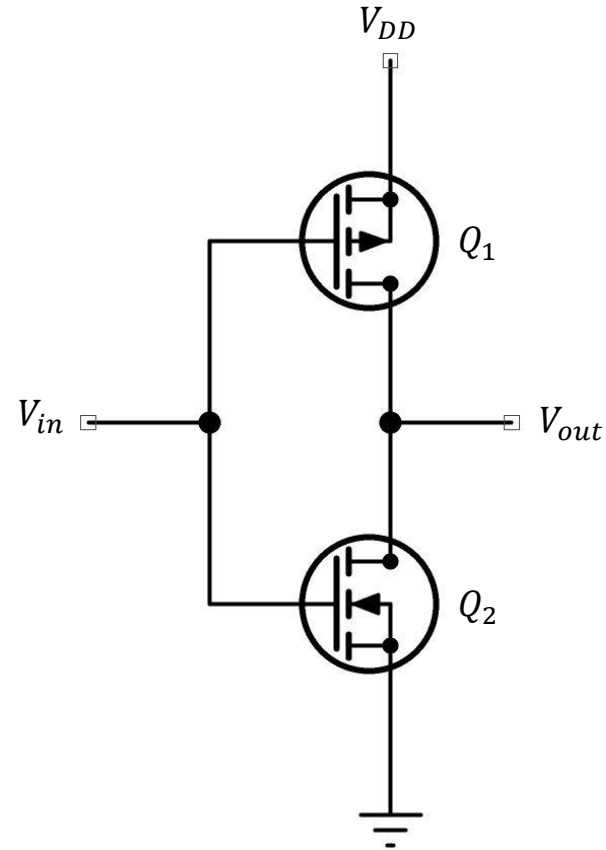


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(Complementary Metal-Oxide Semiconductor) is a technology used in integrated circuit (IC) design that combines n-channel and p-channel MOSFETs in a complementary arrangement.

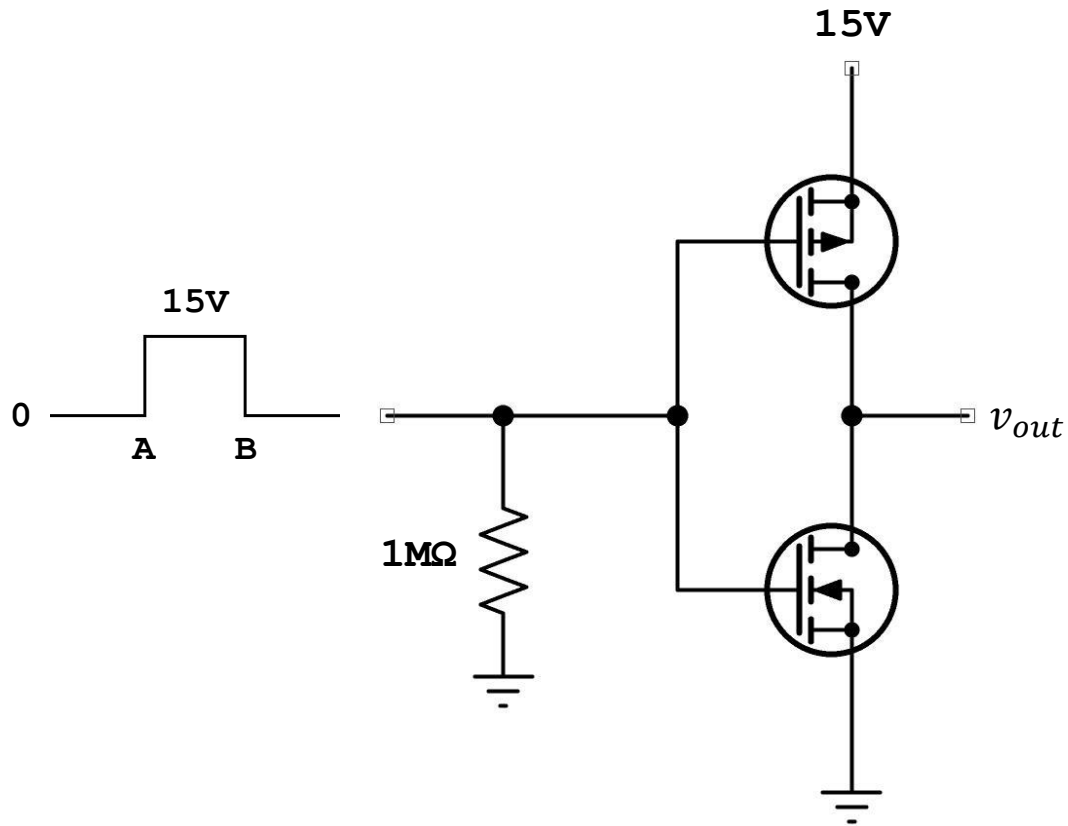
CMOS Inverter



EXERCISE

The MOSFETs have $R_{DS(on)} = 100\Omega$ and $R_{DS(off)} = 1M\Omega$. What does the output waveform look like?

Solution



LABORATORY

