

Cycle	Instruction Retired	Reason
1		
2		
3		
4		
5	lbi r0, 0	The first instruction is in the WriteBack
6	lbi r5, 43	
7	lbi r6, 42	
8	lbi r7, 41	
9	ld r1, r0, 0	
10	nop	To implement Mem-Ex forwarding, we need to stall one cycle
11	st r5, r1, 0	
12	ld r1, r0, 2	
13	nop	To implement Mem-Ex forwarding, we need to stall one cycle
14	st r6, r1, 1	
15	ld r1, r0, 4	
16	nop	To implement Mem-Ex forwarding, we need to stall one cycle
17	st r7, r1, 1	