

# Lab5 Notes

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# 分布式双端口存储器

- **CPU端口**

a: 读/写地址

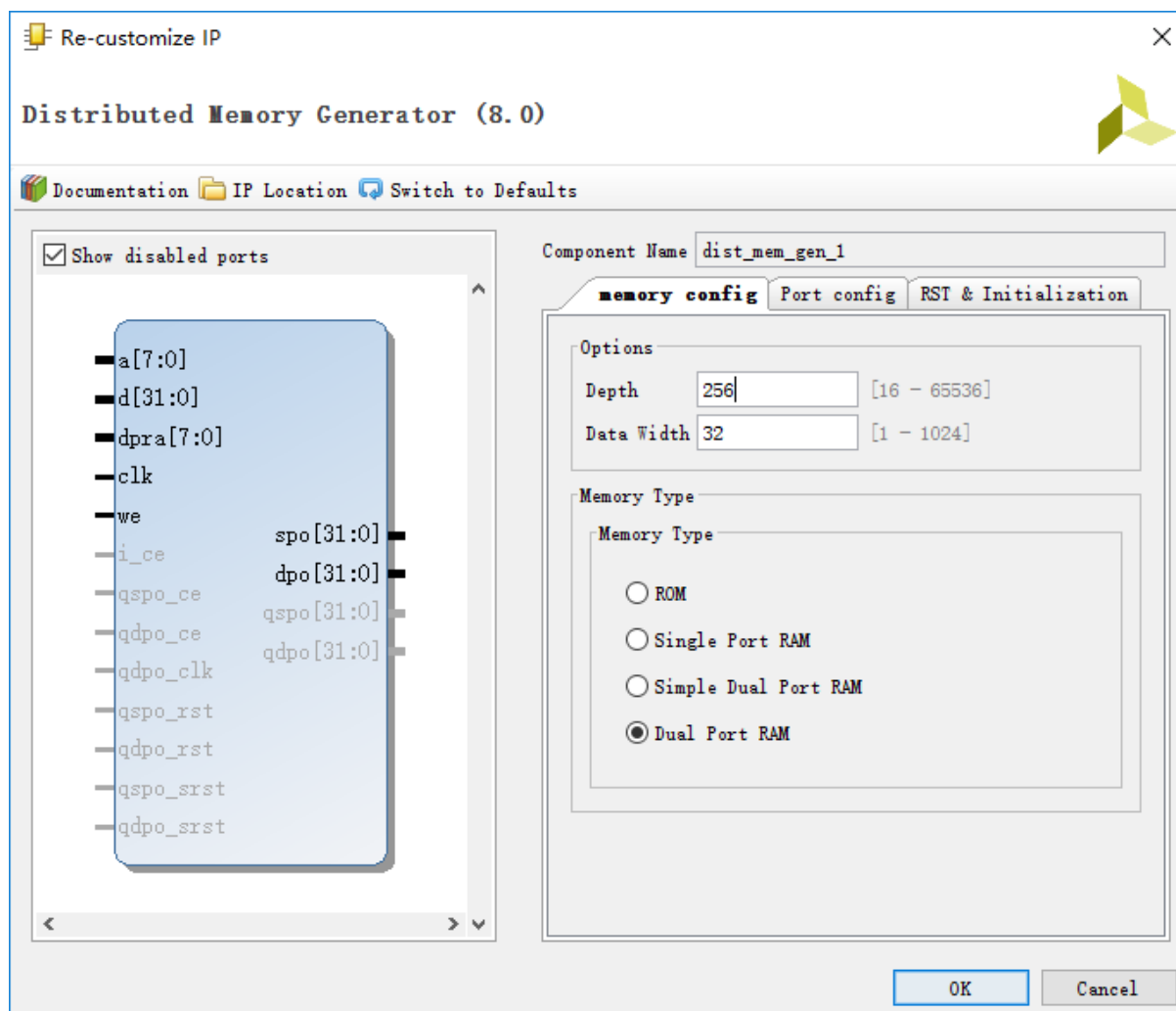
d: 写数据

spo: 读数据

- **调试端口**

dpra: 读地址

dpo: 读数据



# Encoding of the Opcode Field

R-type **op(6 bits)** rs(5 bits) rt(5 bits) rd(5 bits) shamt(5 bits) **funct(6 bits)**

I-type **op(6 bits)** rs(5 bits) rt(5 bits) **addr/immediate(16 bits)**

J-type **op(6 bits)** **addr(26 bits)**

opcode		bits 28..26							
		0	1	2	3	4	5	6	7
bits 31..29		000	001	010	011	100	101	110	111
0	000	<b>SPECIAL <math>\delta</math></b>	REGIMM $\delta$	<b>J</b>	JAL	<b>BEQ</b>	<b>BNE</b>	BLEZ	BGTZ
1	001	<b>ADDI</b>	ADDIU	<b>SLTI</b>	SLTIU	<b>ANDI</b>	<b>ORI</b>	<b>XORI</b>	LUI
2	010	COP0 $\delta$	COP1 $\delta$	COP2 $\theta \delta$	COP1X <sup>1</sup> $\delta$	BEQL $\phi$	BNEL $\phi$	BLEZL $\phi$	BGTZL $\phi$
3	011	$\beta$	$\beta$	$\beta$	$\beta$	SPECIAL2 $\delta$	JALX $\epsilon$	$\epsilon$	SPECIAL3 <sup>2</sup> $\delta \oplus$
4	100	LB	LH	LWL	<b>LW</b>	LBU	LHU	LWR	$\beta$
5	101	SB	SH	SWL	<b>SW</b>	$\beta$	$\beta$	SWR	CACHE
6	110	LL	LWC1	LWC2 $\theta$	PREF	$\beta$	LDC1	LDC2 $\theta$	$\beta$
7	111	SC	SWC1	SWC2 $\theta$	*	$\beta$	SDC1	SDC2 $\theta$	$\beta$

# *SPECIAL* Opcode Encoding of Function Field

R-type op(6 bits) rs(5 bits) rt(5 bits) rd(5 bits) shamt(5 bits) **funct(6 bits)**

function		bits 2..0							
		0	1	2	3	4	5	6	7
bits 5..3		000	001	010	011	100	101	110	111
0	000	SLL <sup>1</sup>	MOVCI $\delta$	SRL $\delta$	SRA	SLLV	*	SRLV $\delta$	SRAV
1	001	JR <sup>2</sup>	JALR <sup>2</sup>	MOVZ	MOVN	SYSCALL	BREAK	*	SYNC
2	010	MFHI	MTHI	MFLO	MTLO	$\beta$	*	$\beta$	$\beta$
3	011	MULT	MULTU	DIV	DIVU	$\beta$	$\beta$	$\beta$	$\beta$
4	100	<b>ADD</b>	ADDU	<b>SUB</b>	SUBU	<b>AND</b>	<b>OR</b>	<b>XOR</b>	<b>NOR</b>
5	101	*	*	<b>SLT</b>	SLTU	$\beta$	$\beta$	$\beta$	$\beta$
6	110	TGE	TGEU	TLT	TLTU	TEQ	*	TNE	*
7	111	$\beta$	*	$\beta$	$\beta$	$\beta$	*	$\beta$	$\beta$

# 测试代码

j \_start

.data

.word 0,8,1,6,0xffffffff,8,1,3,5,0 #编译成机器码时编译器会在前面多加个0

\_start:

addi \$t0,\$0,3        #t0=3

addi \$t1,\$0,5        #t1=5

addi \$t2,\$0,1        #t2=1

add \$s0,\$t1,\$t0    #s0=t1+t0=8 测试add指令 正确继续执行

lw \$s1,12(\$0

bne \$s1,\$s0,\_fail #不正确跳到fail

.....

# 测试代码 (续1)

```
sw $t1,40($0)
lw $s1,40($0)
beq $t1,$s1,_sucess
```

\_fail:

```
sw $0,8($0)      #失败通过看存储器地址0x08里值，若为0
                  #则测试不通过，最初地址0x08里值为0
```

```
j _fail
```

\_sucess:

```
sw $t2,8($0)      #全部测试通过，存储器地址0x08里值为1
j _sucess          #判断测试通过的条件是最后存储器地址
                  #0x08里值为1，说明全部通过测试
```

# COE文件

```
memory_initialization_radix = 16;  
memory_initialization_vector =  
08000000b
```

```
00000000 00000000 00000008  
00000001 00000006 ffffffff8  
00000001 00000003 00000005  
00000000
```

```
20080003 20090005 200a0001
```

```
01288020
```

```
.....
```

```
j _start
```

```
.word
```

```
0,8,1,6,0xffffffff8,1,3,5,0
```

```
addi $t0,$0,3      #t0=3
```

```
addi $t1,$0,5      #t1=5
```

```
addi $t2,$0,1      #t2=1
```

```
add $s0,$t1,$t0
```

# 寄存器使用约定

REGISTER	NAME	USAGE
\$0	\$zero	常量0(constant value 0)
\$1	\$at	保留给汇编器(Reserved for assembler)
\$2-\$3	\$v0-\$v1	函数调用返回值(values for results and expression evaluation)
\$4-\$7	\$a0-\$a3	函数调用参数(arguments)
\$8-\$15	\$t0-\$t7	暂时的(或随便用的)
\$16-\$23	\$s0-\$s7	保存的(或如果用，需要SAVE/RESTORE的)(saved)
\$24-\$25	\$t8-\$t9	暂时的(或随便用的)
\$28	\$gp	全局指针(Global Pointer)
\$29	\$sp	堆栈指针(Stack Pointer)
\$30	\$fp	帧指针(Frame Pointer)
\$31	\$ra	返回地址(return address)



# The End