

TIVA C LAUNCPAD 1.0

RELEASED 16-MAY-2021

.....
1 COVER PAGE

2 BLOCK DIAGRAM

3-5 SCHEMATICS

DESIGN CONSIDERATIONS:

LAYOUT NOTE:
Se utilizarán 6 capas (Top Layer,
GND Plane, Signal Layer, Signal
Layer, Power Plane, Bottom Layer)

DESIGN NOTE:
Todos los componentes utilizados
serán SMD

DESIGN NOTE:
Todos los componentes deben ser
colocados en la top layer.

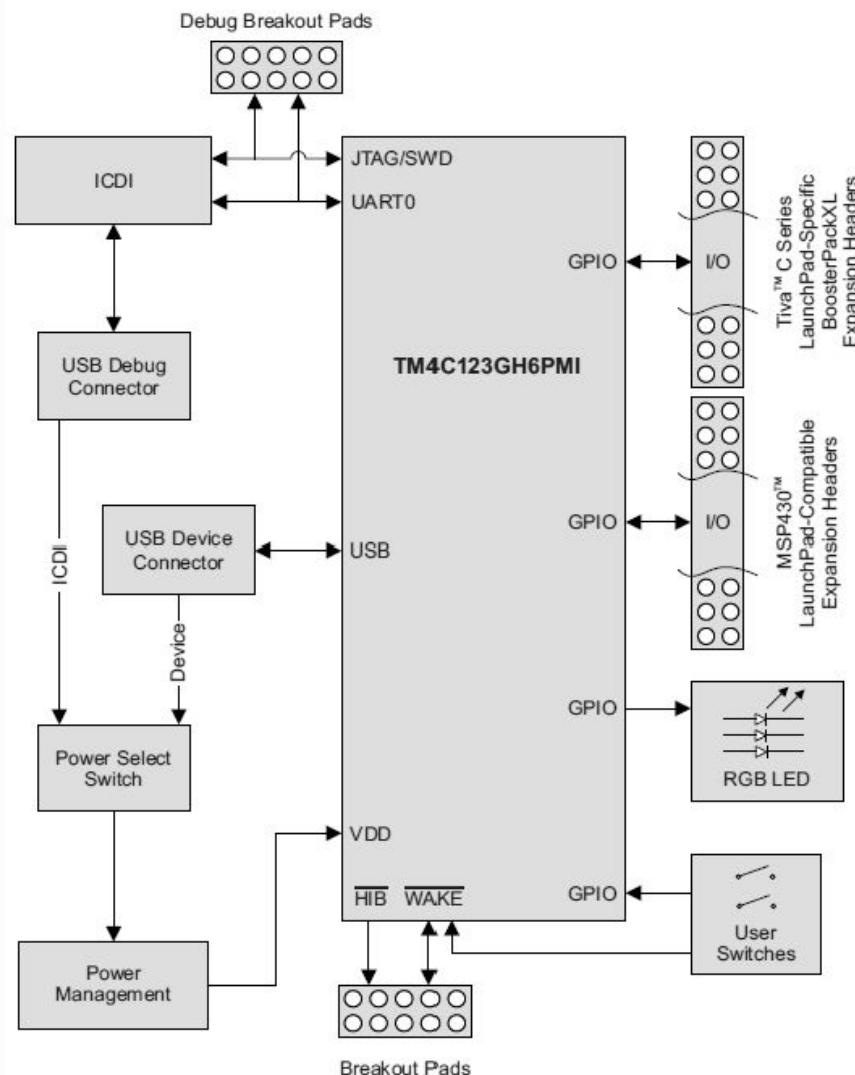
Project: ProyectoMulticapa.PrjPcb

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Revisión: 1.0 Design: Ángel O., Victor V. Date: 30/05/2021

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Diagrama de bloques



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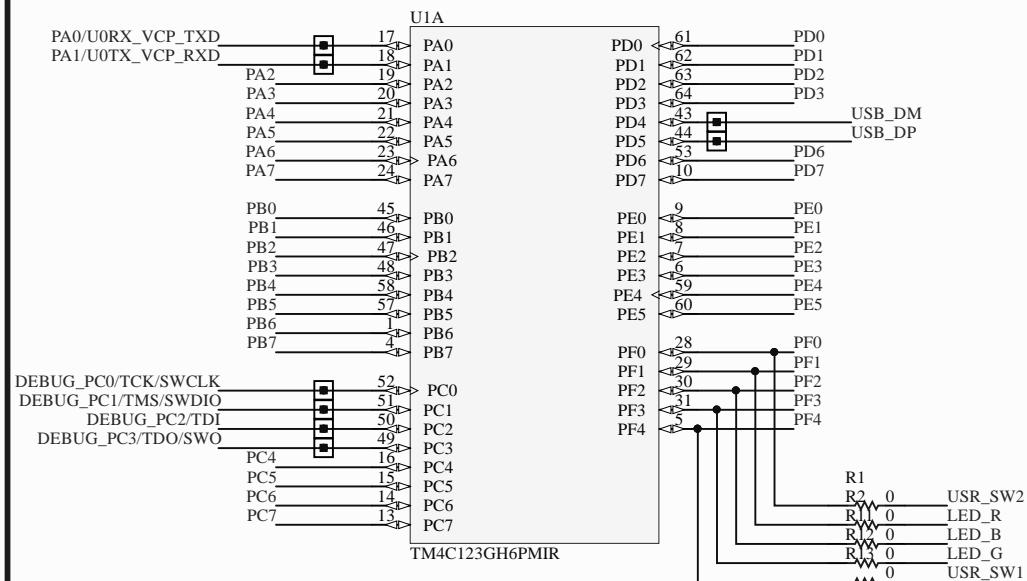
Page Content: [2] BLOCK DIAGRAM.SchDoc

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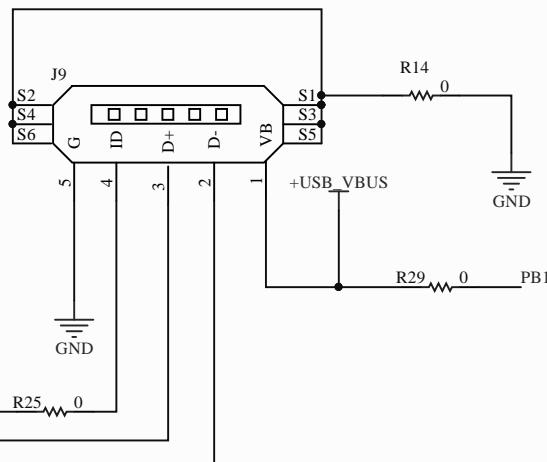
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Microcontroller, USB, Expansion, Buttons and LED

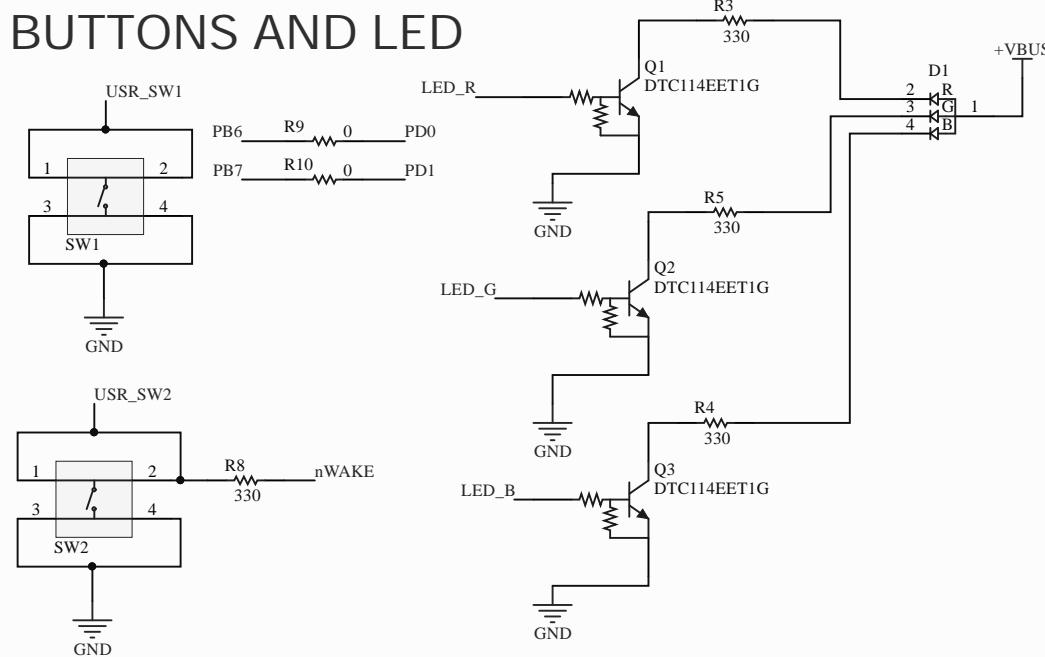
MAIN MICROCONTROLLER



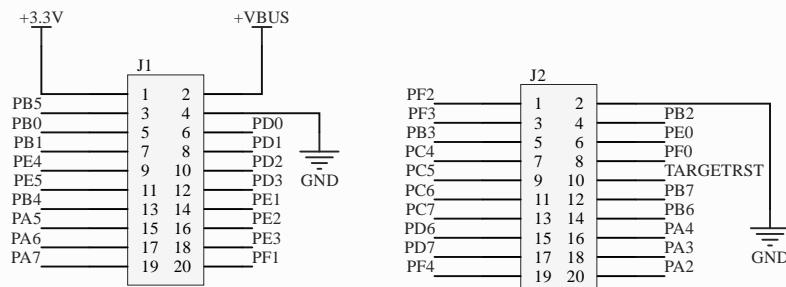
USB



BUTTONS AND LED

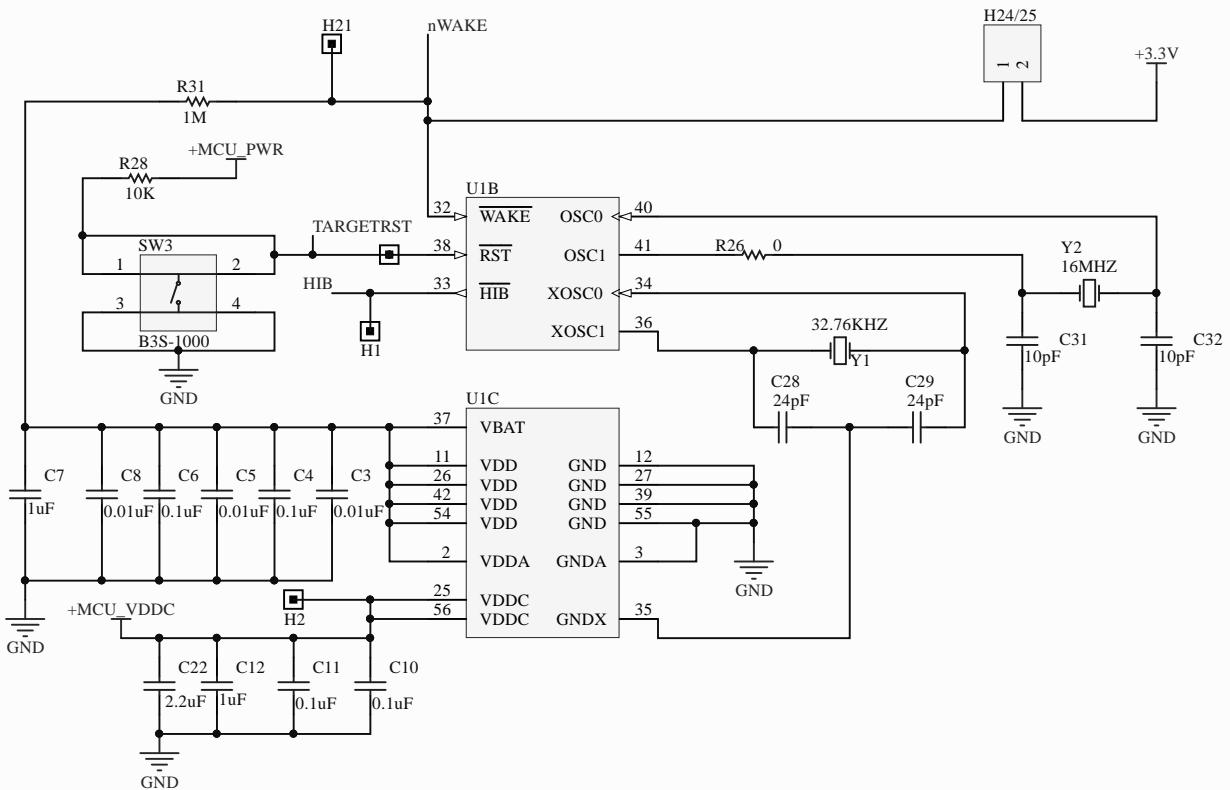


EXPANSION

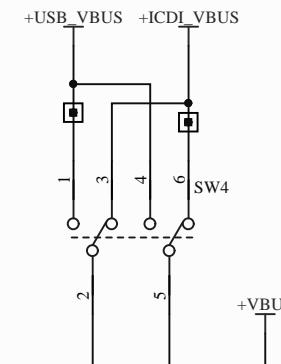


Power Management

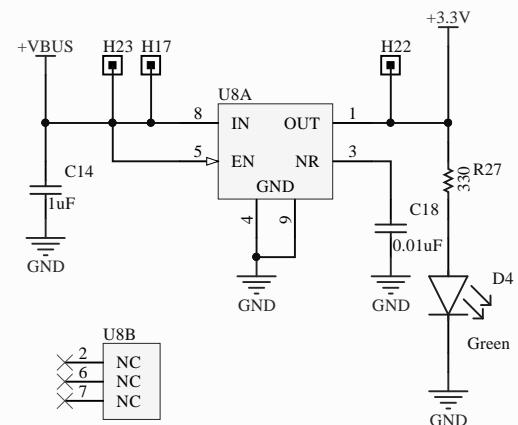
MAIN MICROCONTROLLER



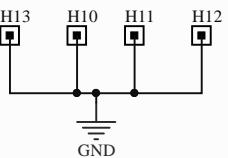
POWER SELECT



3.3V REGULATOR



GROUNDS



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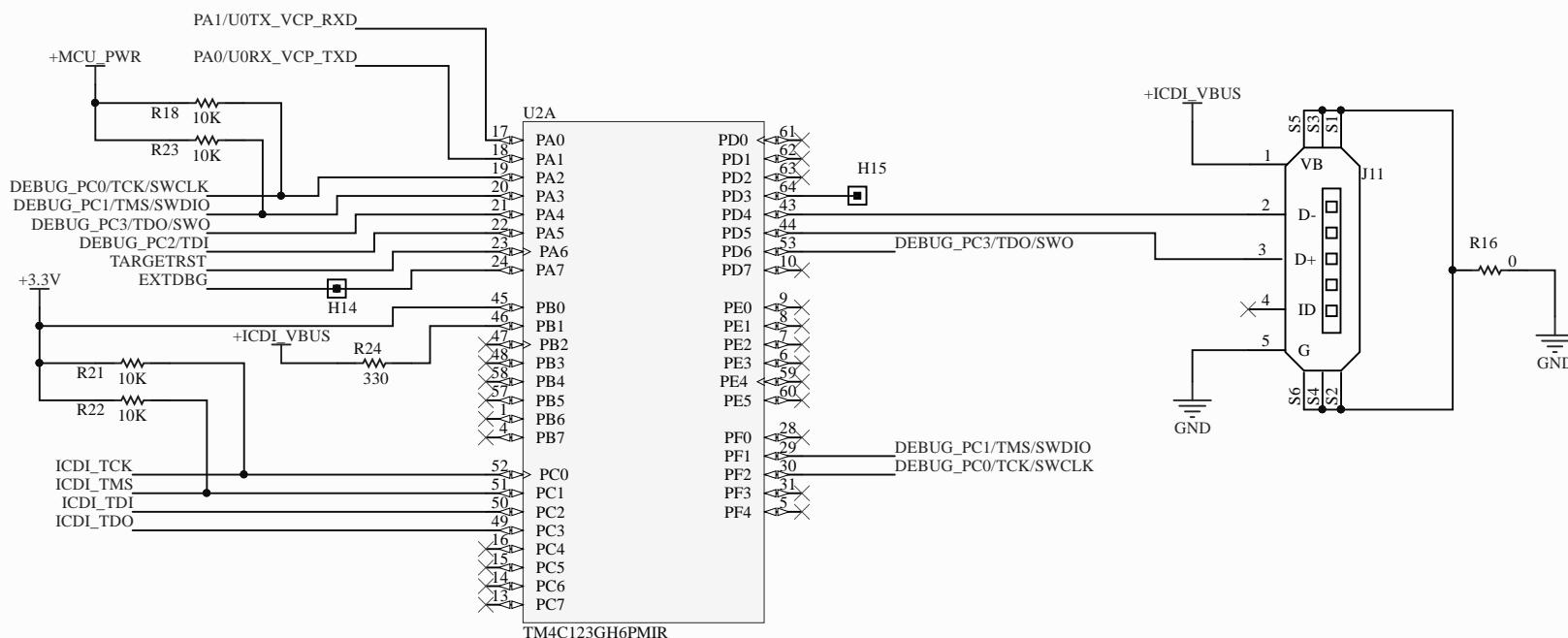
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In Circuit Debug Interface

ICDI



A

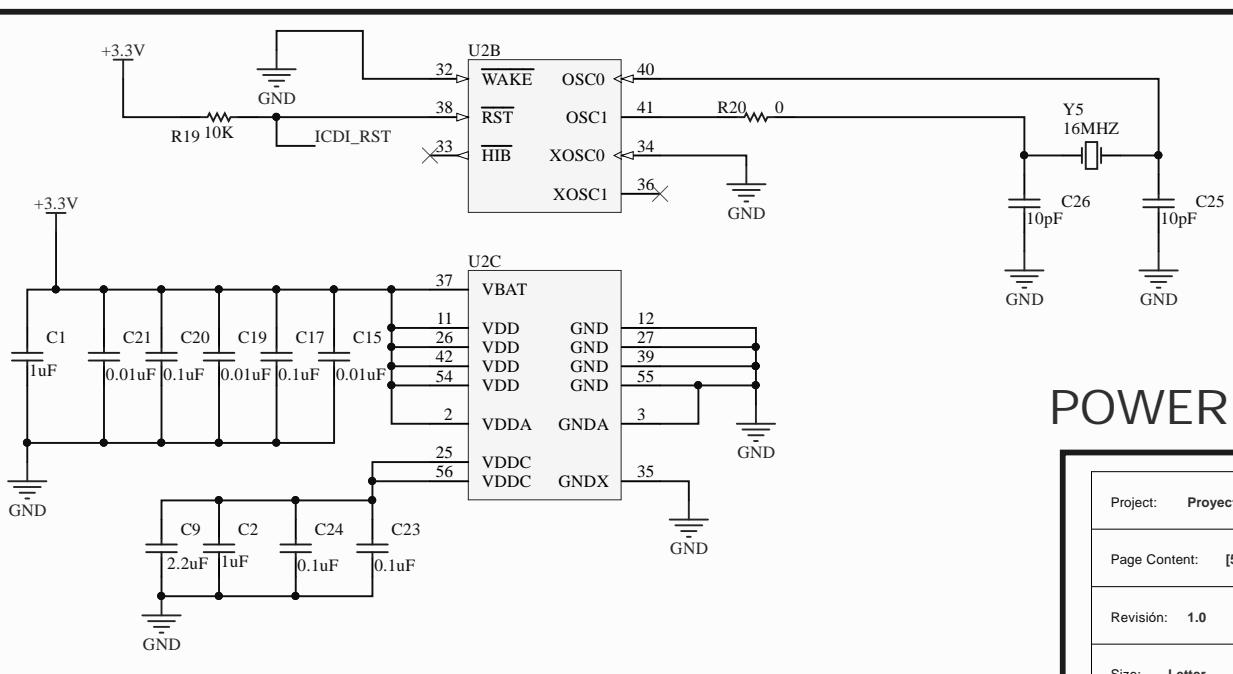
A

B

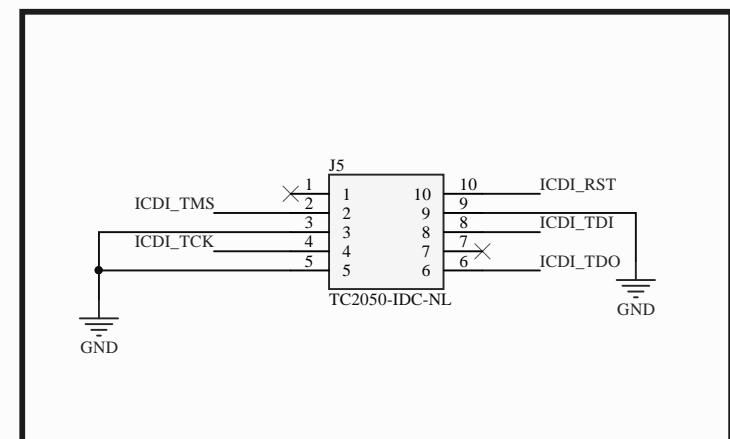
B

C

C



POWER



Project: ProyectoMulticapa.PjPcb

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[1] COVER PAGE



[2] BLOCK DIAGRAM



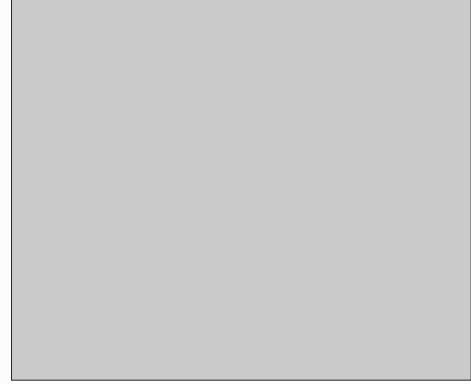
[3] Microcontroller, USB, Expansion, Buttons and LED



[4] Power Management



[5] In Circuit Debug Interface



Schematic Status Explanation

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.

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