

Soldering and Mounting Guidelines for the LGA Accelerometer Sensor to a PC Board

by: Kimberly Tuck, Cheol Han
Sensors and Actuator Solutions Division
Tempe, AZ

INTRODUCTION

MEMS based sensors are sensitive to Printed Circuit Board (PCB) reflow processes. For optimal zero-g offset after PCB mounting, care must be taken to PCB layout and reflow conditions. This application note is a guideline for soldering and mounting the LGA package inertial sensors. The purpose of these guidelines is to minimize the stress on the package after board mounting. Both the MMA73x1L 3-axis analog output family of accelerometers and the MMA745xL digital output accelerometer use the Land Grid Array (LGA) package platform. This application note describes suggested methods of soldering these devices to the PC board for consumer applications. Figure 1 shows the bottom view of LGA 14 lead, 3 x 5 mm individual sensor device. Figure 2 shows the recommended PCB land pattern for the package.

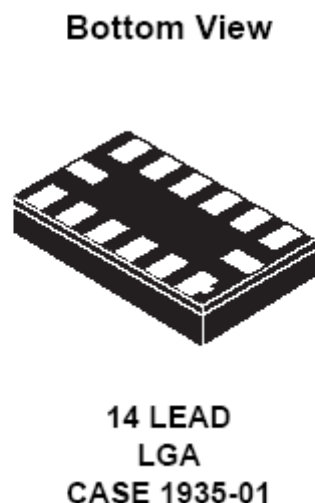


Figure 1. LGA 14-Lead, 5 x 3 mm Die Sensor

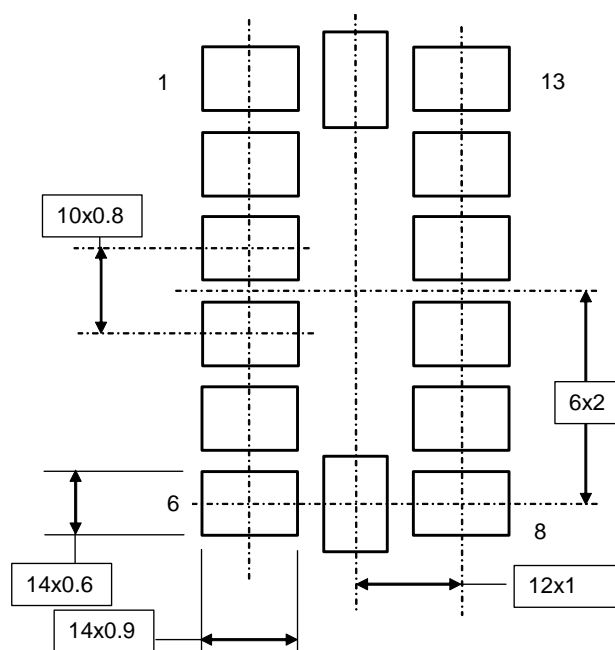


Figure 2. Recommended PCB Land Pattern for the 5 x 3 mm LGA Package

OVERVIEW OF SOLDERING CONSIDERATIONS

Information provided here is based on experiments executed on LGA devices. They do not represent exact conditions present at a customer site. Hence, information herein should be used as a guidance only and process and design optimizations are recommended to develop an application specific solution. It should be noted that with the proper PCB footprint and solder stencil designs the package will self-align during the solder reflow process.

The following are the recommended guidelines to follow for mounting LGA sensors for consumer applications.

PCB Mounting Recommendations

1. The PCB land should be designed with Non Solder Mask Defined (NSMD) as shown in Figure 5.
2. No additional metal pattern underneath package as shown in Figure 4.

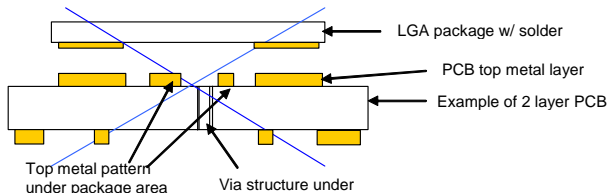


Figure 3. Incorrect PCB Top Metal Pattern Under Package

3. PCB land pad is 0.9mm x 0.6mm which is the size of the package pad plus 0.1mm as shown below in Figure 5.

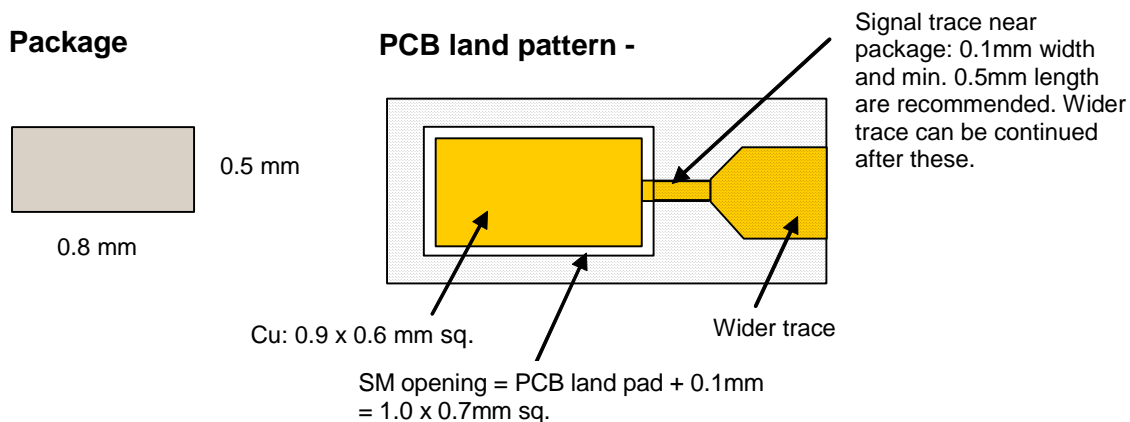


Figure 5. Recommended PCB Land Pad, Solder Mask, and Signal Trace Near Package Design

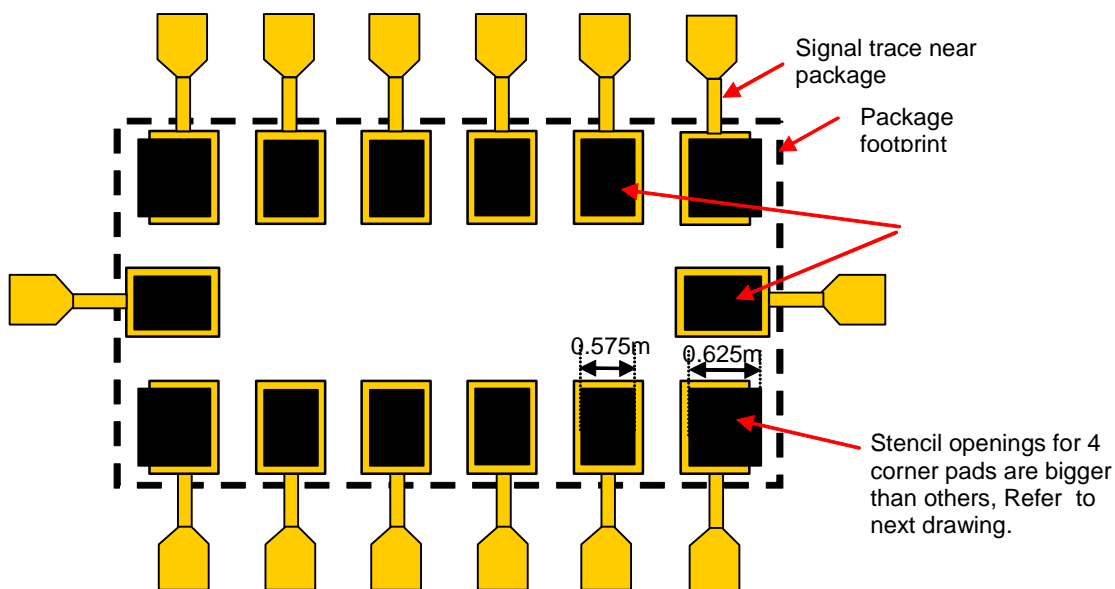


Figure 6. Stencil Design Guidelines



Figure 4. Correct PCB Top Metal Pattern Under Package

4. The solder mask opening is equal to the size of the PCB land pad plus an extra 0.1mm as shown in Figure 5.
5. The stencil aperture size is equal to the PCB land pad - 0.025mm. Also note that for the 4 corner pads the aperture size must be larger for solder balancing as shown in Figure 6 and Figure 7. A 6mil thick stencil is recommended.

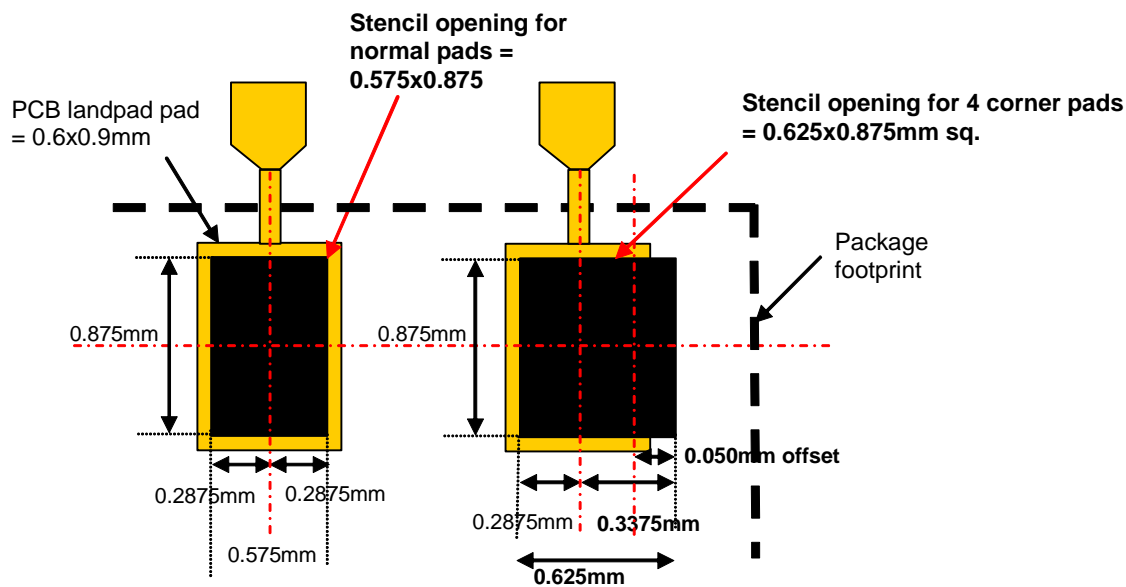


Figure 7. Stencil Design Guidelines (detailed dimensions for corner pads)

6. Do not place any components or vias at a distance less than 2mm from the package land area. This may cause additional package stress if it is too close to the package land area.
7. Signal traces connected to pads should be as symmetric as possible. Put dummy traces on N/C pads in order to have same length of exposed trace for all pads. Signal traces with 0.1mm width and min. 0.5mm length for all PCB land pads near the package are recommended as shown in [Figure 5](#), [Figure 6](#), and [Figure 7](#). Wider trace can be continued after the 0.5mm zone.
8. Use a standard pick and place process and equipment. Do not use a hand soldering process.
9. It is recommended to use a cleanable solder paste with an additional cleaning step after SMT mount.
10. Do not use a screw down or stacking to fix the PCB into an enclosure because this could bend the PCB putting stress on the package.

11. The PCB should be rated for the multiple lead-free reflow condition with max 260°C temperature.
12. The recommended peak temperature for the solder paste for lead free (Pb-free) is 245°C - 250°C and for the tin-lead (Sn-Pb), 215°C - 225°C.

Please cross reference with the device data sheet for mounting guidelines specific to the exact device used.

Freescale LGA sensors are compliant with Restrictions on Hazardous Substances (RoHS), having halide free molding compound (green) and lead-free terminations. These terminations are compatible with tin-lead (Sn-Pb) as well as tin-silver-copper (Sn-Ag-Cu) solder paste soldering processes. Reflow profiles applicable to those processes can be used successfully for soldering the devices.

SUMMARY

There are many new applications being designed using LGA die accelerometers. This document suggests soldering methods for the MMA73x1L family and the MMA745xL accelerometers.

How to Reach Us:

Home Page:

www.freescale.com

Web Support:

<http://www.freescale.com/support>

USA/Europe or Locations Not Listed:

Freescale Semiconductor, Inc.
Technical Information Center, EL516
2100 East Elliot Road
Tempe, Arizona 85284
1-800-521-6274 or +1-480-768-2130
www.freescale.com/support

Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH
Technical Information Center
Schatzbogen 7
81829 Muenchen, Germany
+44 1296 380 456 (English)
+46 8 52200080 (English)
+49 89 92103 559 (German)
+33 1 69 35 48 48 (French)
www.freescale.com/support

Japan:

Freescale Semiconductor Japan Ltd.
Headquarters
ARCO Tower 15F
1-8-1, Shimo-Meguro, Meguro-ku,
Tokyo 153-0064
Japan
0120 191014 or +81 3 5437 9125
support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor China Ltd.
Exchange Building 23F
No. 118 Jianguo Road
Chaoyang District
Beijing 100022
China
+86 010 5879 8000
support.asia@freescale.com

For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center
P.O. Box 5405
Denver, Colorado 80217
1-800-441-2447 or +1-303-675-2140
Fax: +1-303-675-2150
LDCForFreescaleSemiconductor@hibbertgroup.com

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale™ and the Freescale logo are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners.

© Freescale Semiconductor, Inc. 2008. All rights reserved.

