Francesco Verna-Ketel

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SUMMARY

Fourth year college student eager to learn and gain professional work experience with educational experience in C/C++ programming, FPGAs, AC/DC circuit analysis, signal processing, digital logic design, and basic computer architecture design.

EDUCATION

Oregon State University
Electrical and Computer Engineering — 2014-

Accomplishments

- 3.81 GPA
- Recipient of the TP Forever Memorial scholarship.
- Programmed simple games and a read/write storage program in C++.
- Contributed to the design and implementation of a "clicking game" using an FPGA, VHDL, button board, and seven segment display.
- Created an analog to digital voltmeter using an ADC, FPGA, VHDL, and seven segment display.
- Currently working under Assistant Professor Lizhong Chen of OSU in researching deep learning artificial intelligence and its applications.

Relevant Coursework

- Computer Architecture - Basic computer architecture and the issues involved in the design of instruction set architecture including processing, pipelining, and memory organization.
- VLSI System Design - Introduction to custom and basic digital integrated circuit design as used in VLSI systems such as state machines, timing diagrams, block diagrams, and SystemVerilog simulation and synthesis.
- Electronic Materials and Devices - Semiconductor fundamentals and physical dimensions of pn junctions and Schottky barrier diodes.
- Computer Networks - Computer network principles, fundamental networking concepts, packet and network control, and multiple access techniques.
- Data Structures - Analysis and use of abstract data types, dynamic arrays, linked lists, trees/graphs, binary trees, storage management, and complexity analysis of data structures.

Cleveland High School
High School Diploma and Full International Baccalaureate Diploma — 2010-2014

Participated in school sports and American Math Competitions while enrolled in the International Baccalaureate Diploma program. Recognized as a Valedictorian.

SKILLS

- Some experience with bash, C, C++, VLSI, and assembly language.
- Experience in soldering and circuit analysis.
- Can operate an oscilloscope, digital multimeters, and function generators.
- Familiar with Unix/Linux, OSX, and Windows.

REFERENCES

Available upon request.