Daniel H. He

9320 NW Murdock St.
Portland, OR 97229

971 255 9200

hed@oregonstate.edu

SUMMARY

Hard working and eager to learn college third year with experience in hands on work and knowledge in programming. Skilled in C, C++, and Python. Extensive experience with soldering, programmed in VHDL, programmed in assembly, and experience working with FPGAs and AVRs.

EDUCATION

Oregon State University

Electrical Engineering and Computer Science — 2014-present

- GPA: 3.77 GPA

Relevant Coursework

- VLSI System Design – Introduction to custom and semi-custom digital integrated circuit design as used in VLSI systems.
- Signals and Systems - Analytical techniques for continuous-time and discrete time signal, system and circuit analysis.
- Computer Architecture – Using processors, memories, and I/O devices as building blocks.

Academic Projects

- Reflex Tester
  Designed using a FPGA (Mach X02 FPGA), button board, and seven-segment display.
- Programmed abstract data type libraries using C.
- Analog to digital voltmeter
  Created by using a FPGA (Mach X02 FPGA) and programming in VHDL.
- Crossover system for a speaker, bass and tweeter
  Designed and built with op-amps, capacitors and resistors. Three different filters were built to output to three different loads, which were designed to handle a certain range of low to high frequencies.
- Adjustable Gain Amplifier
  Designed and built with only capacitors, resistors, and potentiometers. Final product made using a PCB designed on Eagle and outputs to two speakers.
- Remote Controlled Car
  Using two AVR's (Mega128 board) a remote control and a remote-controlled car was programmed in assembly language.

SKILLS

- Proficient in C, C++, STL, C# programming.
- Skills at soldering and circuit design.
- Knowledge in VHDL and Verilog programming.
- Experience with LTSpice and NGSpice.
- Can operate an oscilloscope, digital multi-meters, and function generators.
- Experience with assembly programming.
- Debugging using GDB and valgrind.

REFERENCES

Available upon request.