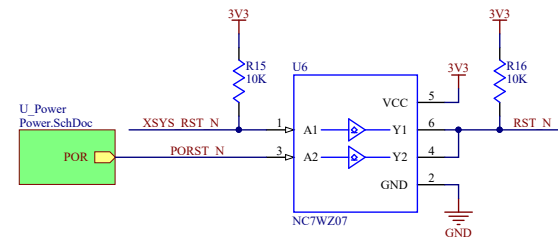
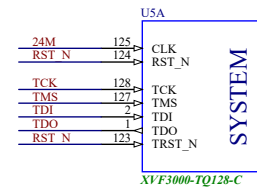
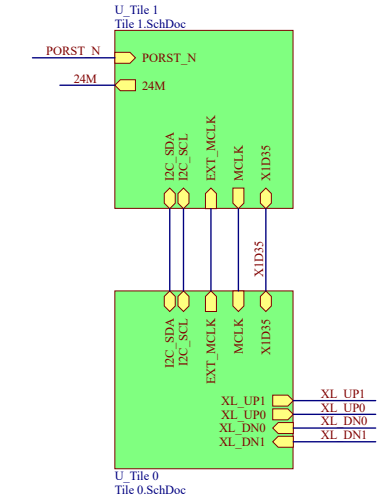
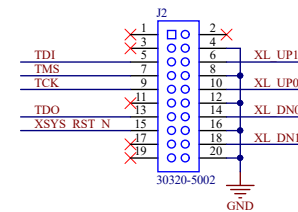
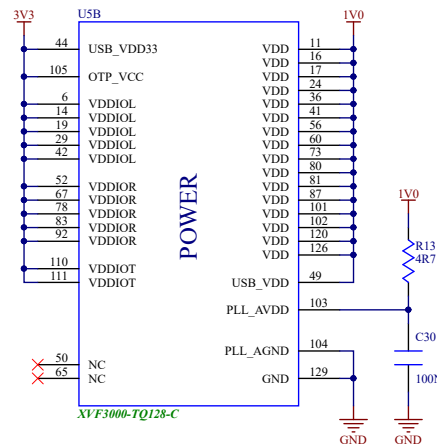
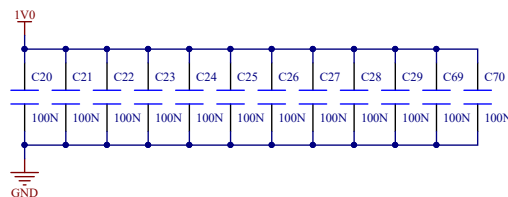
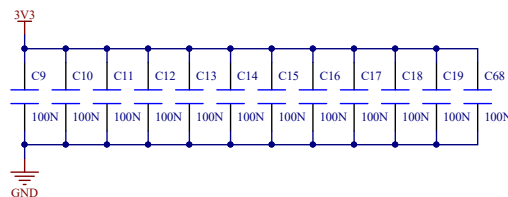
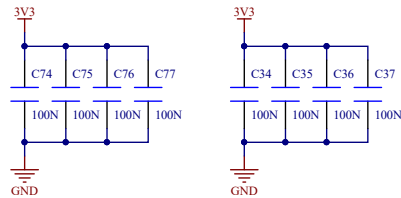
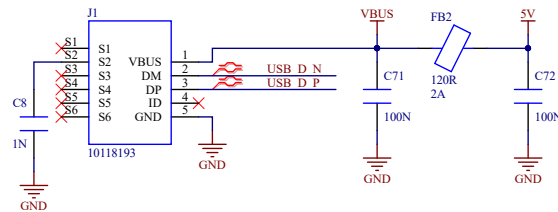
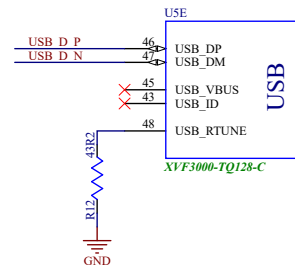


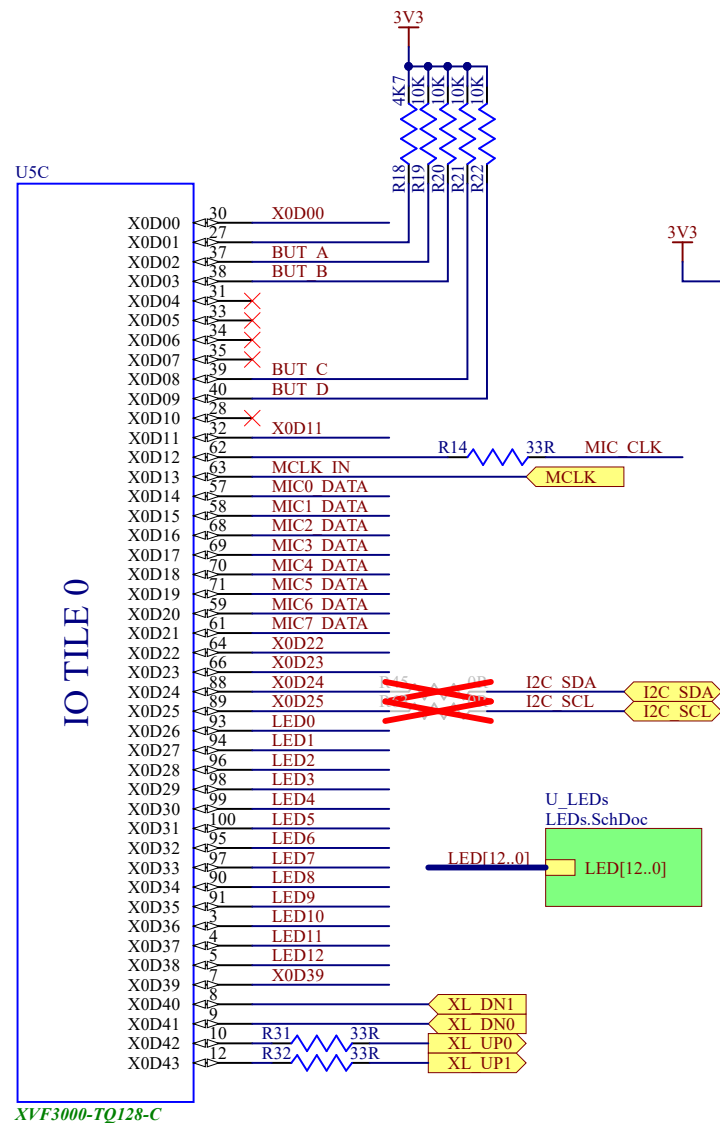
USB_D_N ON PIN 2 FOR FLOW THROUGH LAYOUT



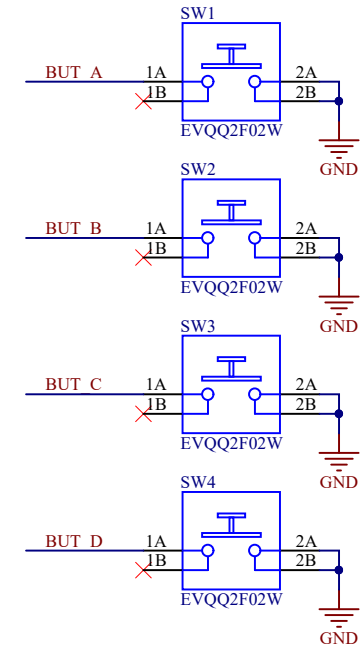
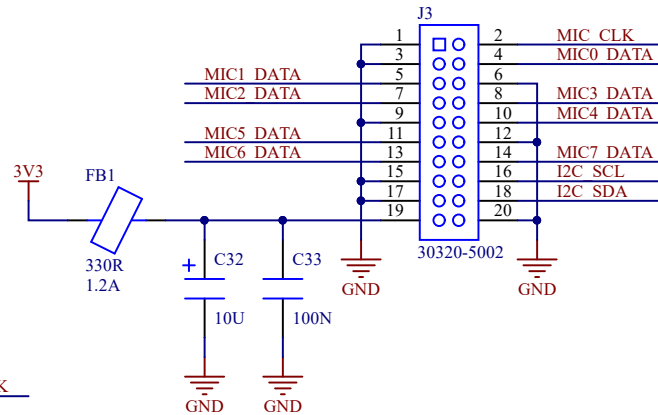
PROD1
LABEL-XLAB-0001

FM1 FIDUCIAL
FM3 FIDUCIAL
FM5 FIDUCIAL
FM2 FIDUCIAL
FM4 FIDUCIAL
FM6 FIDUCIAL

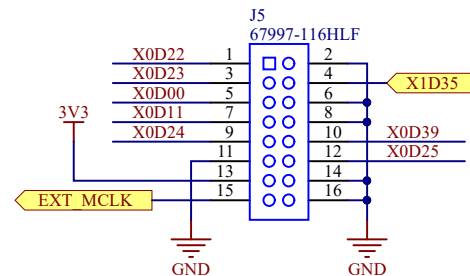
Project Name		
Mic Array Motherboard - Dual Tile		
Build B	Sheet Name Top Level.SchDoc	Rev 1V1
Date 05/09/2017	Sheet 1 of 5	



MIC ARRAY CONNECTOR



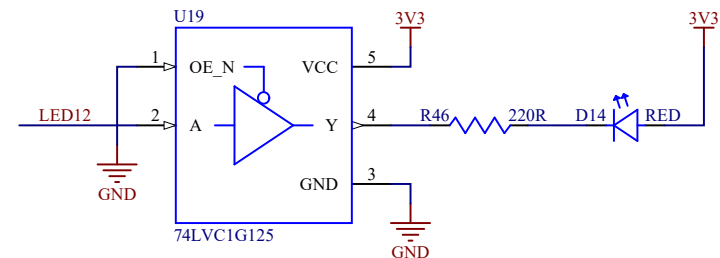
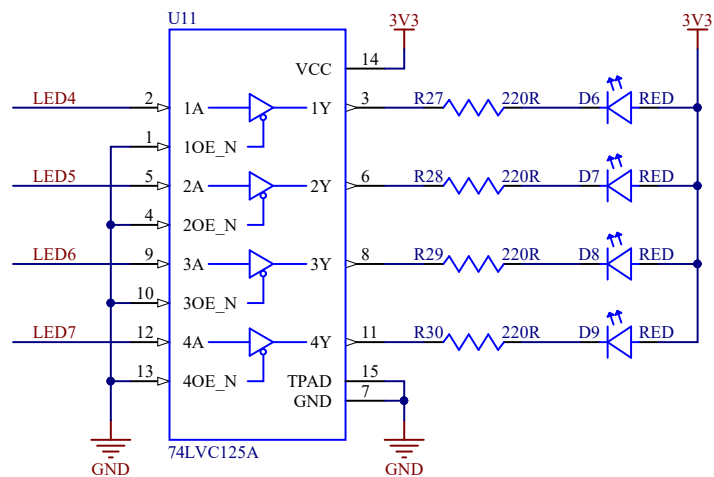
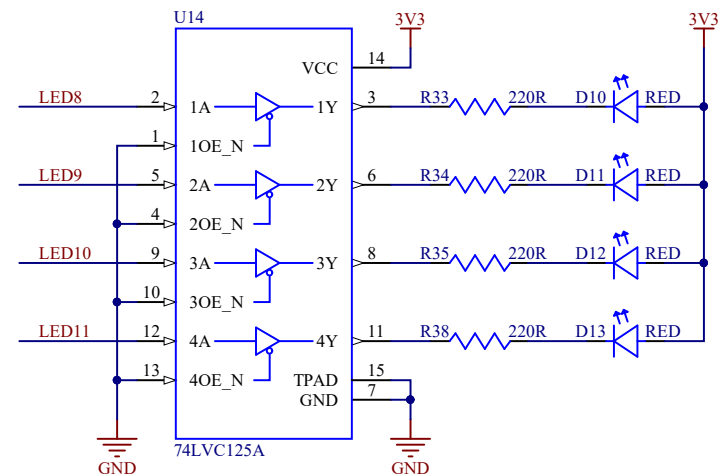
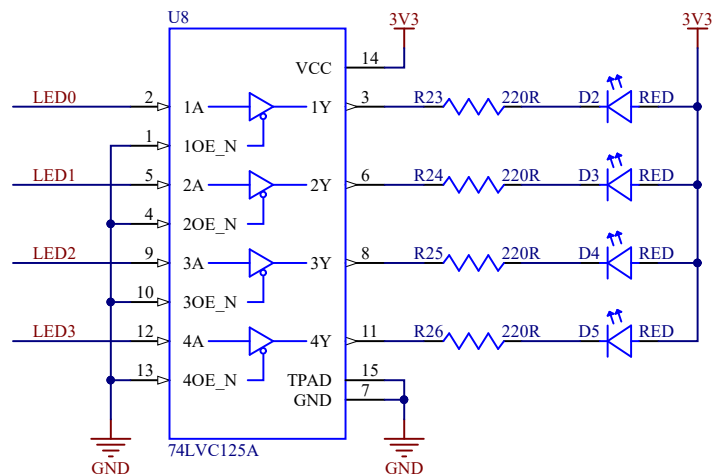
EXPANSION HEADER



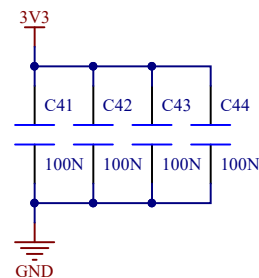
Project Name

Mic Array Motherboard - Dual Tile

Build	Sheet Name	Rev
B	Tile 0.SchDoc	1V1
Date	05/09/2017	Sheet 2 of 5

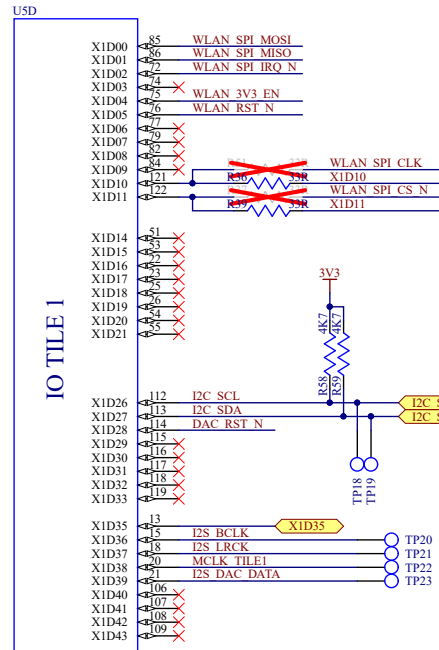


LED[12..0] LED[12..0]

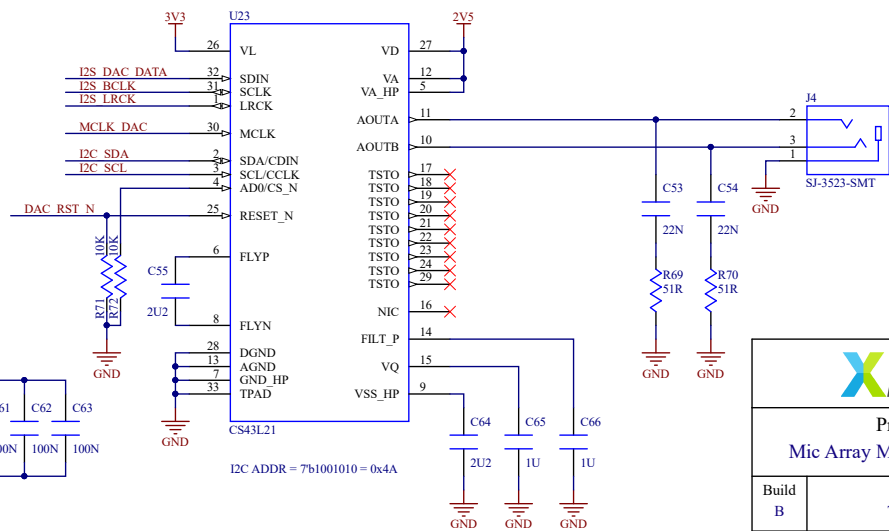
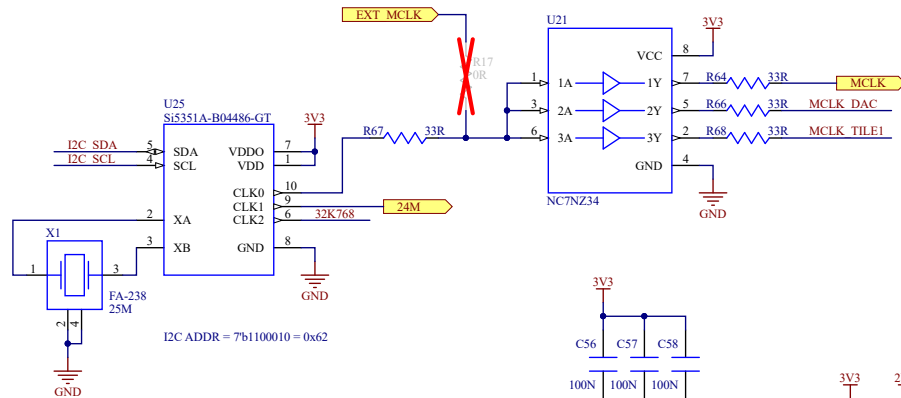
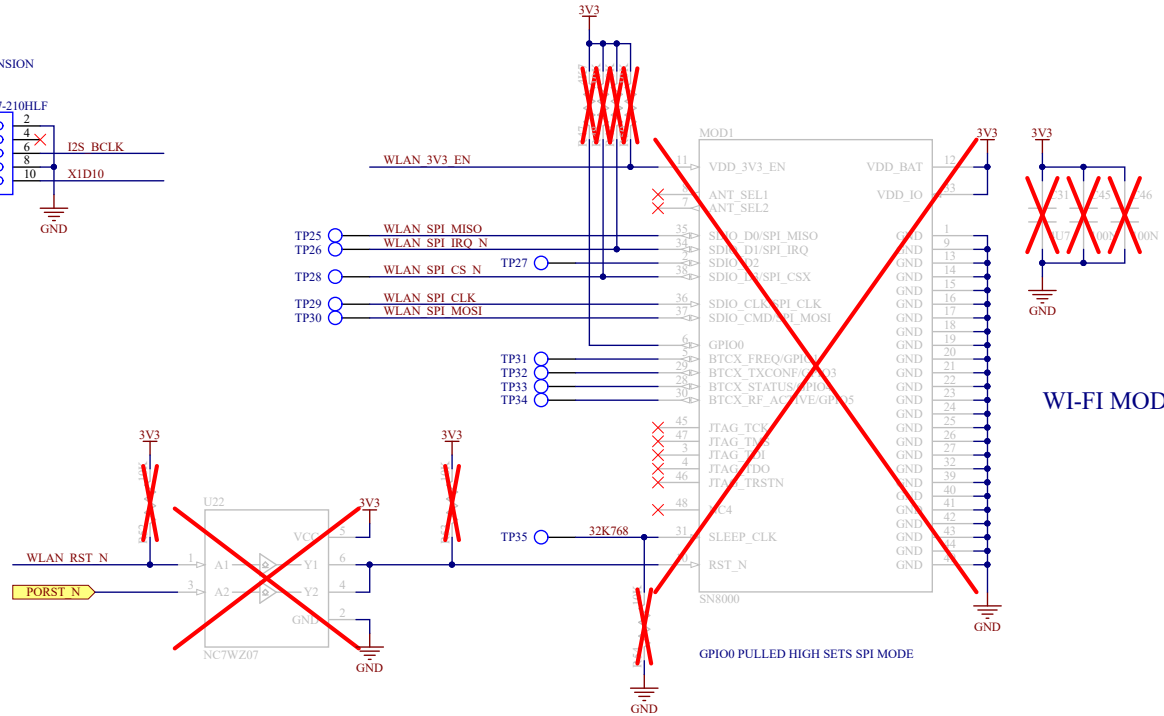
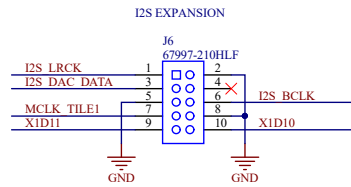


Project Name
Mic Array Motherboard - Dual Tile

Build B	Sheet Name LEDs.SchDoc	Rev 1V1
Date 05/09/2017	Sheet 3 of 5	

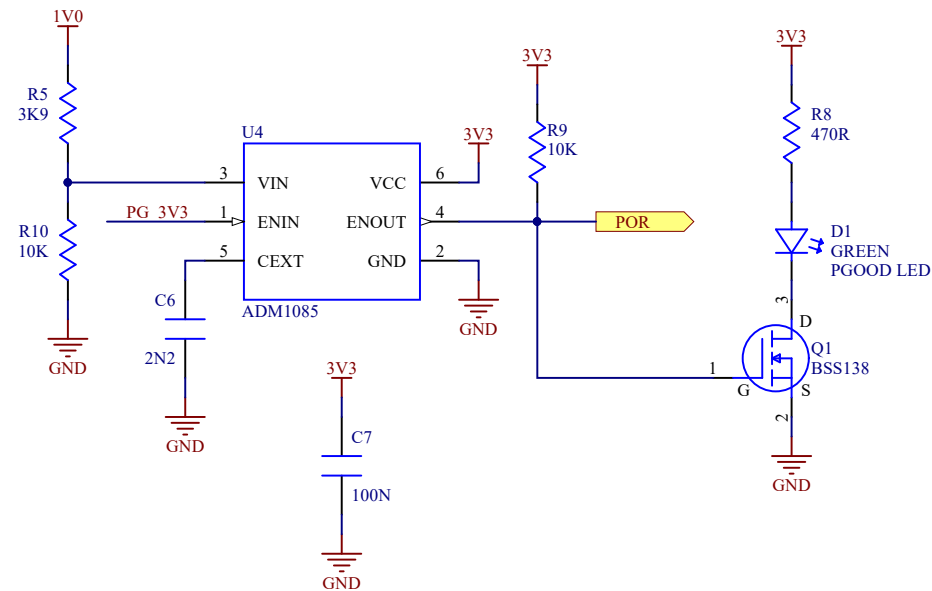
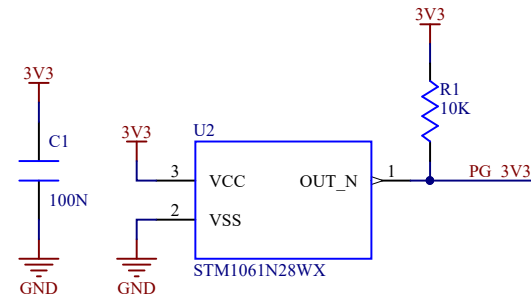
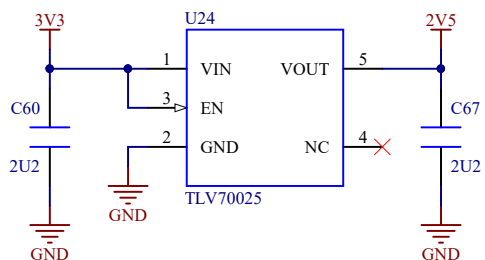
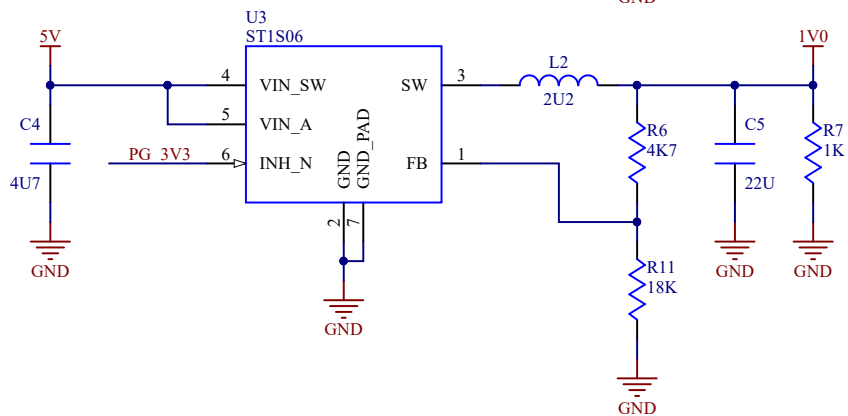
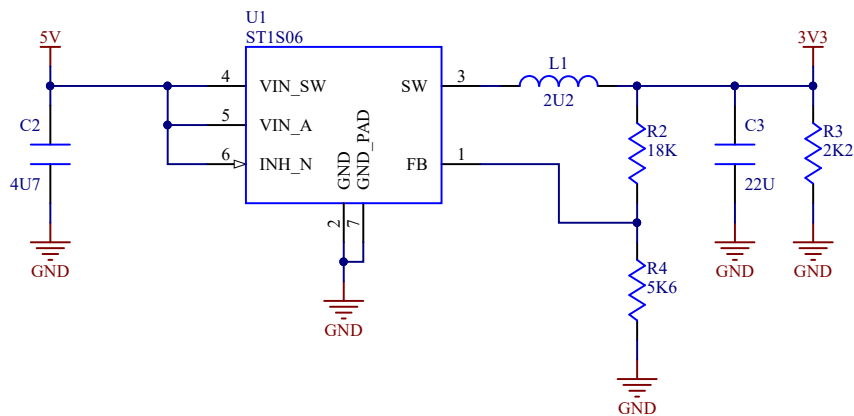


XVF3000-TQ128-C



X MOS

Project Name		
Mic Array Motherboard - Dual Tile		
Build B	Sheet Name Tile 1.SchDoc	Rev 1V1
Date 05/09/2017	Sheet 4 of 5	



XMOS

Project Name
Mic Array Motherboard - Dual Tile

Build B	Sheet Name Power.SchDoc	Rev 1V1
Date 05/09/2017	Sheet 5 of 5	